

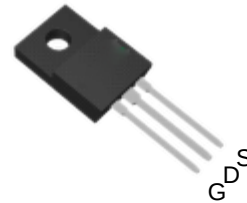
Features

- 100V/70A,
 $R_{DS(ON)} = 16m\Omega$ (typ.) @ $V_{GS}=10V$
- Reliable and Rugged
- Lead Free and Green Devices Available
(RoHS Compliant)
- 100% UIS + R_g Tested

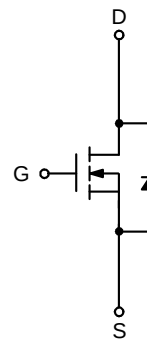
Applications

- High Efficiency Synchronous Rectification in SMPS.
- Uninterruptible Power Supply.
- Hard Switched and High Frequency Circuits.

Pin Description



Top View of TO-220FP



N-Channel MOSFET

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter		Rating	Unit
Common Ratings				
V _{DSS}	Drain-Source Voltage		100	V
V _{GSS}	Gate-Source Voltage		±20	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	
I _S	Diode Continuous Forward Current	T _C =25°C	70	A
I _D	Continuous Drain Current	T _C =25°C	70	A
		T _C =100°C	28	
I _{DM} ^a	Pulsed Drain Current	T _C =25°C	210	
P _D	Maximum Power Dissipation	T _C =25°C	59	W
		T _C =100°C	23	
R _{θJC}	Thermal Resistance-Junction to Case		2.1	°C/W
I _D	Continuous Drain Current	T _A =25°C	7.6	A
		T _A =70°C	6.0	
P _D	Maximum Power Dissipation	T _A =25°C	2	W
		T _A =70°C	1.28	
R _{θJA}	Thermal Resistance-Junction to Ambient		62.5	°C/W
I _{AS} ^b	Avalanche Current, Single pulse	L=0.5mH	26	A
E _{AS} ^b	Avalanche Energy, Single pulse	L=0.5mH	169	mJ

Note a : Pulse width limited by max. junction temperature.

Note b : UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature $T_J = 25^\circ\text{C}$).

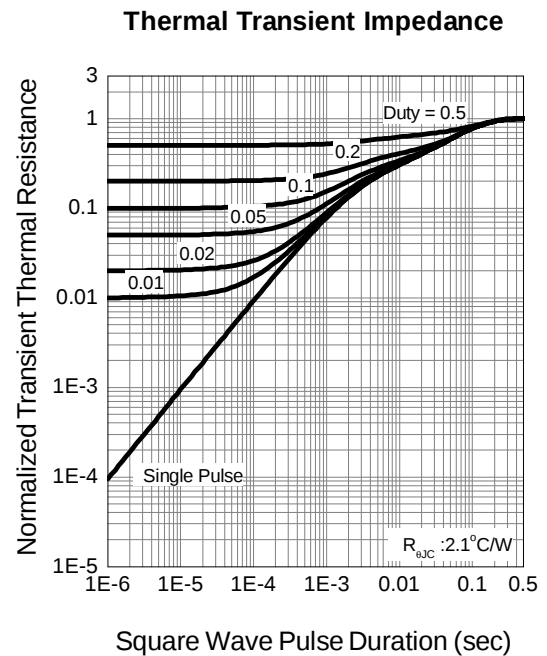
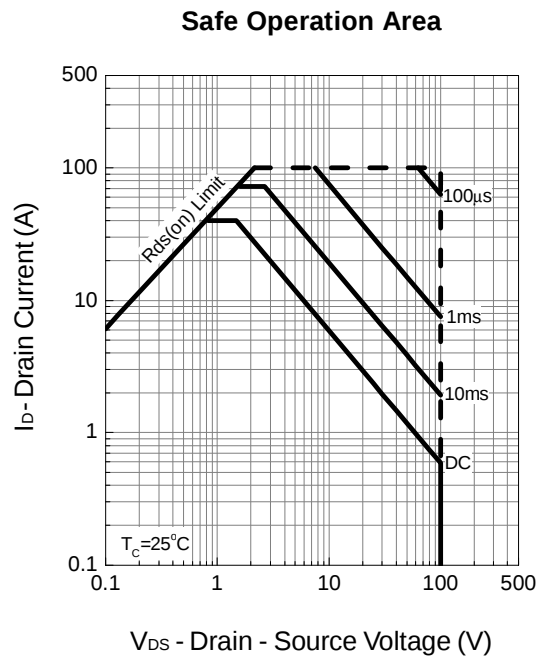
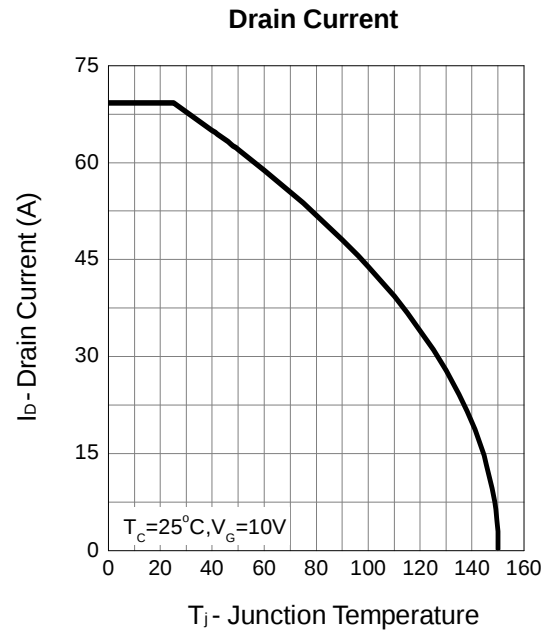
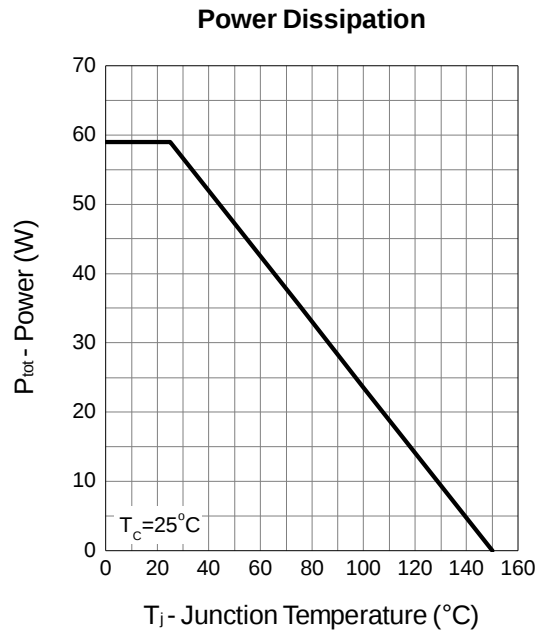
Electrical Characteristics (T_A = 25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	100	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =80V, V _{GS} =0V	-	-	1	μA
		T _j =85°C	-	-	30	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	2	3	4	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} ^c	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =20A	-	16	22	mΩ
Diode Characteristics						
V _{SD} ^c	Diode Forward Voltage	I _{SD} =10A, V _{GS} =0V	-	0.8	1.3	V
t _{rr}	Reverse Recovery Time	I _{SD} =10A, dI _{SD} /dt=100A/μs	-	44	-	ns
Q _{rr}	Reverse Recovery Charge		-	95	-	nC
Dynamic Characteristics ^d						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz	-	1.0	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =30V, Frequency=1.0MHz	-	2100	2730	pF
C _{oss}	Output Capacitance		-	255	-	
C _{rss}	Reverse Transfer Capacitance		-	100	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =30V, R _L =30Ω, I _{DS} =1A, V _{GEN} =10V, R _G =6Ω	-	19	35	ns
t _r	Turn-on Rise Time		-	9	17	
t _{d(OFF)}	Turn-off Delay Time		-	36	65	
t _f	Turn-off Fall Time		-	22	40	
Gate Charge Characteristics ^d						
Q _g	Total Gate Charge	V _{DS} =50V, V _{GS} =10V, I _{DS} =20A	-	42	59	nC
Q _{gs}	Gate-Source Charge		-	12	-	
Q _{gd}	Gate-Drain Charge		-	12	-	

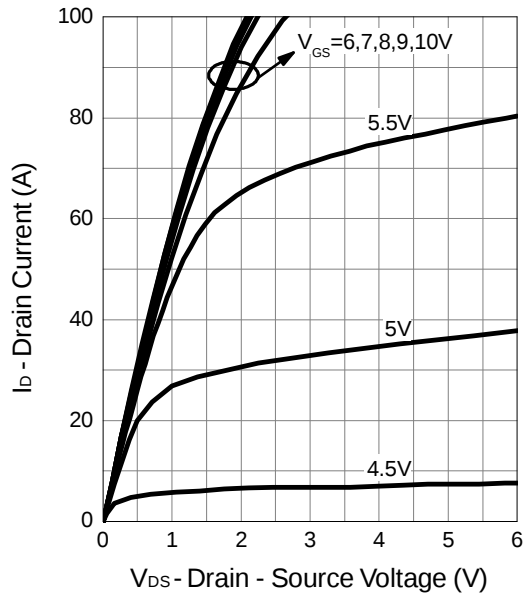
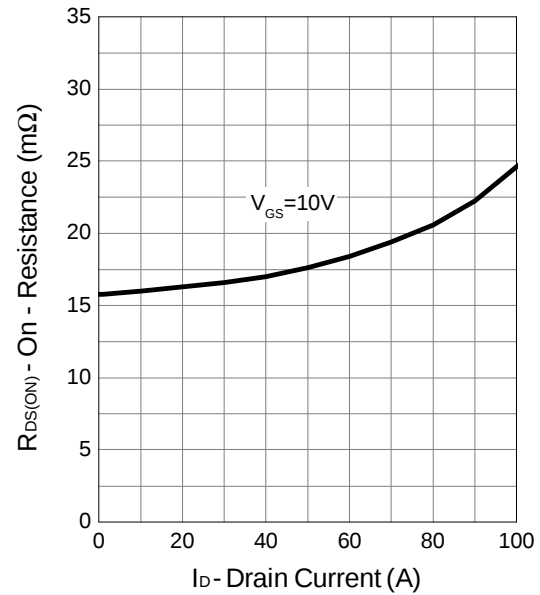
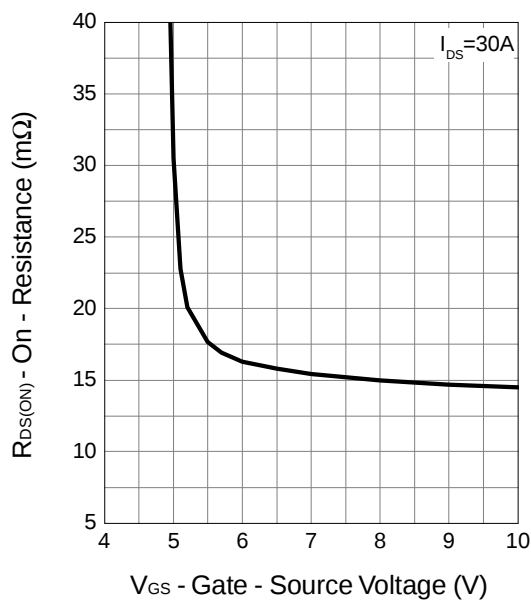
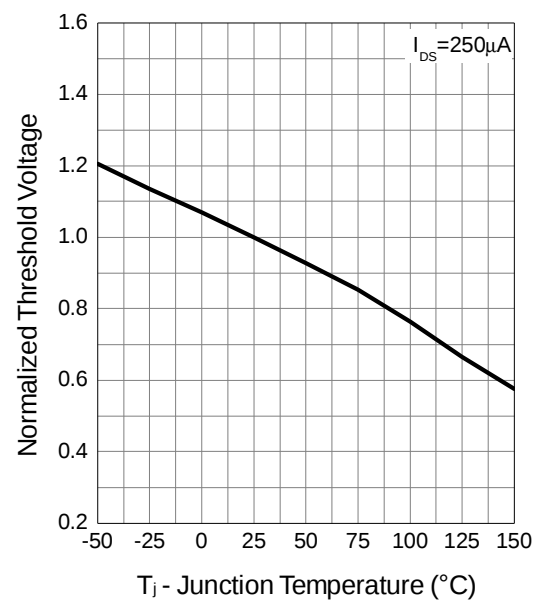
Note c : Pulse test ; pulse width≤300μs, duty cycle≤2%.

Note d : Guaranteed by design, not subject to production testing.

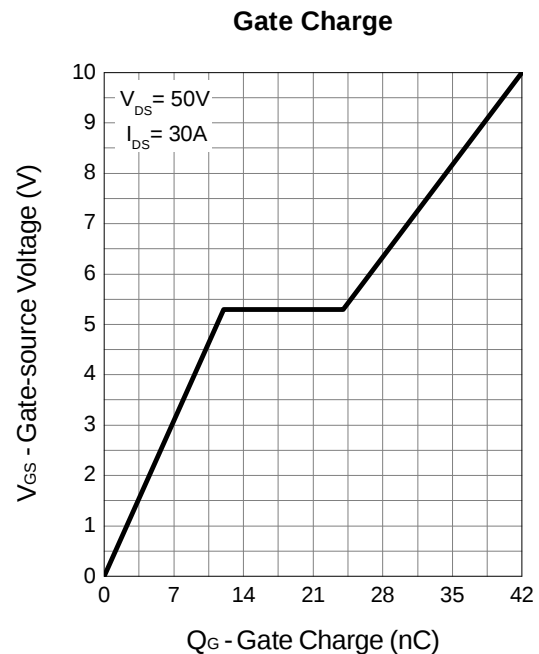
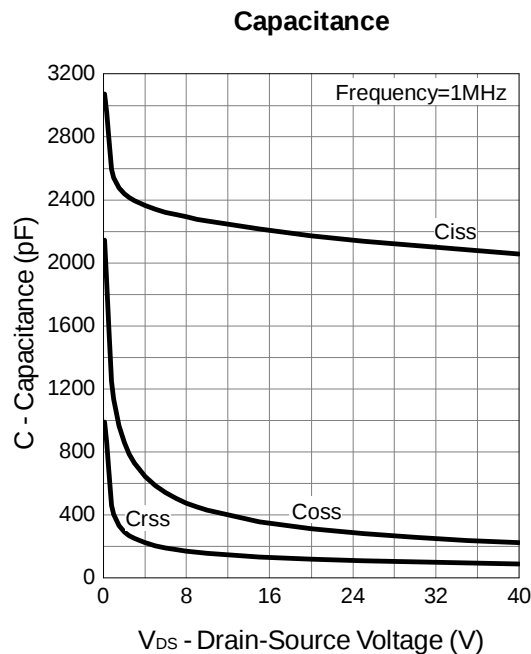
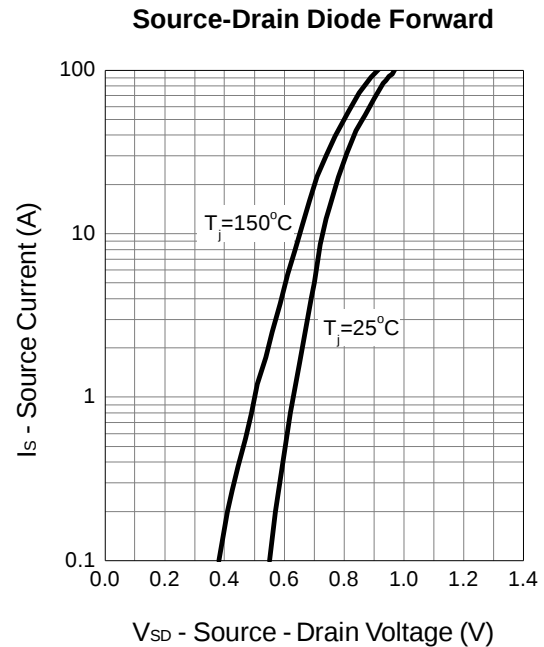
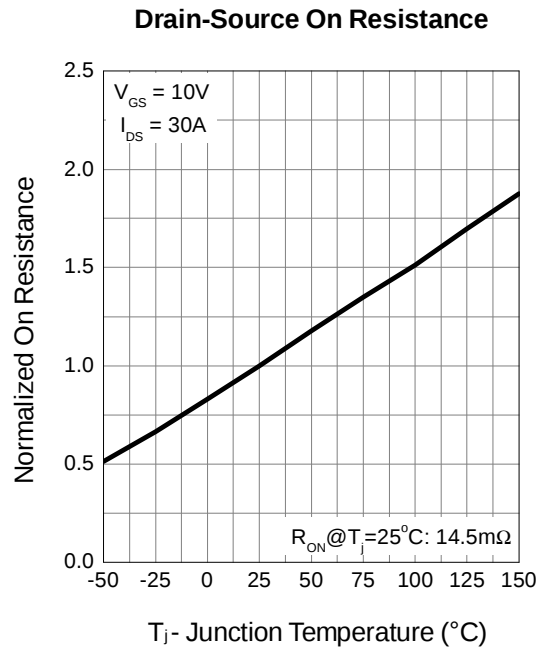
Typical Operating Characteristics



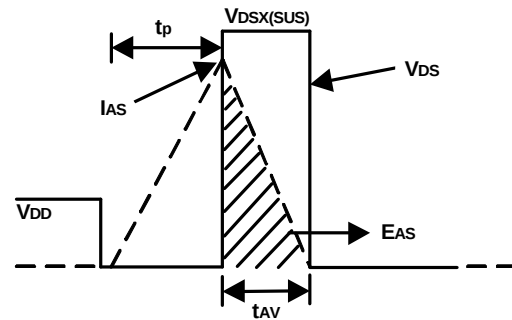
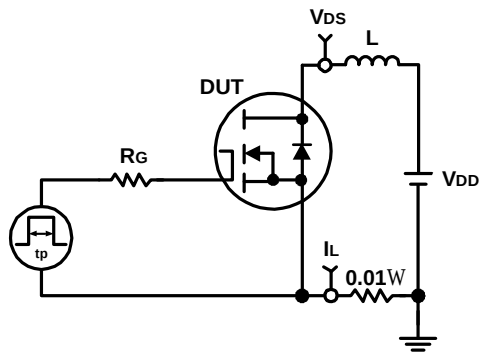
Typical Operating Characteristics (Cont.)

Output Characteristics

Drain-Source On Resistance

Gate-Source On Resistance

Gate Threshold Voltage


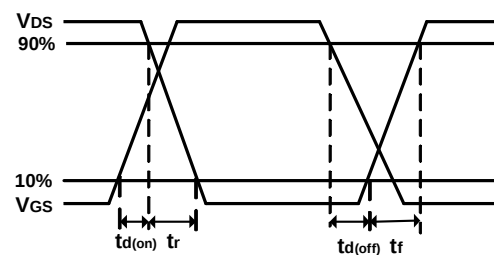
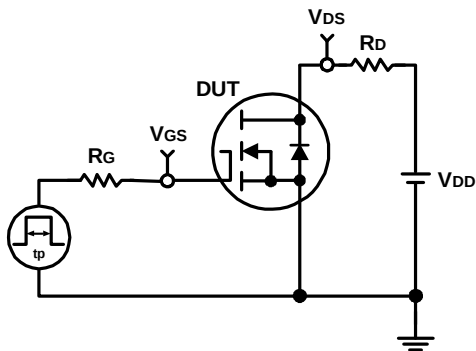
Typical Operating Characteristics (Cont.)



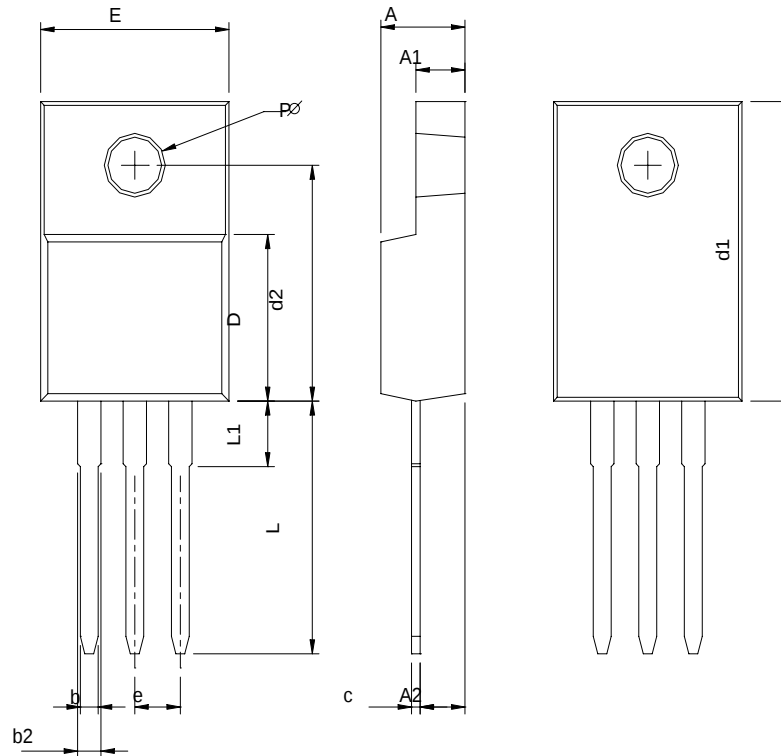
Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



Package Information



SYMBOL	TO-220FP			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	4.20	4.80	0.165	0.189
A1	2.34	3.20	0.092	0.126
A2	2.10	2.90	0.083	0.114
b	0.50	0.90	0.020	0.035
b2	0.91	1.90	0.035	0.075
c	0.30	0.80	0.012	0.031
D	8.10	9.40	0.319	0.370
d1	14.50	16.50	0.571	0.650
d2	12.10	12.90	0.476	0.508
E	9.70	10.70	0.382	0.421
e	2.54 BSC		0.100 BSC	
L	13.00	14.50	0.512	0.570
L1	1.60	4.00	0.063	0.157
P	3.00	3.60	0.118	0.142

RECOMMENDED LAND PATTERN

