

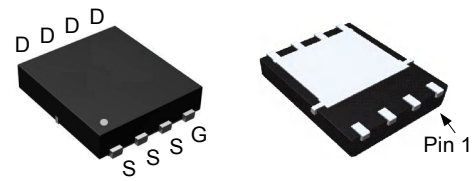
Features

- 100V/40A
 $R_{DS(ON)} = 25m\Omega(max.) @ V_{GS} = 10V$
 $R_{DS(ON)} = 28m\Omega(max.) @ V_{GS} = 4.5V$
- 100% UIS + R_g Tested
- ESD protection
- Reliable and Rugged
- Lead Free and Green Devices Available
(RoHS Compliant)

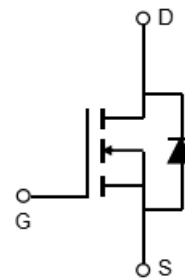
Applications

- Power Management for SMPS.
- DC-DC Converter.

Pin Description



DFN5x6_8L_EP1_P



N-Channel MOSFET

Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ Unless Otherwise Noted)

Symbol	Parameter		Rating	Unit
Common Ratings				
V _{DSS}	Drain-Source Voltage		100	V
V _{GSS}	Gate-Source Voltage		±20	
T _J	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-55 to 150	
I _S	Diode Continuous Forward Current	T _C =25°C	17	A
I _D	Continuous Drain Current	T _C =25°C	40	A
		T _C =100°C	21	
I _{DM} ^a	Pulsed Drain Current	T _C =25°C	135	
P _D	Maximum Power Dissipation	T _C =25°C	52	W
		T _C =100°C	21	
R _{θJC}	Thermal Resistance-Junction to Case		2.4	°C/W
I _D	Continuous Drain Current	T _A =25°C	7.4	A
		T _A =70°C	5.9	
P _D	Maximum Power Dissipation	T _A =25°C	2.5	W
		T _A =70°C	1.6	
R _{θJA}	Thermal Resistance-Junction to Ambient		50	°C/W
I _{AS} ^b	Avalanche Current, Single pulse	L=0.5mH	12	A
E _{AS} ^b	Avalanche Energy, Single pulse	L=0.5mH	36	mJ

Note a : Pulse width limited by maximum junction temperature.

Note b : UIS tested and pulse width limited by maximum junction temperature (initial temperature $T_J=25^{\circ}\text{C}$).

Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

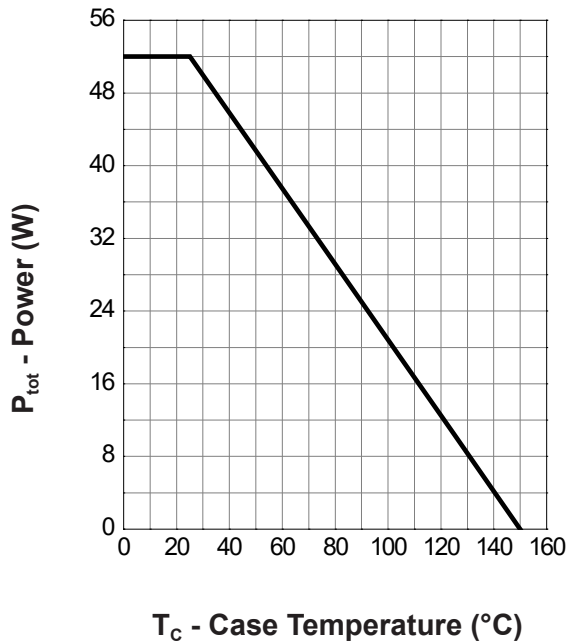
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	100	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =80V, V _{GS} =0V	-	-	1	μA
		T _J =85°C	-	-	30	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	1.2	-	2.2	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±10	μA
R _{DS(ON)} ^c	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =17A	-	19	25	mΩ
		V _{GS} =4.5V, I _{DS} =11A	-	25	28	mΩ
Diode Characteristics						
V _{SD} ^c	Diode Forward Voltage	I _{SD} =8A, V _{GS} =0V	-	0.8	1.3	V
t _{rr} ^d	Reverse Recovery Time	I _{SD} =17A, dI _{SD} /dt=100A/μs	-	33	-	ns
Q _{rr} ^d	Reverse Recovery Charge		-	48	-	nC
Dynamic Characteristics ^d						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	-	1.5	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =50V, Frequency=1.0MHz	-	725	943	pF
C _{oss}	Output Capacitance		-	131	-	
C _{rss}	Reverse Transfer Capacitance		-	18	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =30V, R _L =30Ω, I _{DS} =1A, V _{GEN} =10V, R _G =6Ω	-	12	22	ns
t _r	Turn-on Rise Time		-	6	11	
t _{d(OFF)}	Turn-off Delay Time		-	23	41	
t _f	Turn-off Fall Time		-	23	41	
Gate Charge Characteristics ^d						
Q _g	Total Gate Charge	V _{DS} =50V, V _{GS} =10V, I _{DS} =17A	-	14	19.6	nC
Q _g	Total Gate Charge	V _{DS} =50V, V _{GS} =4.5V, I _{DS} =17A	-	6.9	-	
Q _{gs}	Gate-Source Charge		-	2.5	-	
Q _{gd}	Gate-Drain Charge		-	3.4	-	

Note c : Pulse test ; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

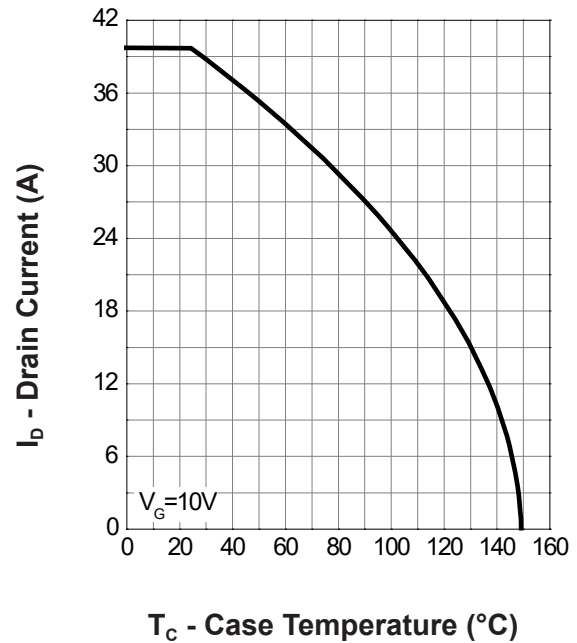
Note d : Guaranteed by design, not subject to production testing.

Typical Operating Characteristics

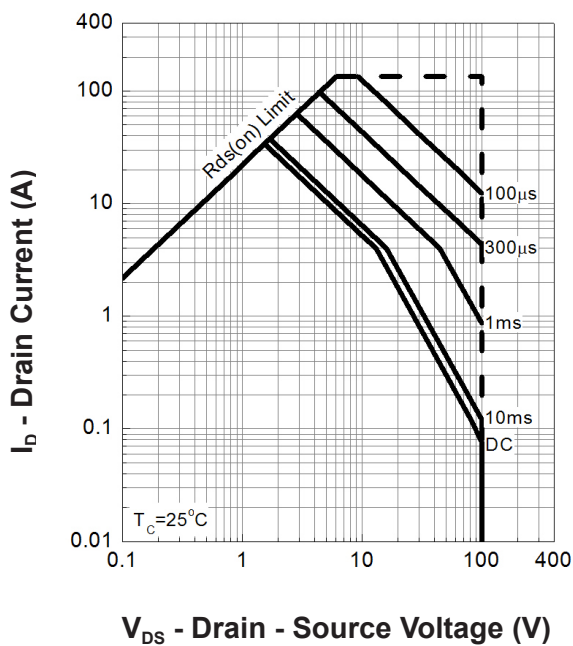
Power Dissipation



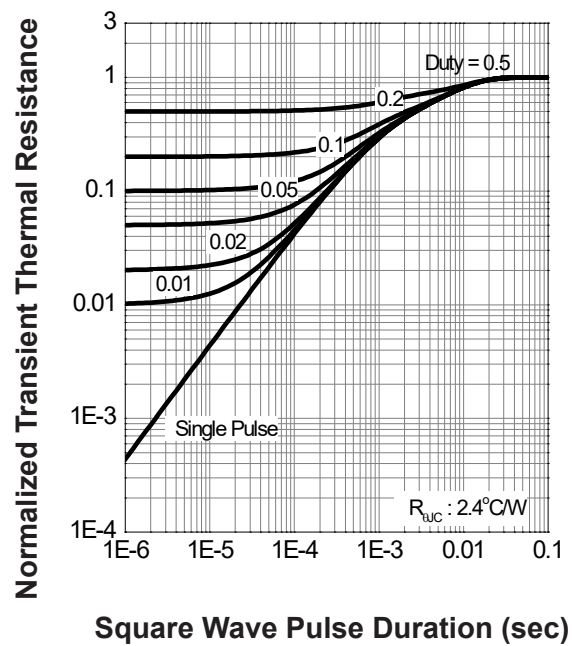
Drain Current



Safe Operation Area

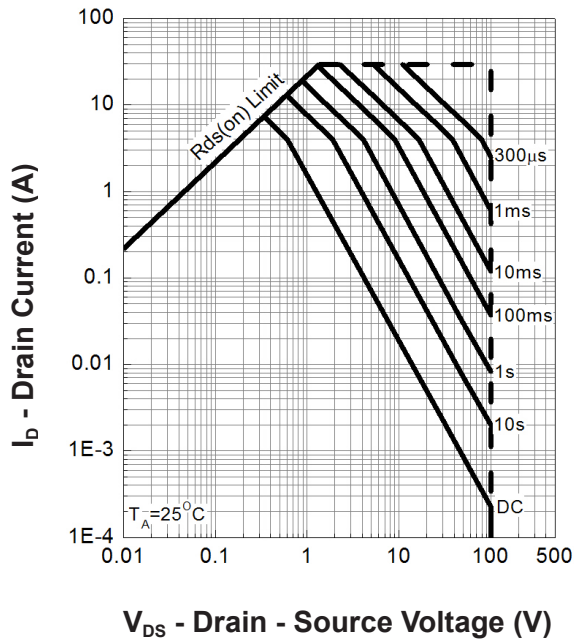


Thermal Transient Impedance

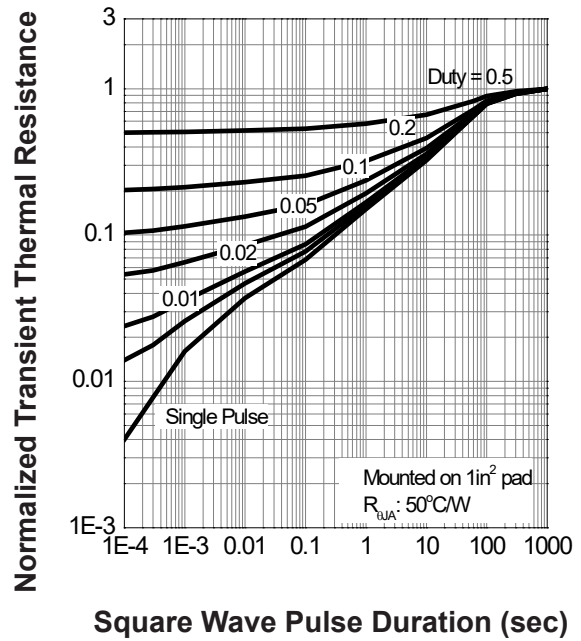


Typical Operating Characteristics(Cont.)

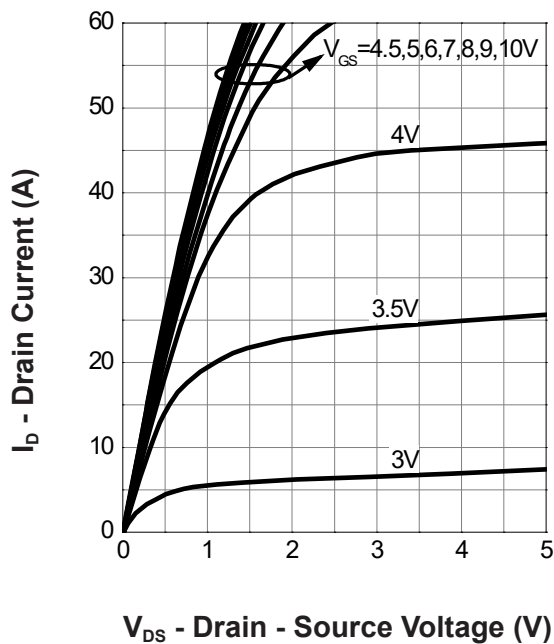
Safe Operation Area



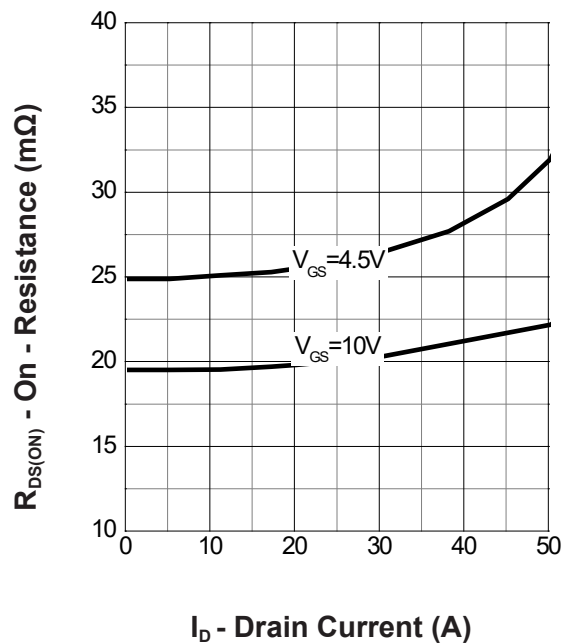
Thermal Transient Impedance



Output Characteristics

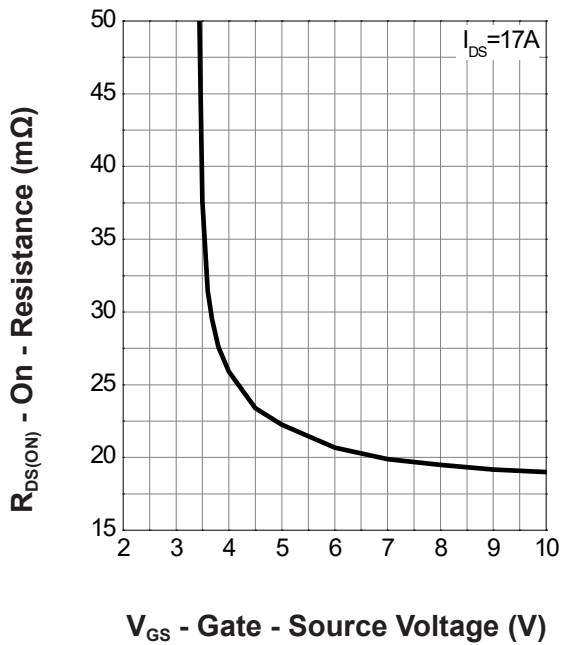


Drain-Source On Resistance

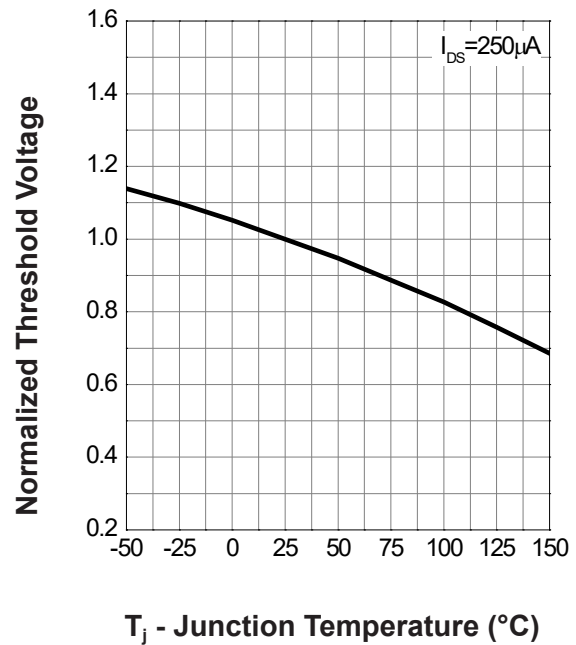


Typical Operating Characteristics(Cont.)

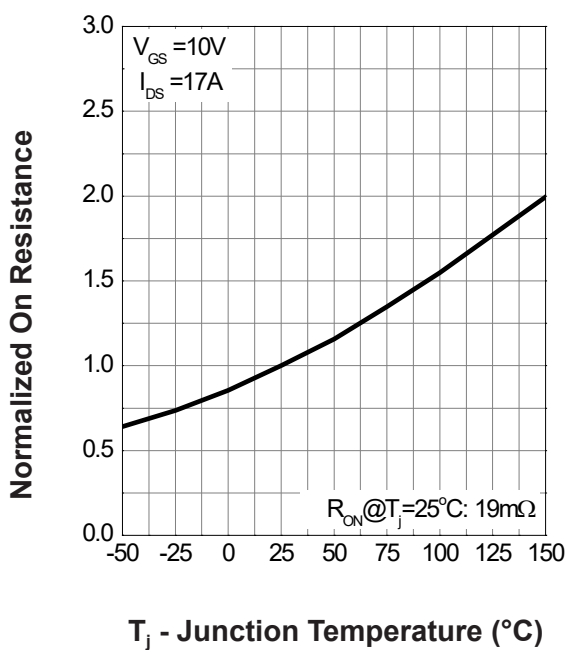
Gate-Source On Resistance



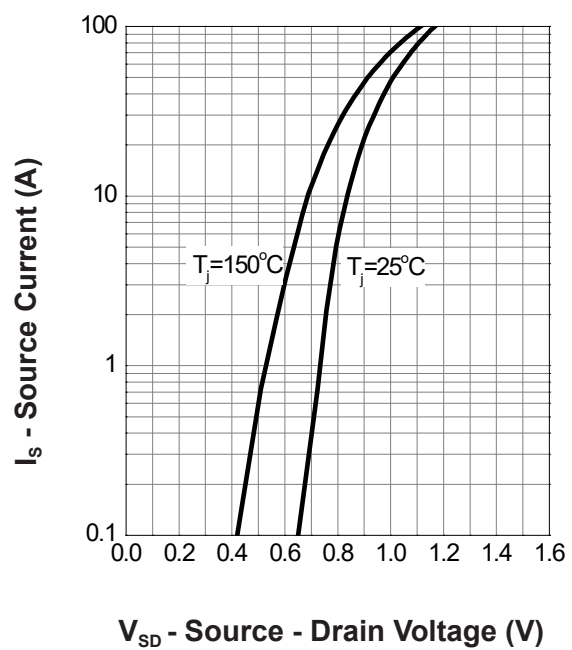
Gate Threshold Voltage



Drain-Source On Resistance

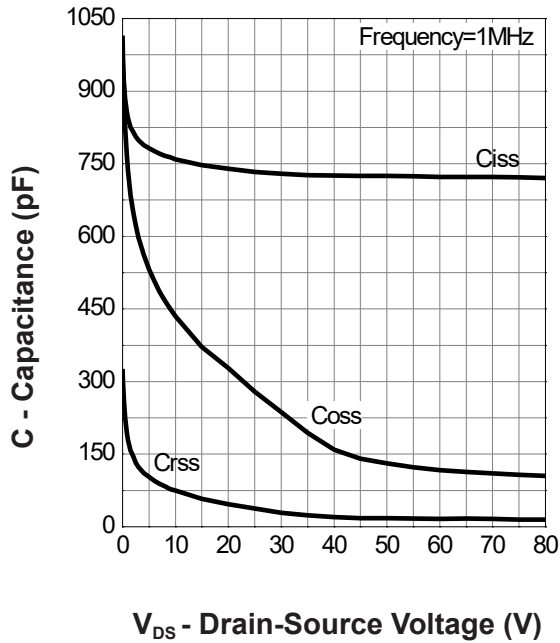


Source-Drain Diode Forward

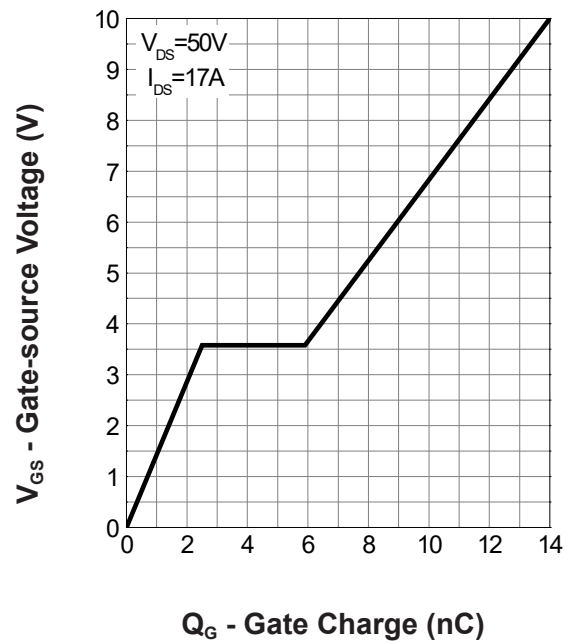


Typical Operating Characteristics(Cont.)

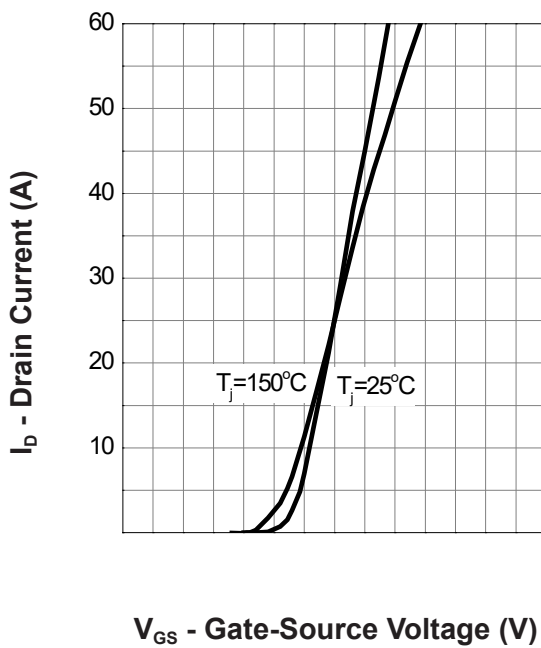
Capacitance



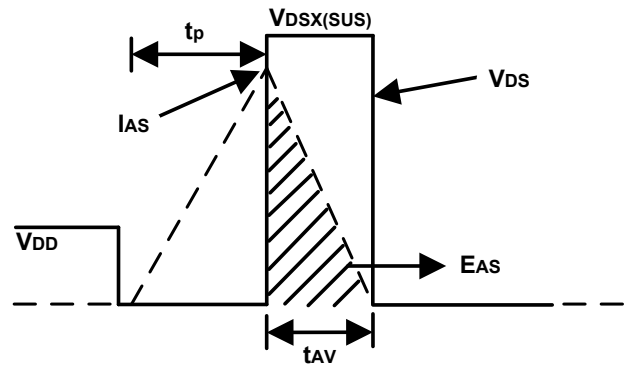
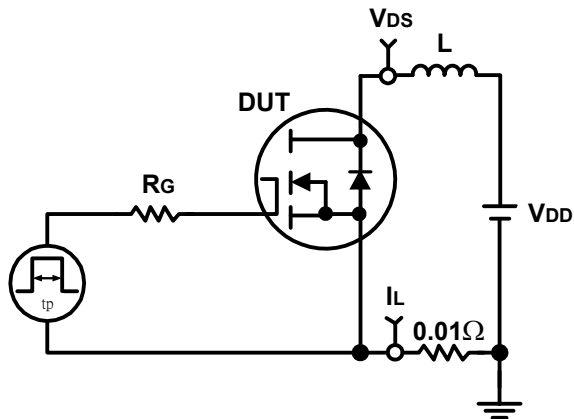
Gate Charge



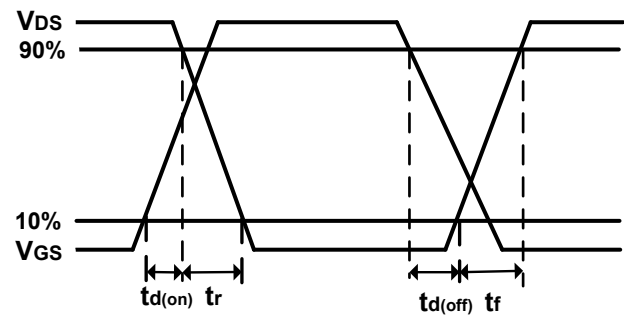
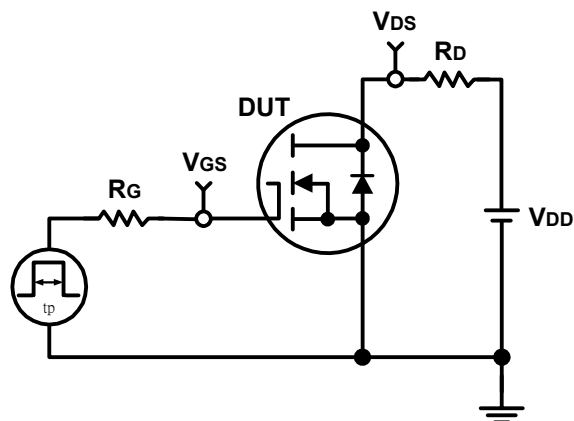
Transfer Characteristics



Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



Package Information

DFN5X6 OUTLINE

