

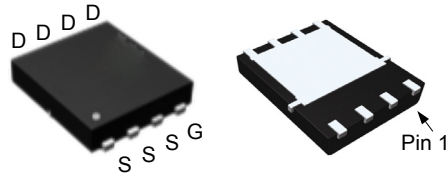
Features

- 30V/90A,
 $R_{DS(ON)} = 3.5m\Omega$ (typ.) @ $V_{GS} = 10V$
 $R_{DS(ON)} = 5.5m\Omega$ (typ.) @ $V_{GS} = 4.5V$
- 100% UIS + R_g Tested
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)
- Moisture Sensitivity Level MSL1 (per JEDEC J-STD-020D)

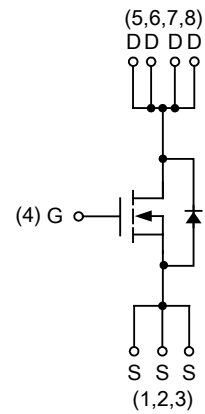
Applications

- Power Management in Desktop Computer or DC/DC Converters.

Pin Description



DFN5x6A-8_EP



N-Channel MOSFET

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit
Common Ratings			
V_{DSS}	Drain-Source Voltage	30	V
V_{GSS}	Gate-Source Voltage	± 20	
T_J	Maximum Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
I_S	Diode Continuous Forward Current	$T_C=25^\circ\text{C}$	A
I_D	Continuous Drain Current	$T_C=25^\circ\text{C}$	A
		$T_C=100^\circ\text{C}$	
I_{DM}^b	Pulse Drain Current	$T_C=25^\circ\text{C}$	A
P_D	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	W
		$T_C=100^\circ\text{C}$	
$R_{\theta JC}$	Thermal Resistance-Junction to Case	Steady State	$^\circ\text{C/W}$
I_D	Continuous Drain Current	$T_A=25^\circ\text{C}$	A
		$T_A=70^\circ\text{C}$	
I_{DM}^b	Pulse Drain Current	$T_A=25^\circ\text{C}$	A
P_D	Maximum Power Dissipation	$T_A=25^\circ\text{C}$	W
		$T_A=70^\circ\text{C}$	
$R_{\theta JA}^c$	Thermal Resistance-Junction to Ambient	$t \leq 10\text{s}$	$^\circ\text{C/W}$
		Steady State	
I_{AS}^d	Avalanche Current, Single pulse	$L=0.5\text{mH}$	A
E_{AS}^d	Avalanche Energy, Single pulse	$L=0.5\text{mH}$	mJ

Note a : Max. continuous current is limited by bonding wire.

Note b : Pulse width is limited by max. junction temperature.

Note c : Surface mounted on 1in^2 pad area, steady state $t = 999\text{s}$.

Note d : UIS tested and pulse width are limited by maximum junction temperature 150°C (initial temperature $T_J=25^\circ\text{C}$).

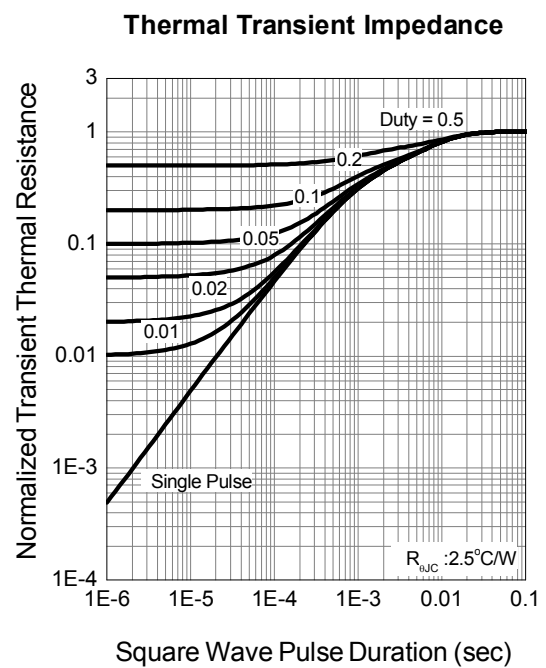
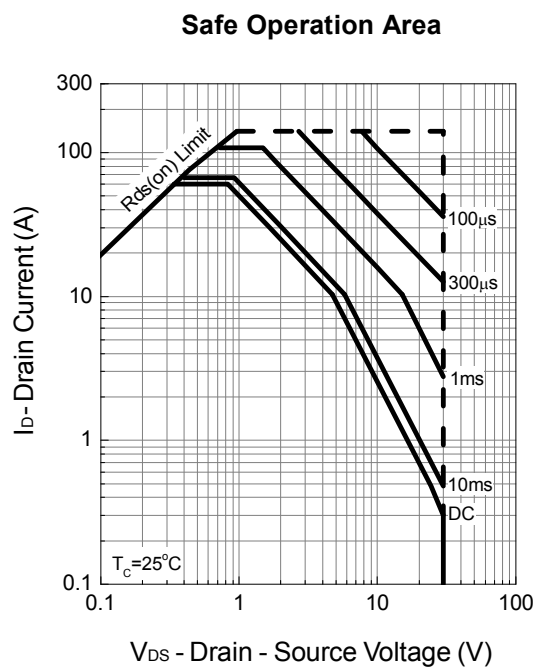
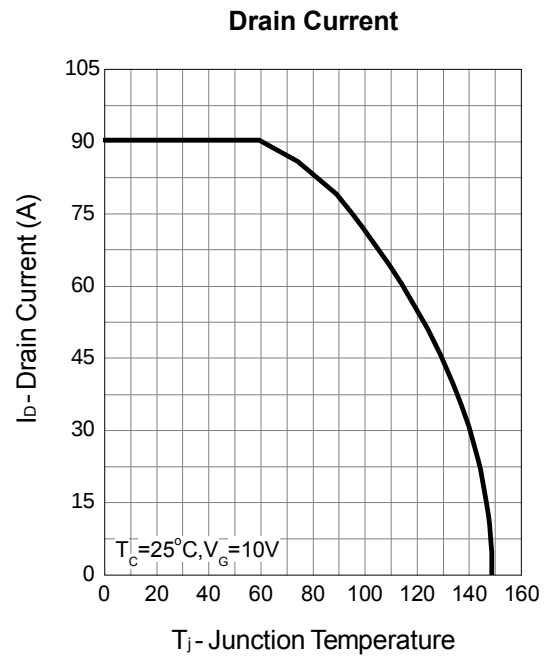
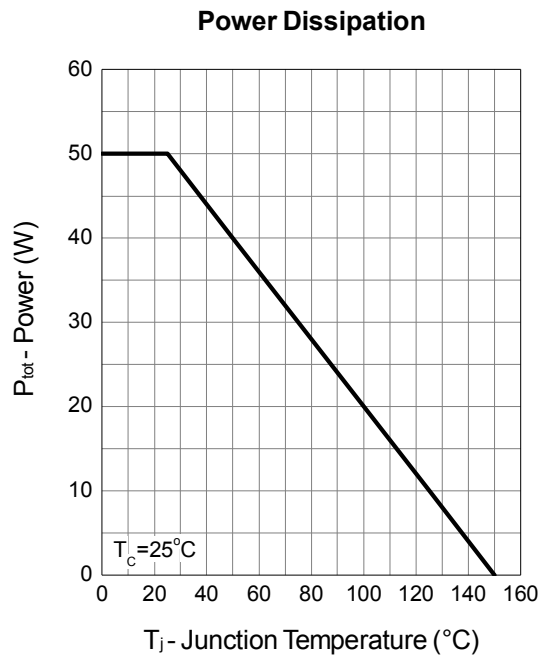
Electrical Characteristics (T_A = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	30	-	-	V
BV _{DSS} t	Drain-Source Breakdown Voltage (transient)	V _{GS} =0V, I _{D(aval)} =20A T _{case} =25°C, t _{transient} =100ns	34	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =24V, V _{GS} =0V T _J =85°C	- -	- -	1 30	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	1.0	1.5	2.5	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)} ^e	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =40A T _J =125°C	- -	4 5.9	5.5 -	mΩ
		V _{GS} =4.5V, I _{DS} =20A	-	5.5	7	
Gfs	Forward Transconductance	V _{DS} =5V, I _{DS} =40A	-	95	-	S
Diode Characteristics						
V _{SD} ^e	Diode Forward Voltage	I _{SD} =20A, V _{GS} =0V	-	0.8	1.1	V
t _{rr}	Reverse Recovery Time	I _{DS} =40A, dI _{SD} /dt=100A/μs	-	21	-	ns
t _a	Charge Time		-	13.2	-	
t _b	Discharge Time		-	7.8	-	
Q _{rr}	Reverse Recovery Charge		-	17	-	nC
Dynamic Characteristics ^f						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	-	3	4.5	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =15V, Frequency=1.0MHz	1200	1500	1820	pF
C _{oss}	Output Capacitance		210	260	310	
C _{rss}	Reverse Transfer Capacitance		100	130	175	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =15V, R _L =15Ω, I _{DS} =1A, V _{GEN} =10V, R _G =6Ω	-	15	28	ns
t _r	Turn-on Rise Time		-	13	24	
t _{d(OFF)}	Turn-off Delay Time		-	32	57	
t _f	Turn-off Fall Time		-	9	17	
Gate Charge Characteristics ^f						
Q _g	Total Gate Charge	V _{DS} =15V, V _{GS} =4.5V, I _{DS} =40A	-	12	17	nC
Q _g	Total Gate Charge	V _{DS} =15V, V _{GS} =10V, I _{DS} =40A	-	25	37	
Q _{gth}	Threshold Gate Charge		-	1.5	-	
Q _{gs}	Gate-Source Charge		-	3	-	
Q _{gd}	Gate-Drain Charge		-	7.5	-	

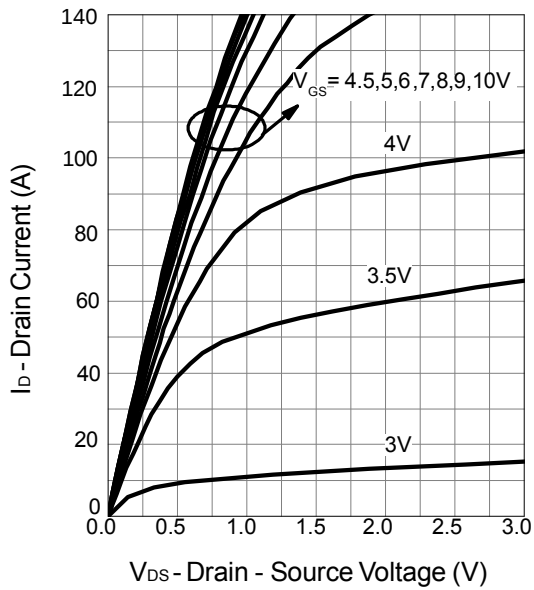
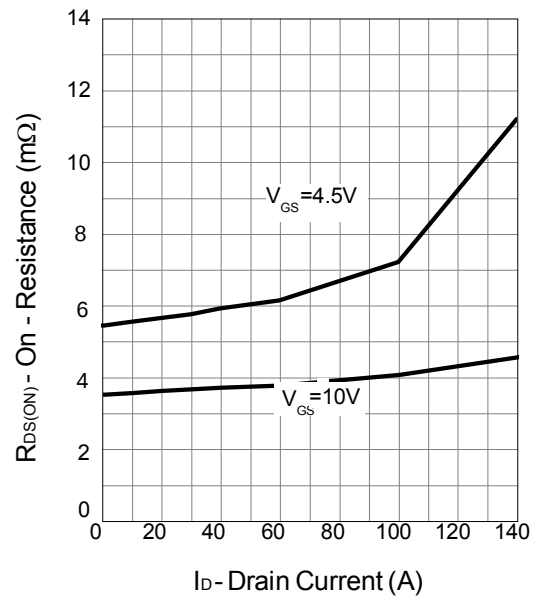
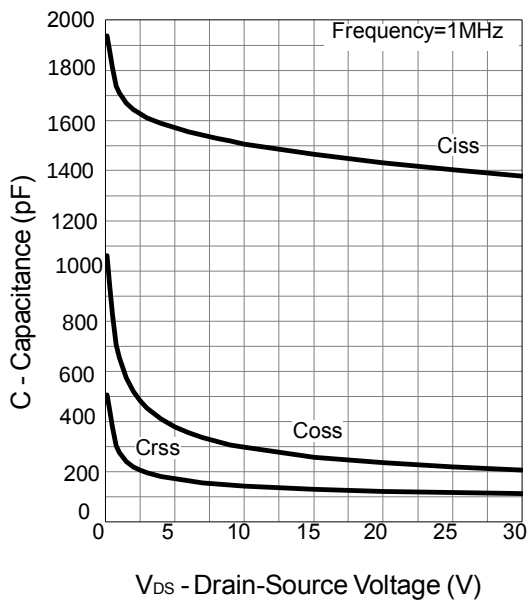
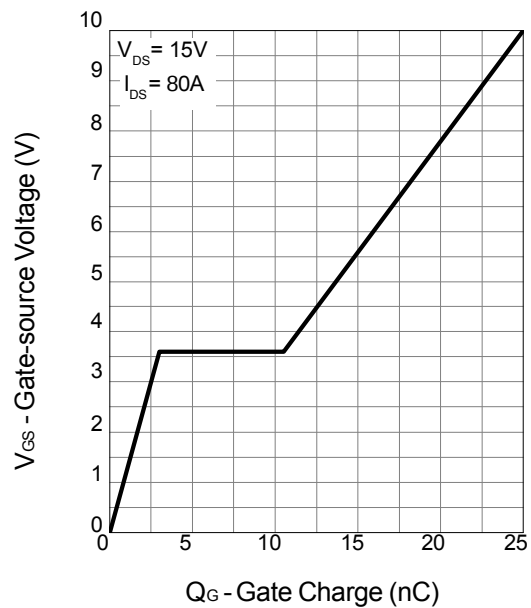
Note e : Pulse test ; pulse width≤300μs, duty cycle≤2%.

Note f : Guaranteed by design, not subject to production testing.

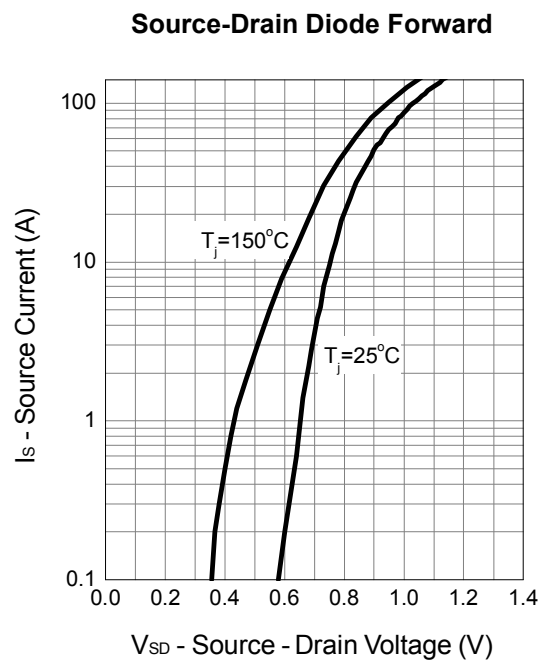
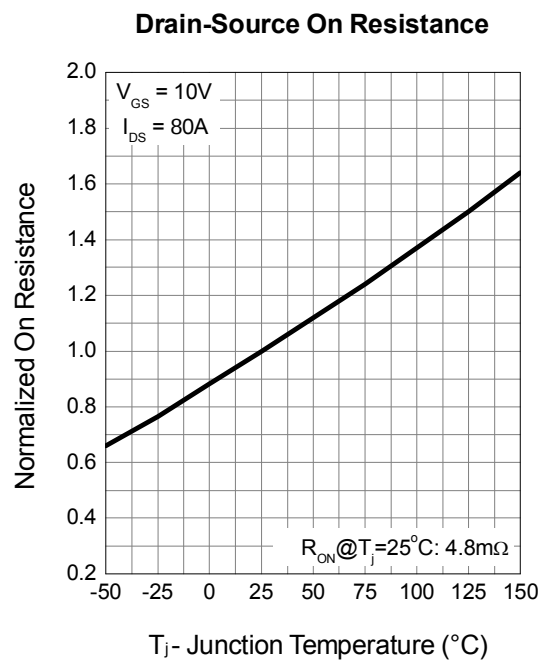
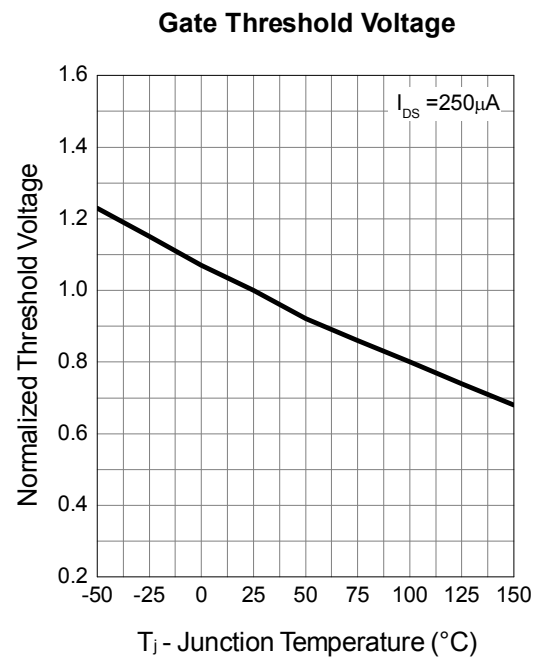
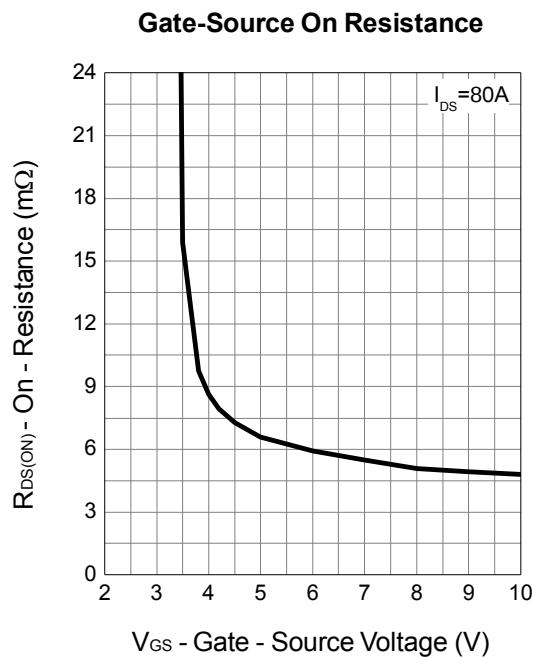
Typical Operating Characteristics



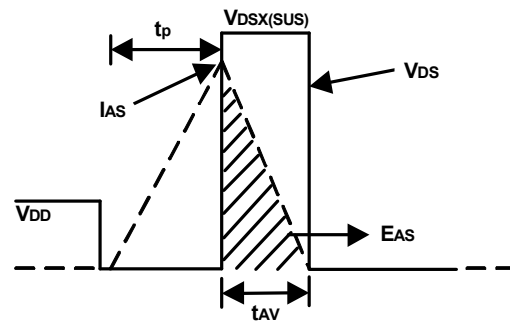
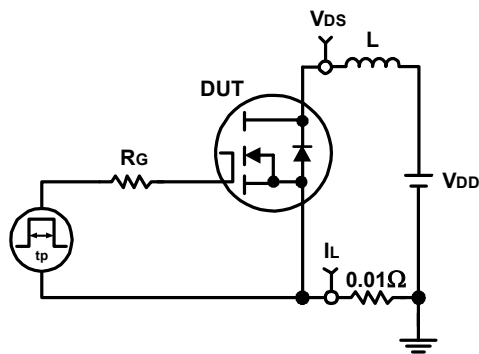
Typical Operating Characteristics (Cont.)

Output Characteristics

Drain-Source On Resistance

Capacitance

Gate Charge


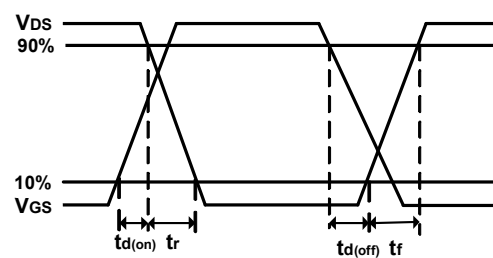
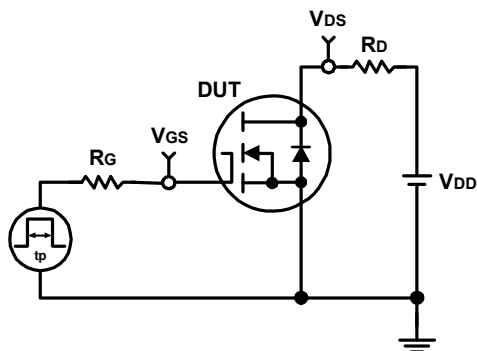
Typical Operating Characteristics (Cont.)



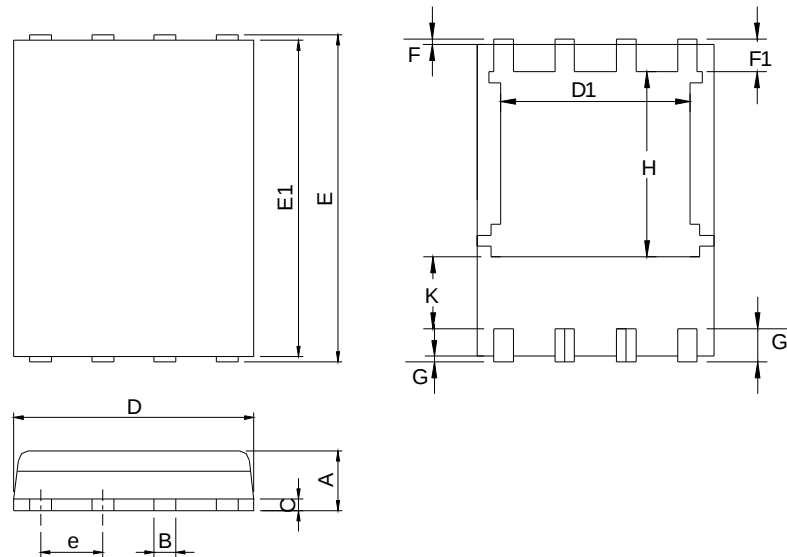
Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms

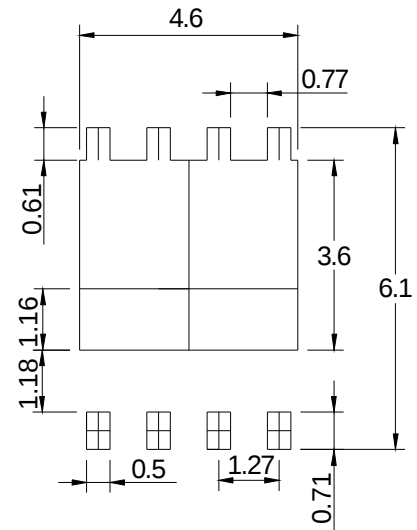


Package Information

DFN5x6-8


SYMBOL	DFN5x6-8			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.90	1.20	0.035	0.047
B	0.3	0.51	0.012	0.020
C	0.19	0.25	0.007	0.010
D	4.80	5.30	0.189	0.209
D1	4.00	4.40	0.157	0.173
E	5.90	6.20	0.232	0.244
E1	5.50	5.80	0.217	0.228
e	1.27 BSC		0.050 BSC	
F	0.05	0.30	0.002	0.012
F1	0.35	0.75	0.014	0.030
G	0.05	0.30	0.002	0.012
G1	0.35	0.75	0.014	0.030
H	3.34	3.9	0.131	0.154
K	0.762	-	0.03	-

RECOMMENDED LAND PATTERN



UNIT: mm

Note : 1.Dimension D, D1,D2 and E1 do not include mold flash or protrusions.
Mold flash or protrusions shall not exceed 10 mil.