Features

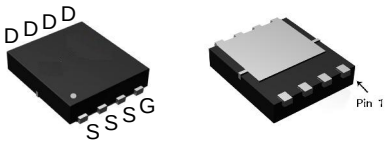
- 30V/100A,
  $R_{DS(ON)}$= 2.9mΩ (typ.) @ V_{GS}=10V
  $R_{DS(ON)}$= 4.8mΩ (typ.) @ V_{GS}=4.5V
- Lower Q_g and Q_{gd} for high-speed switching
- Lower $R_{DS(ON)}$ to Minimize Conduction Losses
- 100% UIS + R_g Tested
- Reliable and Rugged
- Lead Free and Green Devices Available

(RoHS Compliant)

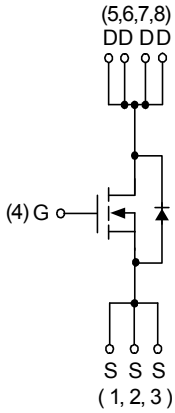
Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.

Pin Description



DFN3.3x3.3_8L_EP1_P



N-Channel MOSFET

Absolute Maximum Ratings (T_A = 25°C Unless Otherwise Noted)

Symbol	Parameter		Rating	Unit
Common Ratings				
V_{DSS}	Drain-Source Voltage		30	V
V_{GSS}	Gate-Source Voltage		±20	
T_J	Maximum Junction Temperature		150	°C
T_{STG}	Storage Temperature Range		-55 to 150	
I_S	Diode Continuous Forward Current	T_C=25°C	100	A
I_D	Continuous Drain Current	T_C=25°C	100	
		T_C=100°C	40	
I_{DM} [a]	Pulsed Drain Current	T_C=25°C	300	A
P_D	Maximum Power Dissipation	T_C=25°C	32.8	W
		T_C=100°C	13.1	
$R_{\theta JC}$	Thermal Resistance-Junction to Case	Steady State	3.8	°C/W
I_D [b]	Continuous Drain Current	T_A=25°C	18.1	A
		T_A=70°C	14.5	
P_D [b]	Maximum Power Dissipation	T_A=25°C	1.78	W
		T_A=70°C	1.14	
$R_{\theta JA}$ [b]	Thermal Resistance-Junction to Ambient	t ≤ 10s	40	°C/W
		Steady State	70	
I_{AS} [c]	Avalanche Current, Single pulse	L=0.1mH	105	A
E_{AS} [c]	Avalanche Energy, Single pulse	L=0.1mH	551.25	mJ

Note a：Pulse width is limited by max. junction temperature.
Note b：Surface Mounted on $1in^2$ pad area, t ≤999sec.
Note c：UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature T_j=25°C).

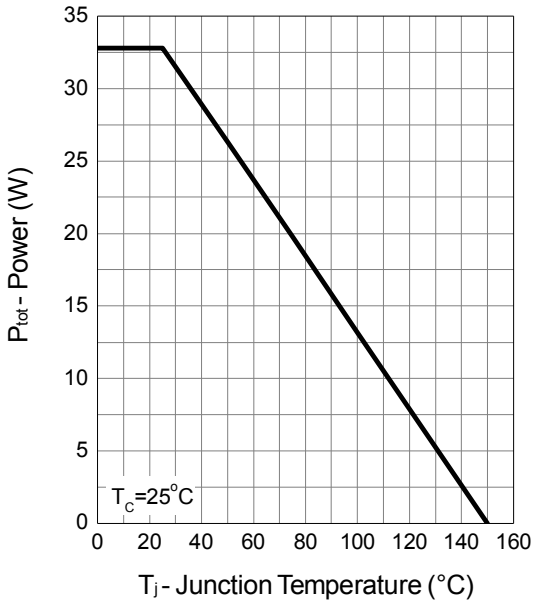
Electrical Characteristics (T_A = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_{DS}=250μA	30	-	-	V
BV_{DSSt}	Drain-Source Breakdown Voltage (transient)	VGS=0V, $I_{D(aval)}$=35A T_{case}=25°C, $t_{transient}$=100ns	34	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=24V, V_{GS}=0V	-	-	1	μA
		T_J=85°C	-	-	30	
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_{DS}=250μA	1.4	1.7	2.5	V
I_{GSS}	Gate Leakage Current	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA
$R_{DS(ON)}$ [d]	Drain-Source On-state Resistance	V_{GS}=10V, I_{DS}=15A	-	2.9	3.4	mΩ
		T_J=125°C	-	4.35	-	
		V_{GS}=4.5V, I_{DS}=10A	-	4.8	6.3	
Gfs	Forward Transconductance	V_{DS}=5V, I_{DS}=10A	-	20	-	S
Diode Characteristics						
V_{SD} [d]	Diode Forward Voltage	I_{SD}=10A, V_{GS}=0V	-	0.77	1.1	V
t_{rr}	Reverse Recovery Time	I_{SD}=5A, dI_{SD}/dt=100A/μs, V_{dd}=15V	-	40	-	ns
t_a	Charge Time		-	20	-	
t_b	Discharge Time		-	20	-	
Q_{rr}	Reverse Recovery Charge		-	33	-	nC
Dynamic Characteristics						
R_G	Gate Resistance	V_{GS}=0V,V_{DS}=0V,F=1MHz	-	0.8	1.8	Ω
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=25V, Frequency=1.0MHz	-	2820	-	pF
C_{oss}	Output Capacitance		-	310	-	
C_{rss}	Reverse Transfer Capacitance		-	270	-	
$t_{d(ON)}$	Turn-on Delay Time	V_{DD}=15V, R_L=15Ω, I_{DS}=1A, V_{GEN}=10V, R_G=1Ω	-	15	-	ns
t_r	Turn-on Rise Time		-	12	-	
$t_{d(OFF)}$	Turn-off Delay Time		-	28	-	
t_f	Turn-off Fall Time		-	32	-	
Gate Charge Characteristics						
Q_g	Total Gate Charge	V_{DS}=15V, V_{GS}=10V, I_{DS}=15A	-	28	37	nC
Q_g	Total Gate Charge	V_{DS}=15V, V_{GS}=4.5V, I_{DS}=15A	-	13.4	-	
Q_{gth}	Threshold Gate Charge		-	2.5	-	
Q_{gs}	Gate-Source Charge		-	4	-	
Q_{gd}	Gate-Drain Charge		-	4.5	-	

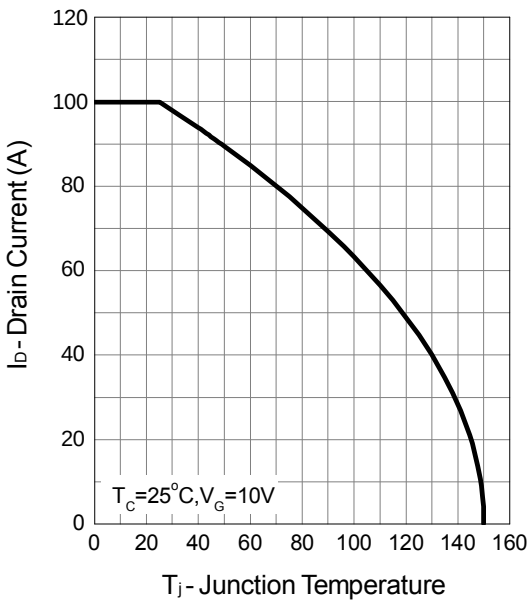
Note d：Pulse test ; pulse width≤300μs, duty cycle≤2%.

Typical Operating Characteristics

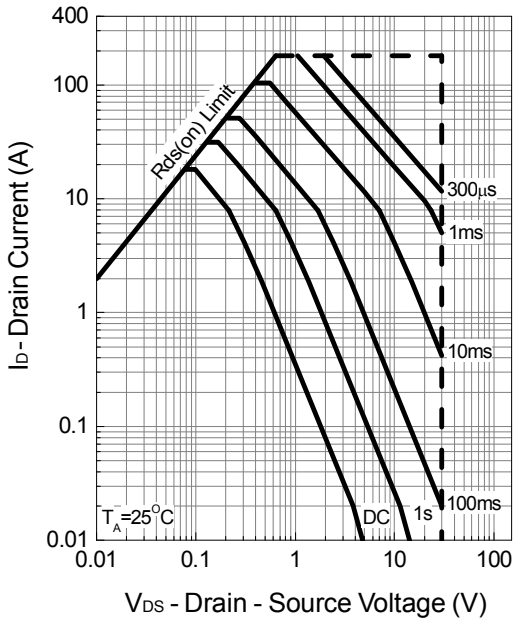
Power Dissipation



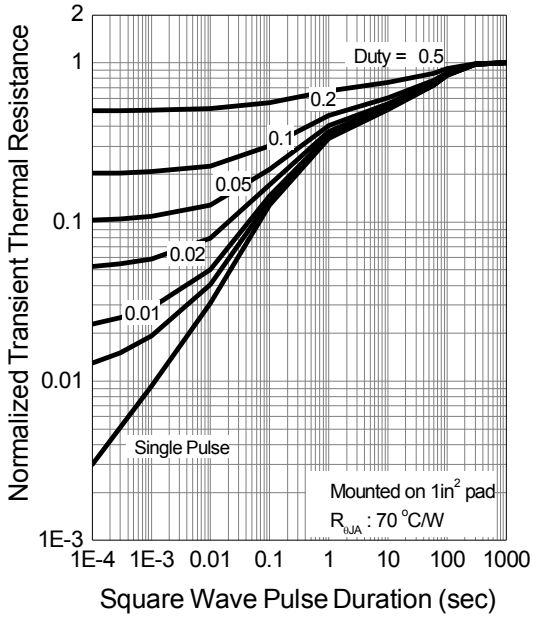
Drain Current



Safe Operation Area

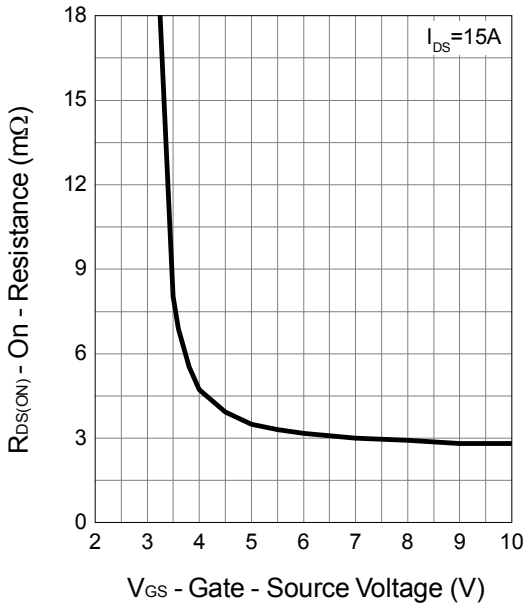


Thermal Transient Impedance

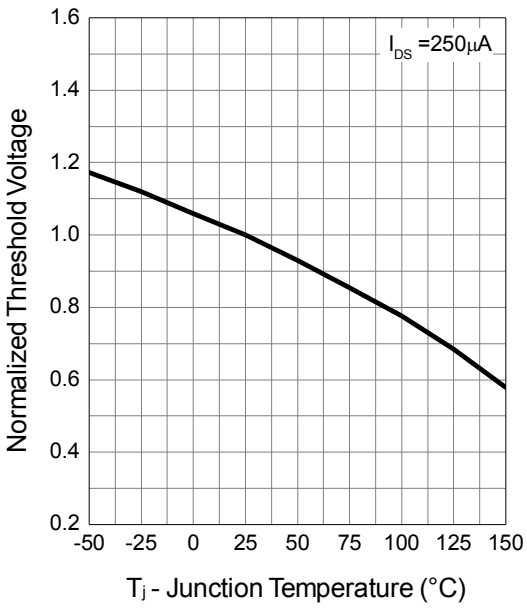


Typical Operating Characteristics (Cont.)

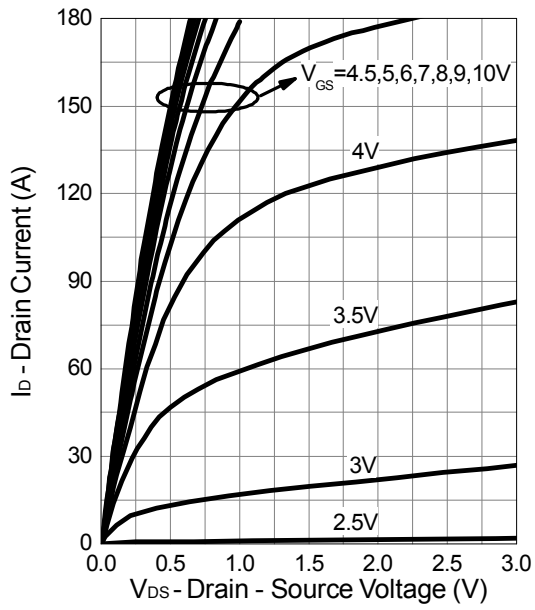
Gate-Source On Resistance



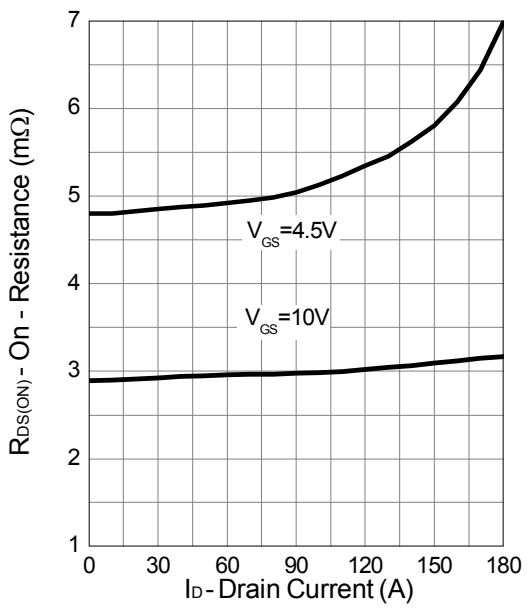
Gate Threshold Voltage



Output Characteristics

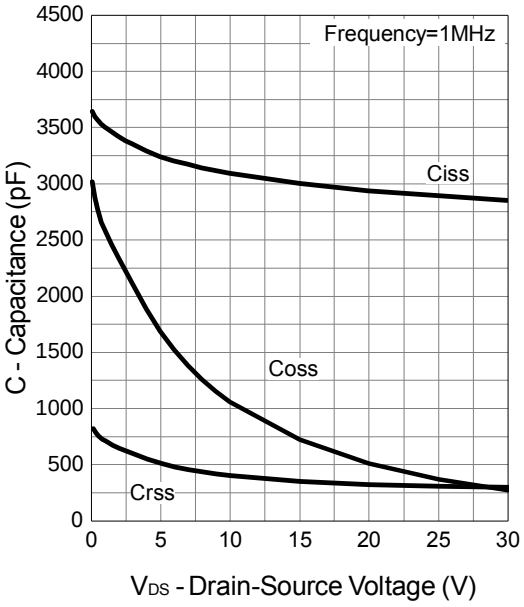


Drain-Source On Resistance

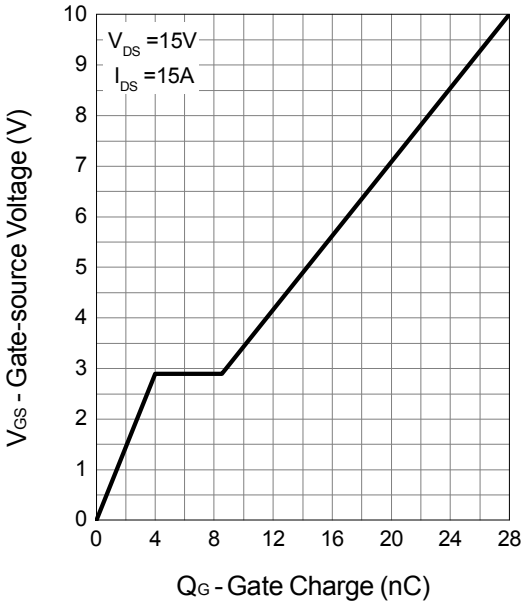


Typical Operating Characteristics (Cont.)

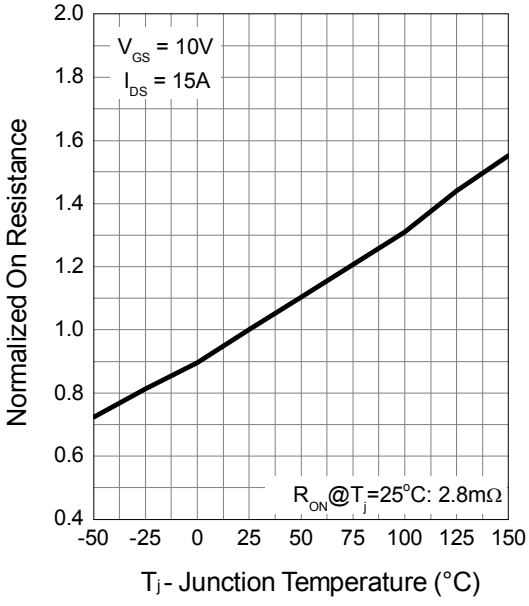
Capacitance



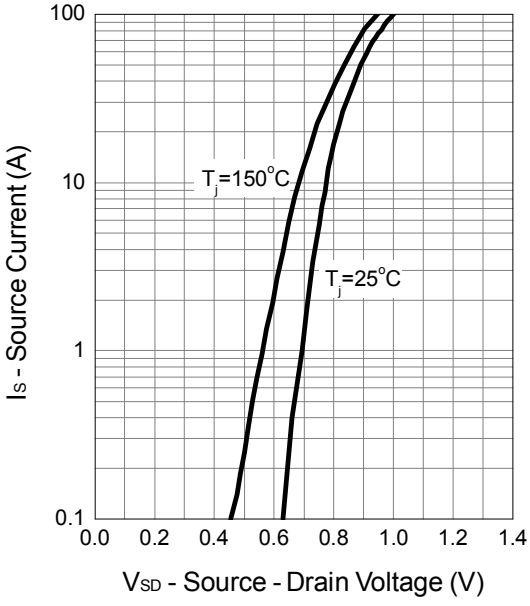
Gate Charge



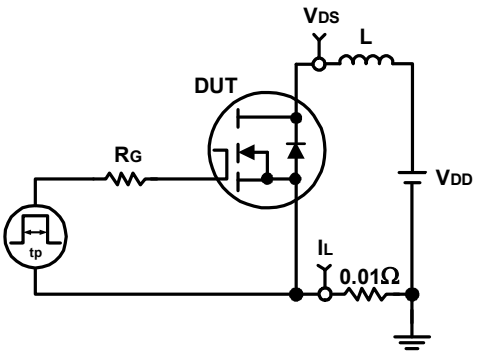
Drain-Source On Resistance

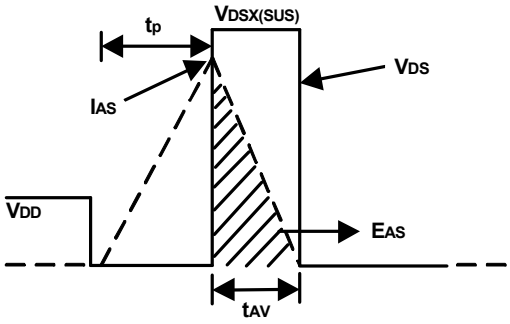


Source-Drain Diode Forward

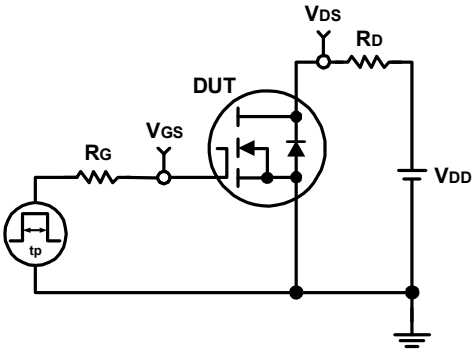


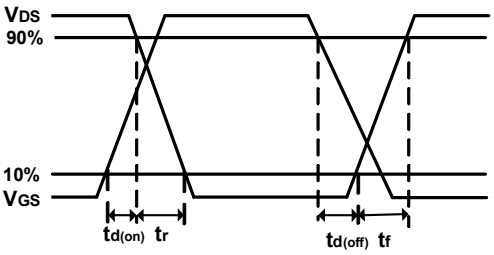
Avalanche Test Circuit and Waveforms





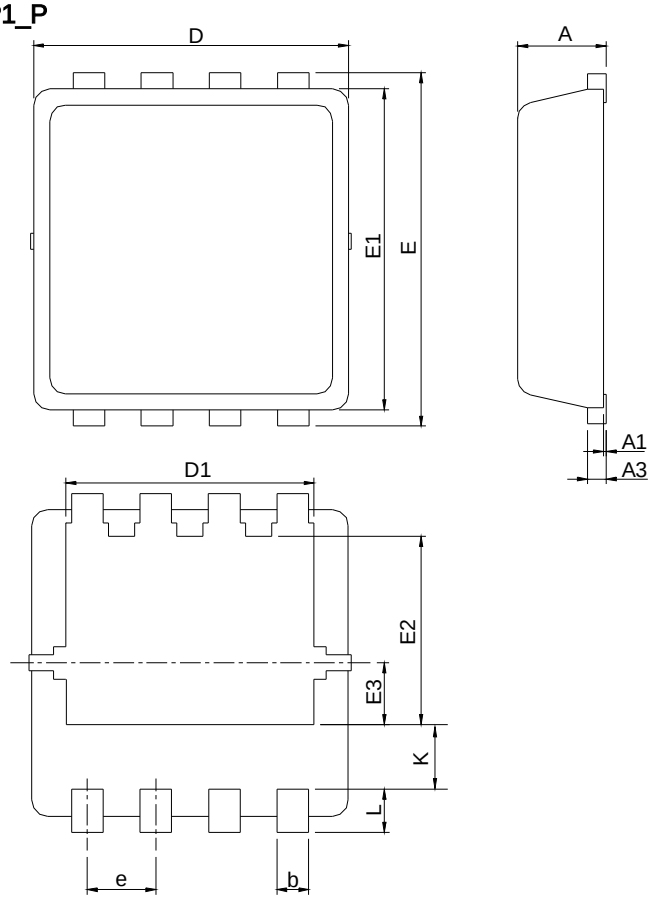
Switching Time Test Circuit and Waveforms





Package Information

DFN3.3x3.3_8L_EP1_P



SYMBOL	DFN3x3-8			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.80	1.00	0.031	0.039
A1	0.00	0.05	0.000	0.002
A3	0.10	0.25	0.004	0.010
b	0.24	0.35	0.009	0.014
D	2.90	3.10	0.114	0.122
D1	2.25	2.45	0.089	0.096
E	3.10	3.30	0.122	0.130
E1	2.90	3.10	0.114	0.122
E2	1.65	1.85	0.065	0.073
E3	0.56	0.58	0.022	0.023
e	0.65 BSC		0.026 BSC	
K	0.475	0.775	0.019	0.031
L	0.30	0.50	0.012	0.020

RECOMMENDED LAND PATTERN