

## Features

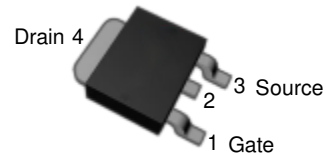
- -60V/-10A,  
 $R_{DS(ON)} = 100m\Omega(\text{typ.}) @ V_{GS} = -10V$   
 $R_{DS(ON)} = 135m\Omega(\text{typ.}) @ V_{GS} = -4.5V$
- Reliable and Rugged
- Lead Free and Green Devices Available  
(RoHS Compliant)
- 100% UIS +  $R_g$  Tested

**Note :** The diode connected between the gate and source serves only as protection against ESD. No gate overvoltage rating is implied.

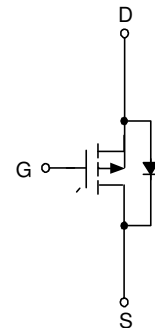
## Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.

## Pin Description



Top View of TO-252-2



P-Channel MOSFET

## Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

| Symbol                                                        | Parameter                                 |                       | Rating     | Unit |
|---------------------------------------------------------------|-------------------------------------------|-----------------------|------------|------|
| Common Ratings (T <sub>A</sub> = 25°C Unless Otherwise Noted) |                                           |                       |            |      |
| V <sub>DSS</sub>                                              | Drain-Source Voltage                      |                       | -60        | V    |
| V <sub>GSS</sub>                                              | Gate-Source Voltage                       |                       | ±25        |      |
| T <sub>J</sub>                                                | Maximum Junction Temperature              |                       | 150        | °C   |
| T <sub>STG</sub>                                              | Storage Temperature Range                 |                       | -55 to 150 |      |
| I <sub>S</sub>                                                | Diode Continuous Forward Current          | T <sub>C</sub> =25°C  | -10        | A    |
| I <sub>D</sub>                                                | Continuous Drain Current                  | T <sub>C</sub> =25°C  | -10        |      |
|                                                               |                                           | T <sub>C</sub> =100°C | -7.5       |      |
| I <sub>DM</sub>                                               | Pulsed Drain Current                      | T <sub>C</sub> =25°C  | -30        |      |
| P <sub>D</sub>                                                | Maximum Power Dissipation                 | T <sub>C</sub> =25°C  | 44.6       | W    |
|                                                               |                                           | T <sub>C</sub> =100°C | 17.9       |      |
| R <sub>θJC</sub>                                              | Thermal Resistance-Junction to Case       | Steady State          | 2.8        | °C/W |
| R <sub>θJA</sub>                                              | Thermal Resistance-Junction to Ambient    | t ≤ 10s               | 20         | °C/W |
|                                                               |                                           | Steady State          | 55         |      |
| I <sub>AS</sub> <sup>a</sup>                                  | Avalanche Current, Single pulse (L=0.1mH) |                       | 38         | A    |
| E <sub>AS</sub> <sup>a</sup>                                  | Avalanche Energy, Single pulse (L=0.1mH)  |                       | 72         | mJ   |

Note \* : Current limited by bond wire.

Note a : UIS tested and pulse width are limited by maximum junction temperature  $150^\circ\text{C}$  (initial temperature  $T_J = 25^\circ\text{C}$ ).

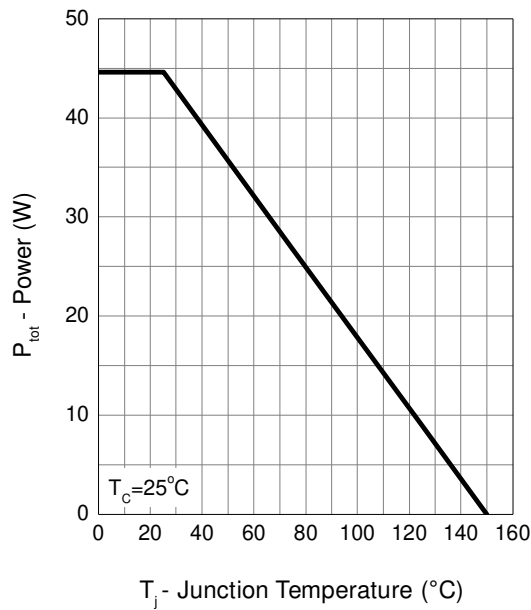
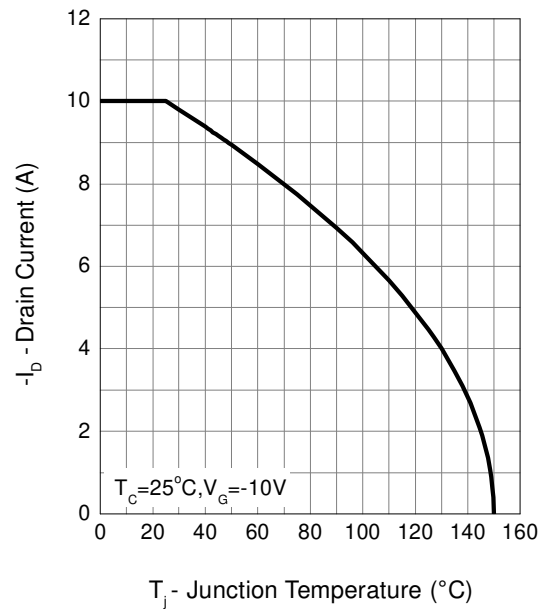
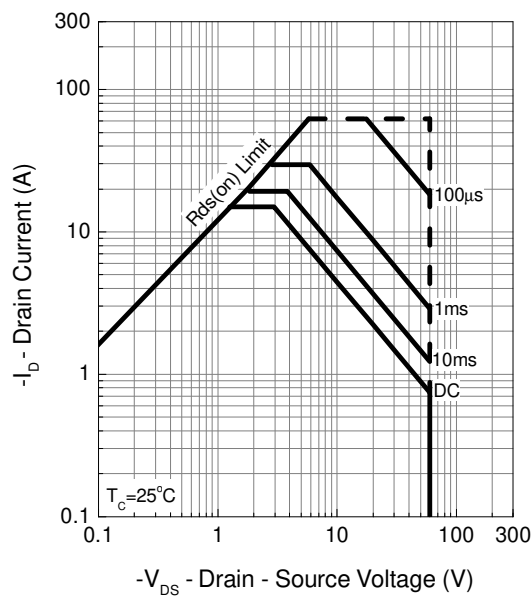
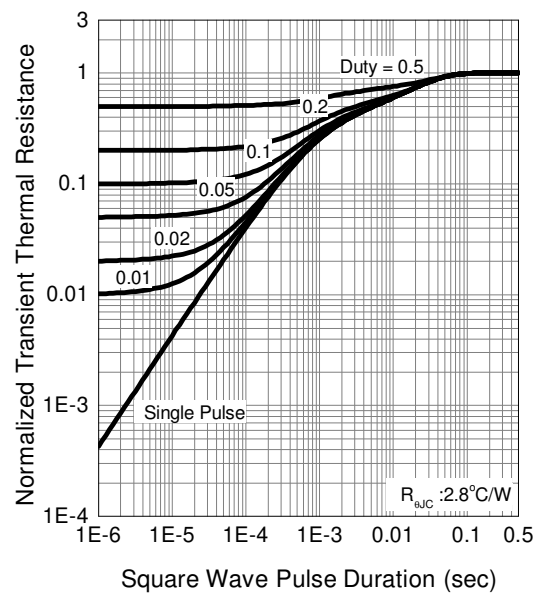
## Electrical Characteristics (T<sub>A</sub> = 25°C Unless Otherwise Noted)

| Symbol                                   | Parameter                        | Test Conditions                                                                                                    | Min. | Typ.       | Max.       | Unit |
|------------------------------------------|----------------------------------|--------------------------------------------------------------------------------------------------------------------|------|------------|------------|------|
| Static Characteristics                   |                                  |                                                                                                                    |      |            |            |      |
| BV <sub>DSS</sub>                        | Drain-Source Breakdown Voltage   | V <sub>GS</sub> =0V, I <sub>DS</sub> =-250μA                                                                       | -60  | -          | -          | V    |
| I <sub>DSS</sub>                         | Zero Gate Voltage Drain Current  | V <sub>DS</sub> =-48V, V <sub>GS</sub> =0V<br>T <sub>J</sub> =85°C                                                 | -    | -          | -1<br>-30  | μA   |
| V <sub>GS(th)</sub>                      | Gate Threshold Voltage           | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =-250μA                                                         | -1   | -1.7       | -2.5       | V    |
| I <sub>GSS</sub>                         | Gate Leakage Current             | V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V                                                                         | -    | -          | ±100       | nA   |
| R <sub>DS(ON)</sub> <sup>b</sup>         | Drain-Source On-state Resistance | V <sub>GS</sub> =-10V, I <sub>DS</sub> =-5.8A<br>V <sub>GS</sub> =-4.5V, I <sub>DS</sub> =-3.5A                    | -    | 100<br>135 | 120<br>160 | mΩ   |
| Diode Characteristics                    |                                  |                                                                                                                    |      |            |            |      |
| V <sub>SD</sub> <sup>b</sup>             | Diode Forward Voltage            | I <sub>SD</sub> =-1A, V <sub>GS</sub> =0V                                                                          | -    | -0.7       | -1         | V    |
| t <sub>rr</sub> <sup>c</sup>             | Reverse Recovery Time            | I <sub>SD</sub> =-5.8A,<br>dI <sub>SD</sub> /dt=100A/μs                                                            | -    | 22         | -          | ns   |
| Q <sub>rr</sub> <sup>c</sup>             | Reverse Recovery Charge          |                                                                                                                    | -    | 23         | -          | nC   |
| Dynamic Characteristics <sup>c</sup>     |                                  |                                                                                                                    |      |            |            |      |
| R <sub>G</sub>                           | Gate Resistance                  | V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz                                                                   | -    | 10         | 20         | Ω    |
| C <sub>iss</sub>                         | Input Capacitance                | V <sub>GS</sub> =0V,<br>V <sub>DS</sub> =-30V,<br>Frequency=1.0MHz                                                 | -    | 940        | -          | pF   |
| C <sub>oss</sub>                         | Output Capacitance               |                                                                                                                    | -    | 86         | -          |      |
| C <sub>rss</sub>                         | Reverse Transfer Capacitance     |                                                                                                                    | -    | 36         | -          |      |
| t <sub>d(ON)</sub>                       | Turn-on Delay Time               | V <sub>DD</sub> =-30V, R <sub>L</sub> =30Ω,<br>I <sub>DS</sub> =-1A, V <sub>GEN</sub> =-10V,<br>R <sub>G</sub> =6Ω | -    | 9          | -          | ns   |
| t <sub>r</sub>                           | Turn-on Rise Time                |                                                                                                                    | -    | 6          | -          |      |
| t <sub>d(OFF)</sub>                      | Turn-off Delay Time              |                                                                                                                    | -    | 36         | -          |      |
| t <sub>f</sub>                           | Turn-off Fall Time               |                                                                                                                    | -    | 25         | -          |      |
| Gate Charge Characteristics <sup>c</sup> |                                  |                                                                                                                    |      |            |            |      |
| Q <sub>g</sub>                           | Total Gate Charge                | V <sub>DS</sub> =-30V, V <sub>GS</sub> =-10V,<br>I <sub>DS</sub> =-5.8A                                            | -    | 12         | -          | nC   |
| Q <sub>gs</sub>                          | Gate-Source Charge               |                                                                                                                    | -    | 1.5        | -          |      |
| Q <sub>gd</sub>                          | Gate-Drain Charge                |                                                                                                                    | -    | 3.3        | -          |      |

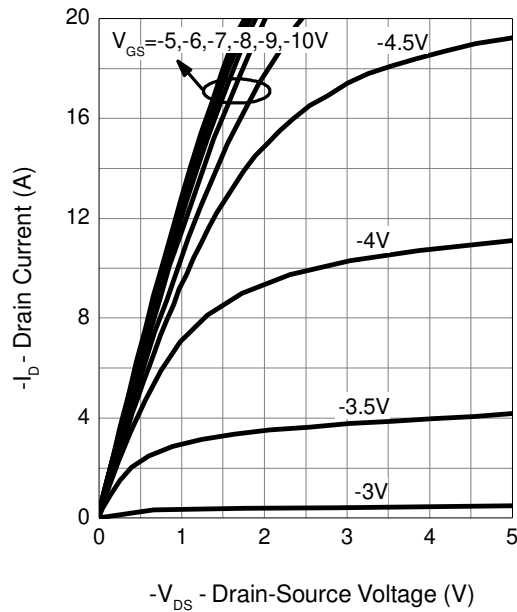
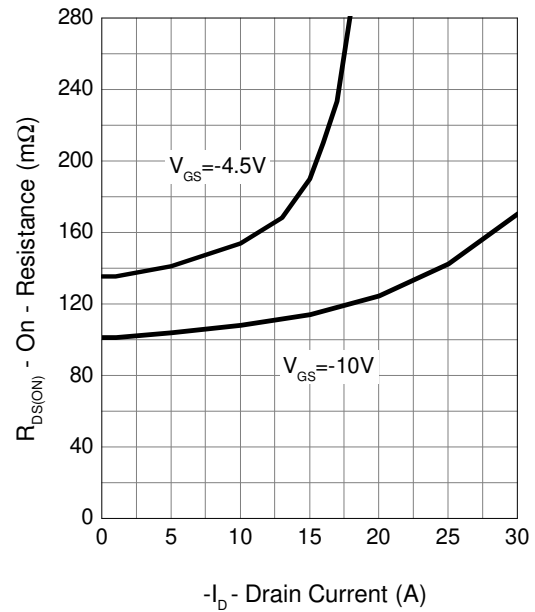
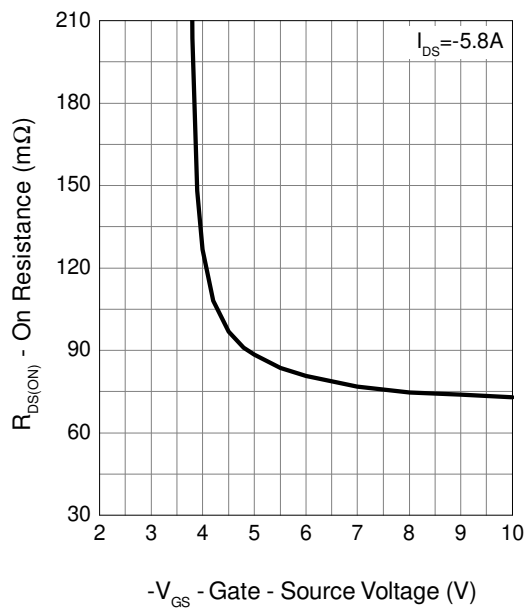
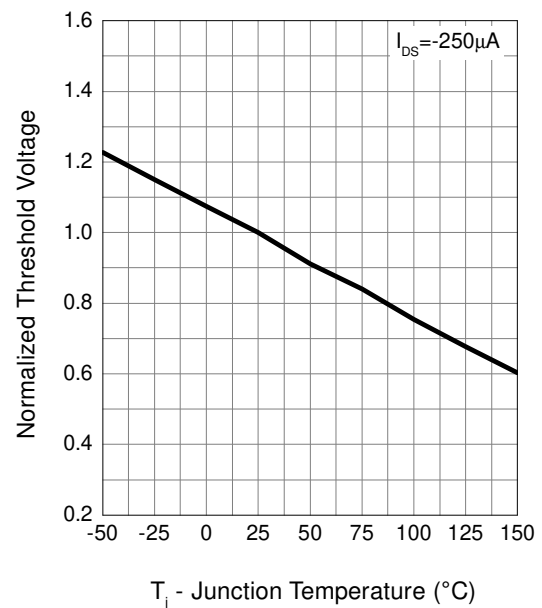
Note b : Pulse test ; pulse width≤300μs, duty cycle≤2%.

Note c : Guaranteed by design, not subject to production testing.

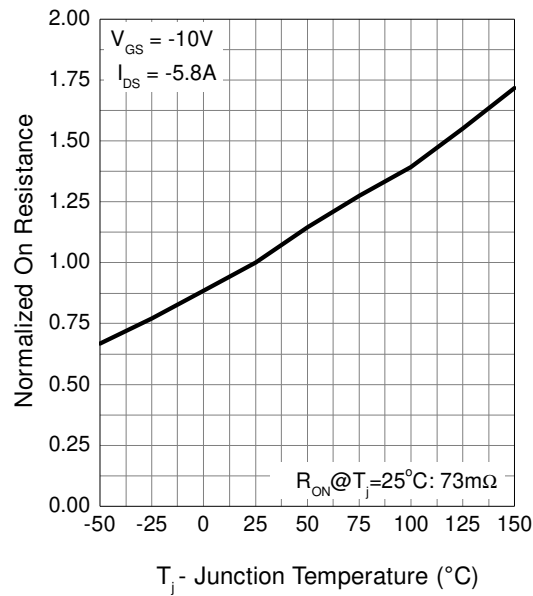
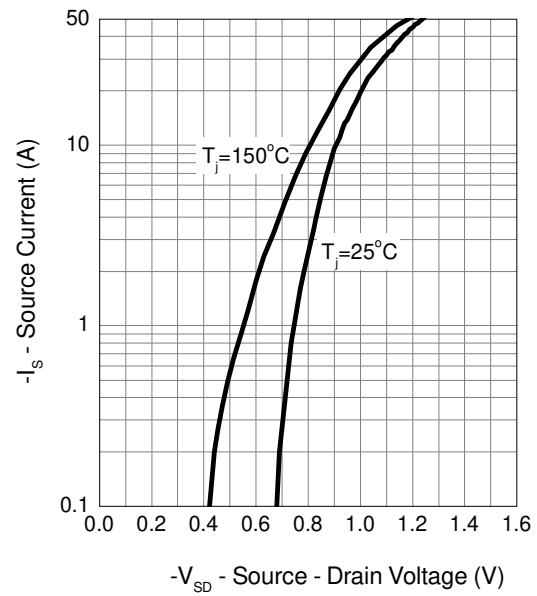
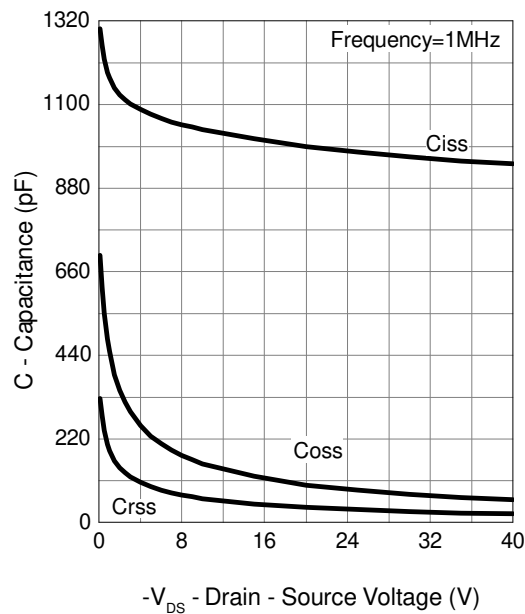
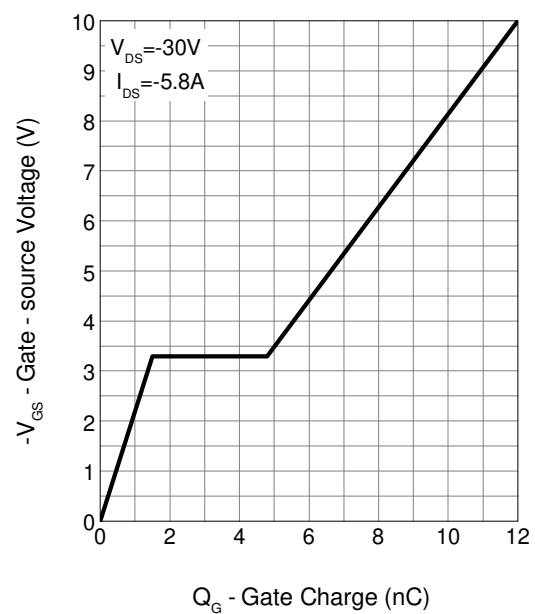
## Typical Operating Characteristics

**Power Dissipation**

**Drain Current**

**Safe Operation Area**

**Thermal Transient Impedance**


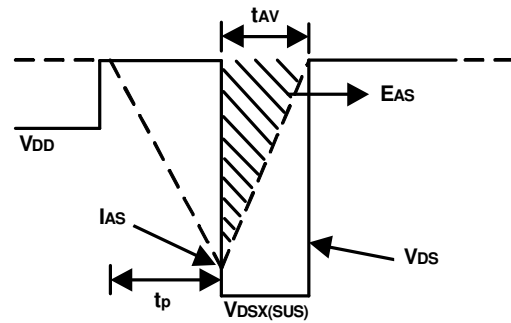
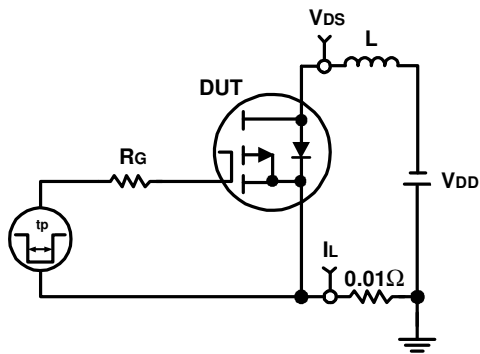
## Typical Operating Characteristics (Cont.)

**Output Characteristics**

**Drain-Source On Resistance**

**Gate-Source On Resistance**

**Gate Threshold Voltage**


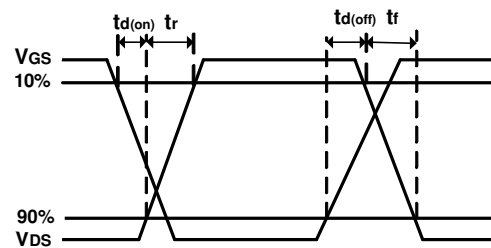
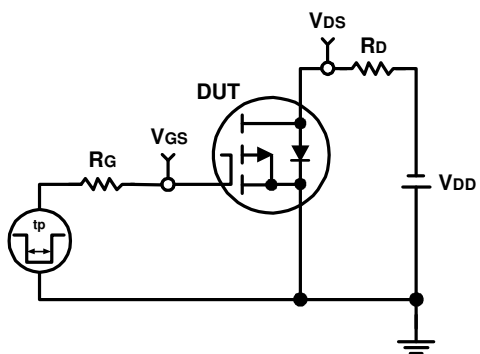
## Typical Operating Characteristics (Cont.)

**Drain-Source On Resistance**

**Source-Drain Diode Forward**

**Capacitance**

**Gate Charge**


## Avalanche Test Circuit and Waveforms

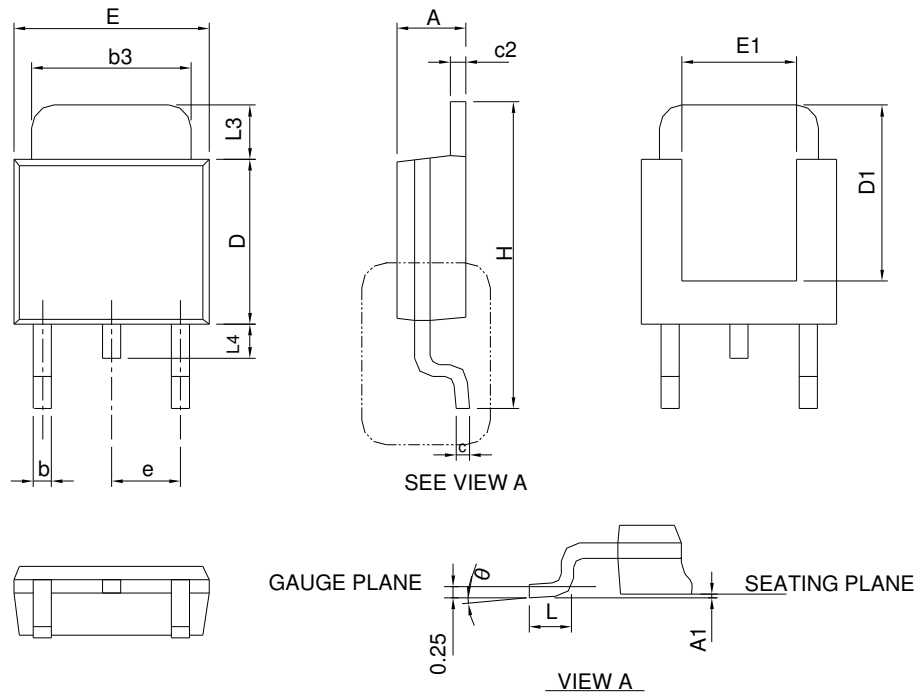


## Switching Time Test Circuit and Waveforms



## Package Information

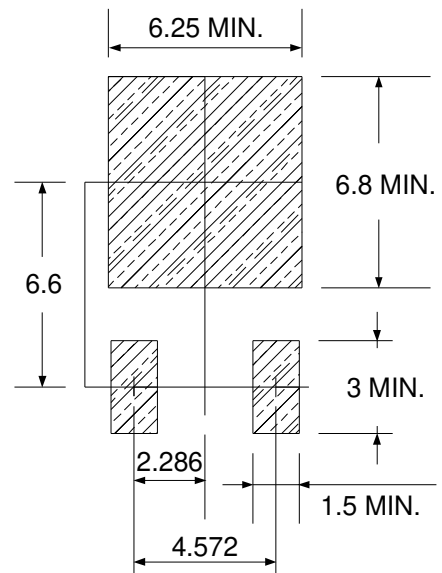
TO-252-2



| SYMBOL | TO-252-2    |       |           |       |
|--------|-------------|-------|-----------|-------|
|        | MILLIMETERS |       | INCHES    |       |
|        | MIN.        | MAX.  | MIN.      | MAX.  |
| A      | 2.18        | 2.39  | 0.086     | 0.094 |
| A1     | -           | 0.13  | -         | 0.005 |
| b      | 0.50        | 0.89  | 0.020     | 0.035 |
| b3     | 4.95        | 5.46  | 0.195     | 0.215 |
| c      | 0.46        | 0.61  | 0.018     | 0.024 |
| c2     | 0.46        | 0.89  | 0.018     | 0.035 |
| D      | 5.33        | 6.22  | 0.210     | 0.245 |
| D1     | 4.57        | 6.00  | 0.180     | 0.236 |
| E      | 6.35        | 6.73  | 0.250     | 0.265 |
| E1     | 3.81        | 6.00  | 0.150     | 0.236 |
| e      | 2.29 BSC    |       | 0.090 BSC |       |
| H      | 9.40        | 10.41 | 0.370     | 0.410 |
| L      | 0.90        | 1.78  | 0.035     | 0.070 |
| L3     | 0.89        | 2.03  | 0.035     | 0.080 |
| L4     | -           | 1.02  | -         | 0.040 |
| θ      | 0°          | 8°    | 0°        | 8°    |

Note : Follow JEDEC TO-252 .

### RECOMMENDED LAND PATTERN



UNIT: mm