

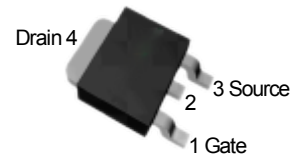
Features

- 200V/5.8A,
 $R_{DS(ON)} = 600m\Omega$ (max.) @ $V_{GS}=10V$
 $R_{DS(ON)} = 620m\Omega$ (max.) @ $V_{GS}=4.5V$
- Reliable and Rugged
- Lead Free and Green Devices Available
(RoHS Compliant)

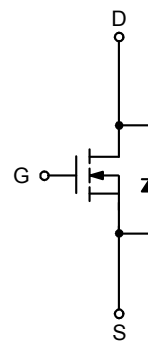
Applications

- High Efficiency Synchronous Rectification in SMPS.
- Uninterruptible Power Supply.
- High Speed Power Switching.
- Hard Switched and High Frequency Circuits.

Pin Description



Top View of TO-252-2



N-Channel MOSFET

Absolute Maximum Ratings@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	200	V
V _{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Drain Current, V _{GS} @ 10V	5.8	A
I _D @T _C =100°C	Drain Current, V _{GS} @ 10V	3.7	A
I _{DM}	Pulsed Drain Current ¹	20	A
P _D @T _C =25°C	Total Power Dissipation	44.6	W
P _D @T _A =25°C	Total Power Dissipation	2	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Value	Unit
R _{thj-c}	Maximum Thermal Resistance, Junction-case	2.8	°C/W
R _{thj-a}	Maximum Thermal Resistance, Junction-ambient (PCB mount) ³	62.5	°C/W
R _{thj-a}	Maximum Thermal Resistance, Junction-ambient	110	°C/W

Note a: Bond wire current limit is 5.8A.

Note b: Pulse width limited by max. junction temperature.

Note c: UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature T_j=25°C).

Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	200	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =5A	-	-	600	mΩ
		V _{GS} =4.5V, I _D =2A	-	-	620	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	1	-	3	V
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =5A	-	7	-	S
I _{DSS}	Drain-Source Leakage Current	V _{DS} =160V, V _{GS} =0V	-	-	25	uA
I _{GSS}	Gate-Source Leakage	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Q _g	Total Gate Charge ²	I _D =5A	-	30	48	nC
Q _{gs}	Gate-Source Charge	V _{DS} =160V	-	3	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =10V	-	11.5	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DD} =100V	-	6	-	ns
t _r	Rise Time	I _D =5A	-	8.5	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =3.3Ω	-	30	-	ns
t _f	Fall Time	V _{GS} =10V	-	5	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	530	850	pF
C _{oss}	Output Capacitance	V _{DS} =25V	-	70	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	35	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =5A, V _{GS} =0V	-	-	1.3	V
t _{rr}	Reverse Recovery Time ²	I _S =5A, V _{GS} =0V,	-	190	-	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	1.6	-	uC

Note d Pulse test ; pulse width≤300μs, duty cycle≤2%.

Note e Guaranteed by design, not subject to production testing.

Typical Operating Characteristics

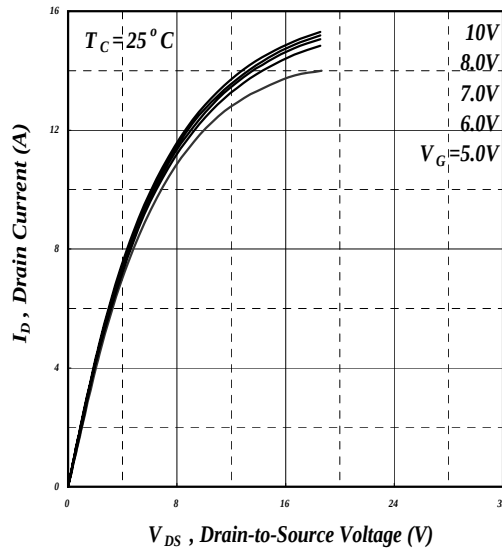


Fig 1. Typical Output Characteristics

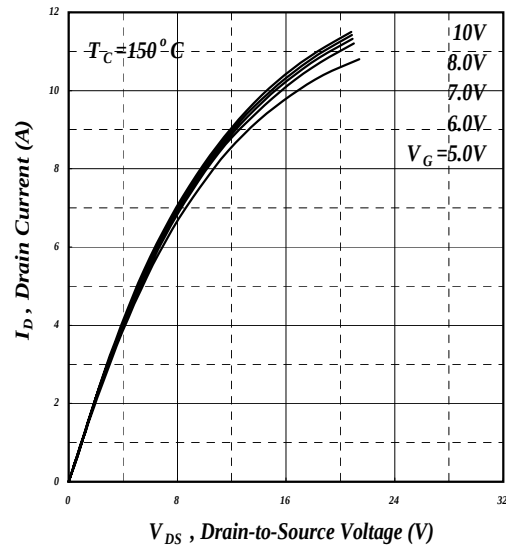


Fig 2. Typical Output Characteristics

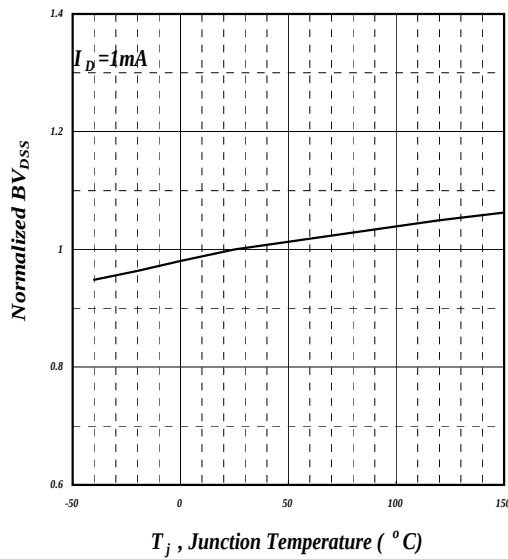


Fig 3. Normalized BV_{DSS} v.s. Junction Temperature

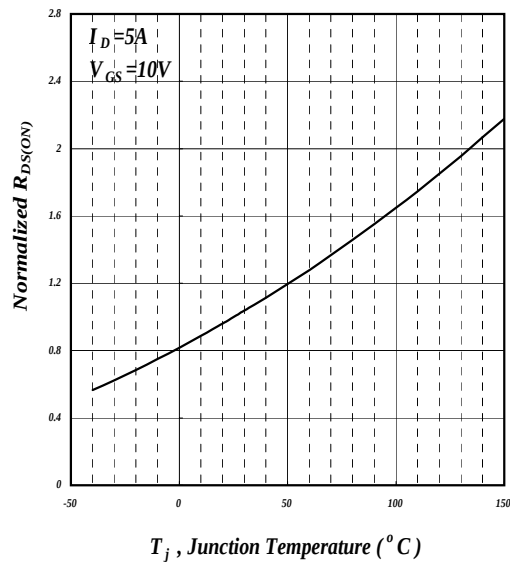


Fig 4. Normalized On-Resistance v.s. Junction Temperature

Typical Operating Characteristics (Cont.)

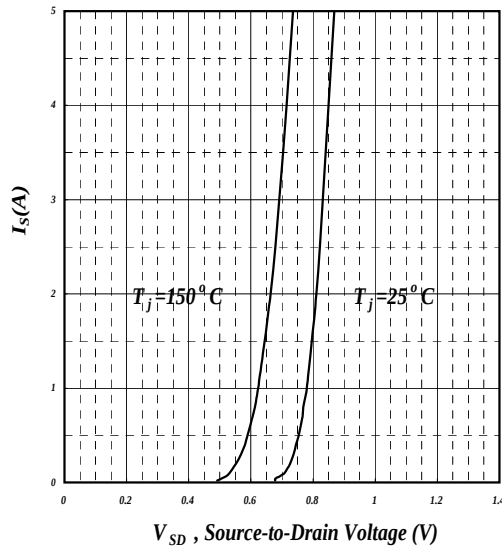


Fig 5. Forward Characteristic of Reverse Diode

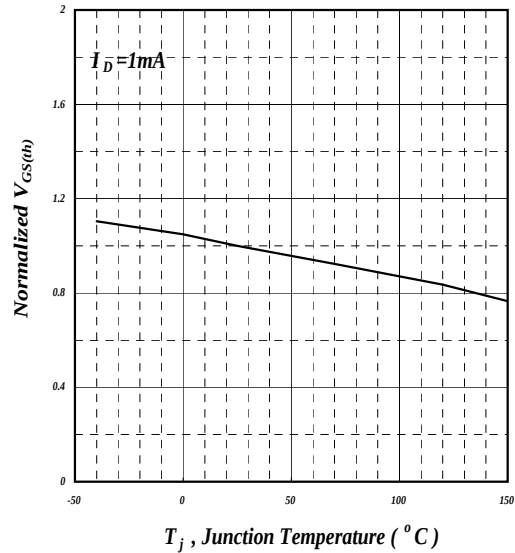


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

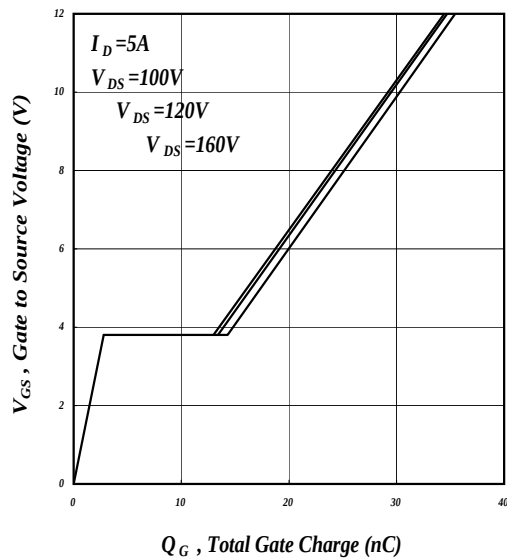


Fig 7. Gate Charge Characteristics

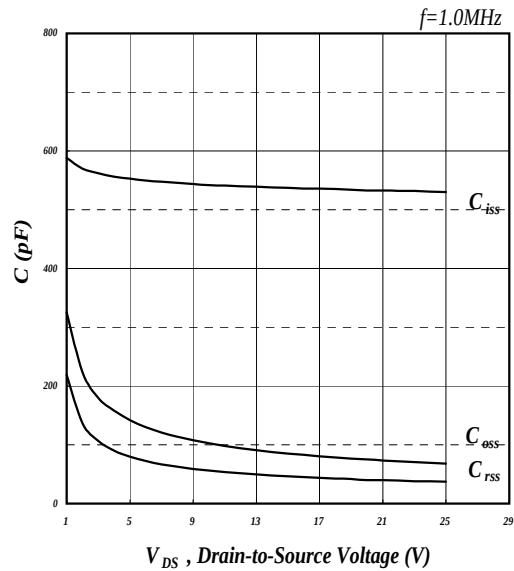


Fig 8. Typical Capacitance Characteristics

Typical Operating Characteristics (Cont.)

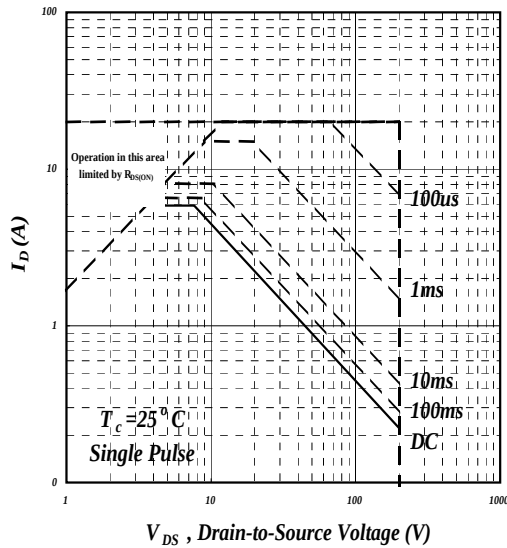


Fig 9. Maximum Safe Operating Area

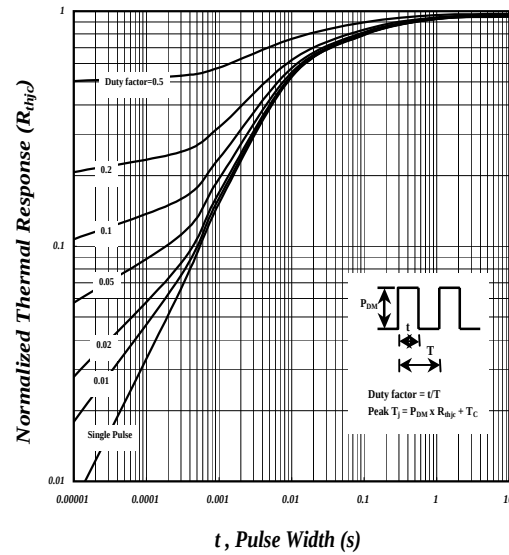


Fig10. Effective Transient Thermal Impedance

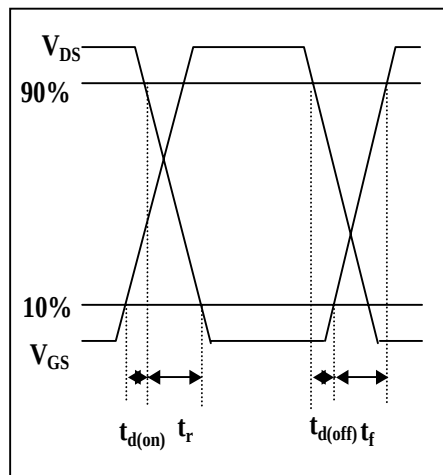


Fig 11. Switching Time Waveform

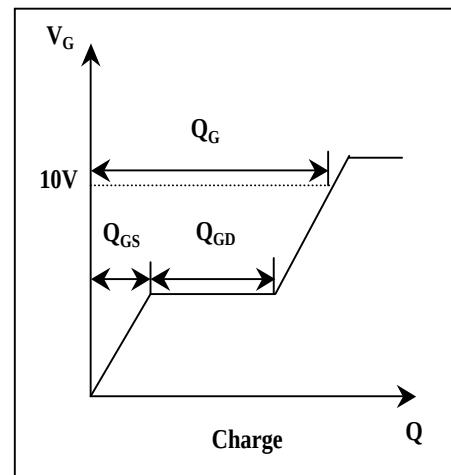
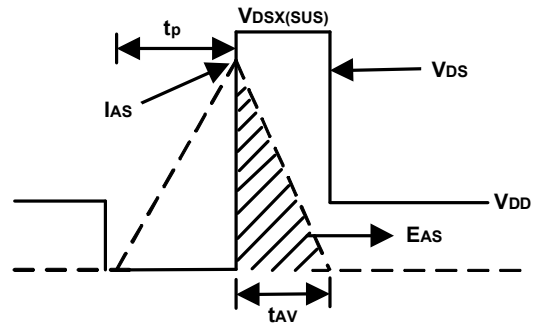
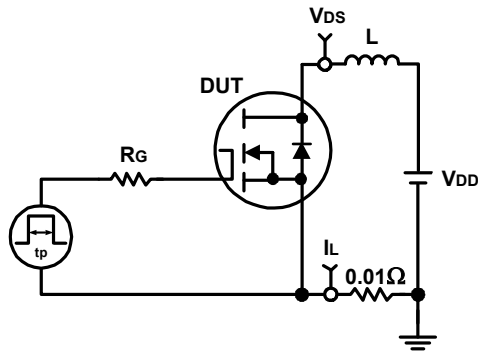
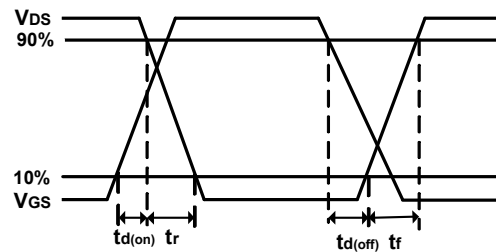
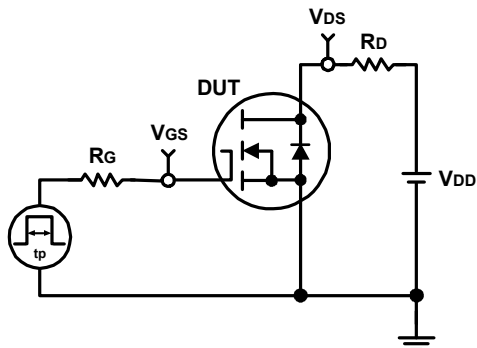


Fig 12. Gate Charge Waveform

Avalanche Test Circuit and Waveforms

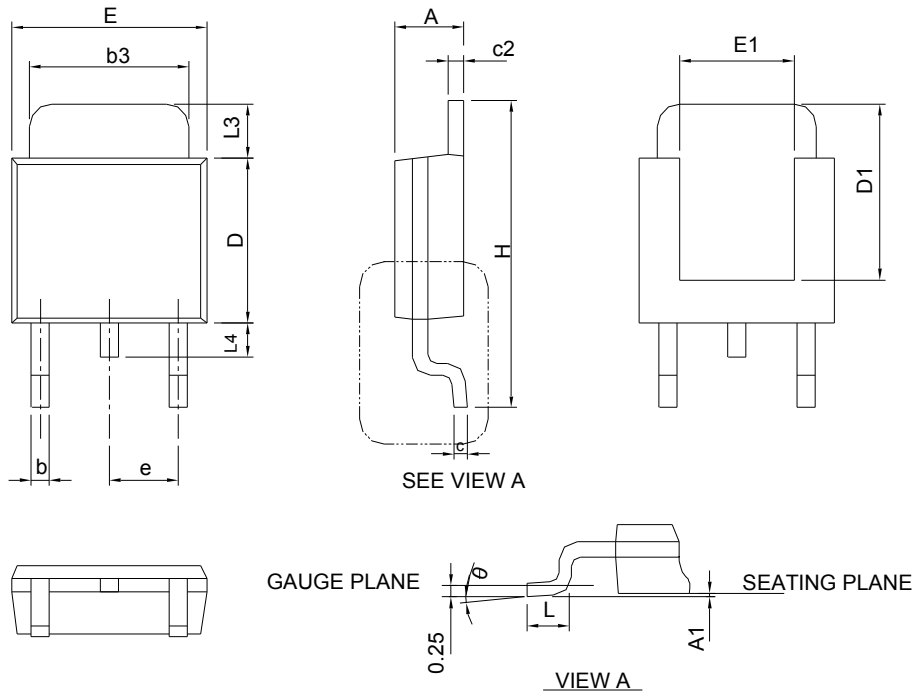


Switching Time Test Circuit and Waveforms



Package Information

TO-252-2



SYMBOL	TO-252-2			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	2.18	2.39	0.086	0.094
A1	-	0.13	-	0.005
b	0.50	0.89	0.020	0.035
b3	4.95	5.46	0.195	0.215
c	0.46	0.61	0.018	0.024
c2	0.46	0.89	0.018	0.035
D	5.33	6.22	0.210	0.245
D1	4.57	6.00	0.180	0.236
E	6.35	6.73	0.250	0.265
E1	3.81	6.00	0.150	0.236
e	2.29 BSC		0.090 BSC	
H	9.40	10.41	0.370	0.410
L	0.90	1.78	0.035	0.070
L3	0.89	2.03	0.035	0.080
L4	-	1.02	-	0.040
θ	0°	8°	0°	8°

Note : Follow JEDEC TO-252-2 .

RECOMMENDED LAND PATTERN

