

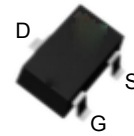
## Features

- -60V/-0.13A,  
 $R_{DS(ON)} = 2.2 \Omega$  (typ.) @  $V_{GS} = -10V$   
 $R_{DS(ON)} = 2.5 \Omega$  (typ.) @  $V_{GS} = -4.5V$
- 100% UIS+ $R_g$  Tested
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

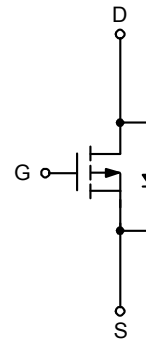
## Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.

## Pin Description



Top View of SOT-23-3



P-Channel MOSFET

## Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

| Symbol            | Parameter                              |                          | Rating     | Unit               |
|-------------------|----------------------------------------|--------------------------|------------|--------------------|
| $V_{DS}$          | Drain-Source Voltage                   |                          | -60        | V                  |
| $V_{GS}$          | Gate-Source Voltage                    |                          | $\pm 20$   |                    |
| $I_D^*$           | Continuous Drain Current               | $T_A = 25^\circ\text{C}$ | -0.13      | A                  |
|                   |                                        | $T_A = 70^\circ\text{C}$ | -0.098     |                    |
| $I_{DM}^*$        | Pulsed Drain Current                   | $T_A = 25^\circ\text{C}$ | -0.39      |                    |
|                   |                                        | $T_A = 70^\circ\text{C}$ | -0.296     |                    |
| $I_S^*$           | Diode Continuous Forward Current       |                          | -0.13      |                    |
| $T_J$             | Maximum Junction Temperature           |                          | 150        | $^\circ\text{C}$   |
| $T_{STG}$         | Storage Temperature Range              |                          | -55 to 150 |                    |
| $P_D^*$           | Maximum Power Dissipation              | $T_A = 25^\circ\text{C}$ | 0.225      | W                  |
|                   |                                        | $T_A = 70^\circ\text{C}$ | 0.09       |                    |
| $R_{\theta JA}^*$ | Thermal Resistance-Junction to Ambient | $t \leq 10\text{s}$      | 90         | $^\circ\text{C/W}$ |
|                   |                                        | Steady state             | 125        |                    |

Note \* : Surface Mounted on  $1\text{in}^2$  pad area,  $t \leq 10\text{sec}$ .

## Electrical Characteristics (T<sub>A</sub> = 25°C unless otherwise noted)

| Symbol                                   | Parameter                        | Test Conditions                                                                                                     | Min.   | Typ.       | Max.       | Unit |
|------------------------------------------|----------------------------------|---------------------------------------------------------------------------------------------------------------------|--------|------------|------------|------|
| Static Characteristics                   |                                  |                                                                                                                     |        |            |            |      |
| BV <sub>DSS</sub>                        | Drain-Source Breakdown Voltage   | V <sub>GS</sub> =0V, I <sub>DS</sub> =-250μA                                                                        | -60    | -          | -          | V    |
| I <sub>DSS</sub>                         | Zero Gate Voltage Drain Current  | V <sub>DS</sub> =-48V, V <sub>GS</sub> =0V<br>T <sub>J</sub> =85°C                                                  | -      | -          | -1<br>-30  | μA   |
| V <sub>GS(th)</sub>                      | Gate Threshold Voltage           | V <sub>DS</sub> =V <sub>GS</sub> , I <sub>DS</sub> =-250μA                                                          | -0.8   | -1.6       | -2.0       | V    |
| I <sub>GSS</sub>                         | Gate Leakage Current             | V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V                                                                          | -      | -          | ±100       | nA   |
| R <sub>DS(ON)</sub> <sup>a</sup>         | Drain-Source On-State Resistance | V <sub>GS</sub> =-10V, I <sub>DS</sub> =-0.5A<br>V <sub>GS</sub> =-4.5V, I <sub>DS</sub> =-0.2A                     | -<br>- | 2.2<br>2.5 | 3.2<br>3.5 | Ω    |
| V <sub>SD</sub> <sup>a</sup>             | Diode Forward Voltage            | I <sub>SD</sub> =-1A, V <sub>GS</sub> =0V                                                                           | -      | -0.7       | -1         | V    |
| Dynamic Characteristics <sup>b</sup>     |                                  |                                                                                                                     |        |            |            |      |
| R <sub>G</sub>                           | Gate Resistance                  | V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz                                                                    | -      | 10.7       | -          | Ω    |
| C <sub>iss</sub>                         | Input Capacitance                | V <sub>GS</sub> =0V,<br>V <sub>DS</sub> =-5V,<br>Frequency=1.0MHz                                                   | -      | 30         | -          | pF   |
| C <sub>oss</sub>                         | Output Capacitance               |                                                                                                                     | -      | 10         | -          |      |
| C <sub>rss</sub>                         | Reverse Transfer Capacitance     |                                                                                                                     | -      | 5          | -          |      |
| t <sub>d(ON)</sub>                       | Turn-on Delay Time               | V <sub>DD</sub> =-10V, R <sub>L</sub> =10Ω,<br>I <sub>DS</sub> =-1A, V <sub>GEN</sub> =-4.5V,<br>R <sub>G</sub> =6Ω | -      | 2.8        | -          | ns   |
| t <sub>r</sub>                           | Turn-on Rise Time                |                                                                                                                     | -      | 1.2        | -          |      |
| t <sub>d(OFF)</sub>                      | Turn-off Delay Time              |                                                                                                                     | -      | 16         | -          |      |
| t <sub>f</sub>                           | Turn-off Fall Time               |                                                                                                                     | -      | 8          | -          |      |
| t <sub>rr</sub>                          | Reverse Recovery Time            | I <sub>SD</sub> =-3.3A, dI <sub>SD</sub> /dt =100A/μs                                                               | -      | 16         | -          | nC   |
| Q <sub>rr</sub>                          | Reverse Recovery Charge          |                                                                                                                     | -      | 6          | -          |      |
| Gate Charge Characteristics <sup>b</sup> |                                  |                                                                                                                     |        |            |            |      |
| Q <sub>g</sub>                           | Total Gate Charge                | V <sub>DS</sub> =-10V, V <sub>GS</sub> =-4.5V,<br>I <sub>DS</sub> =-3.3A                                            | -      | 4.6        | -          | nC   |
| Q <sub>gs</sub>                          | Gate-Source Charge               |                                                                                                                     | -      | 0.7        | -          |      |
| Q <sub>gd</sub>                          | Gate-Drain Charge                |                                                                                                                     | -      | 1.8        | -          |      |

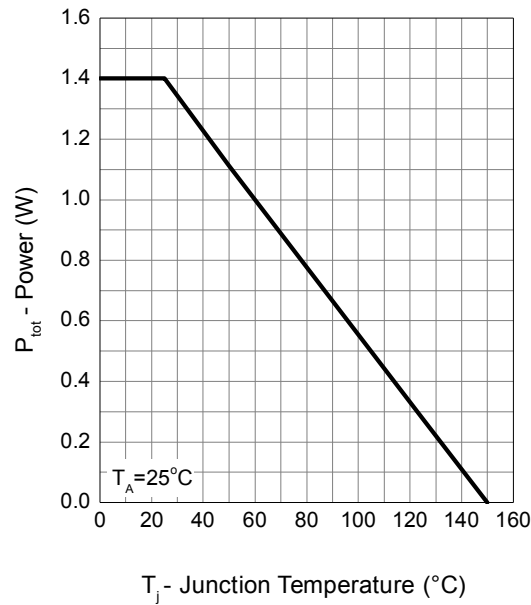
Note a : Pulse test; pulse width≤300μs, duty cycle≤2%.

Note b : Guaranteed by design, not subject to production testing.

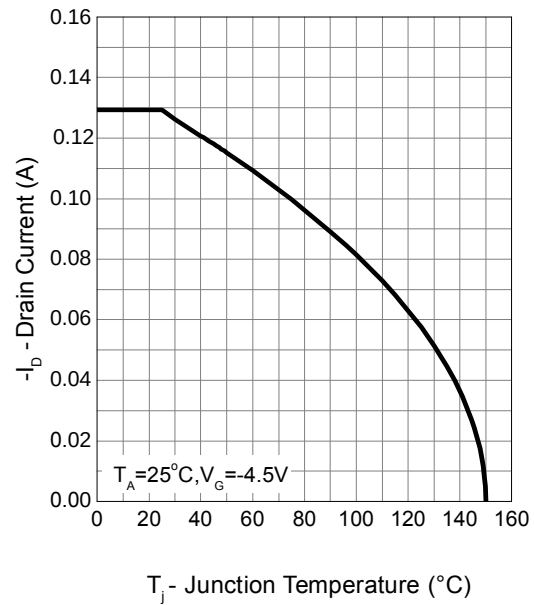
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## Typical Operating Characteristics

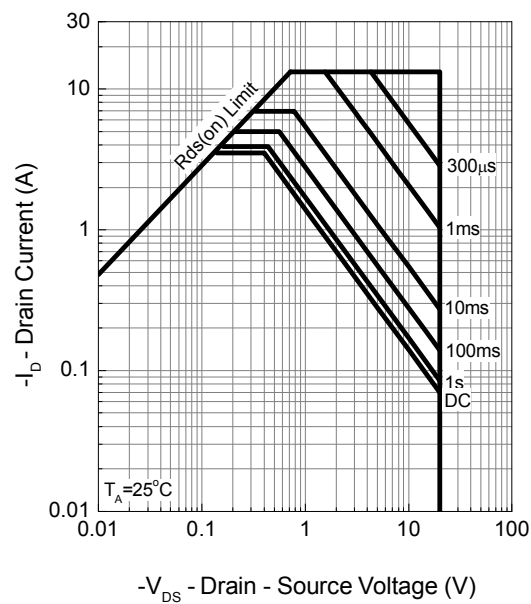
Power Dissipation



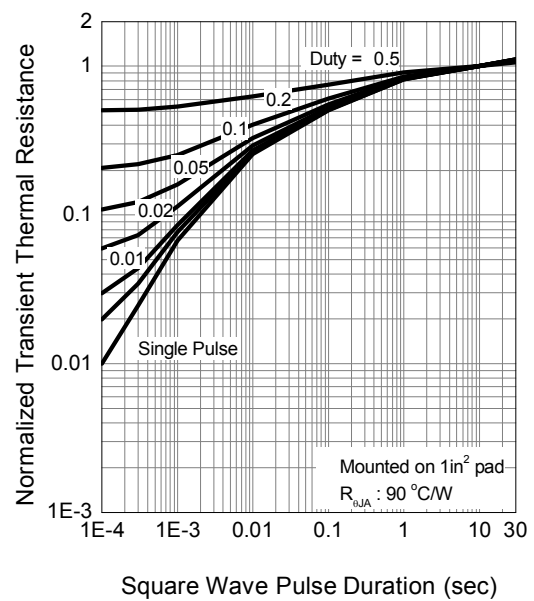
Drain Current



Safe Operation Area

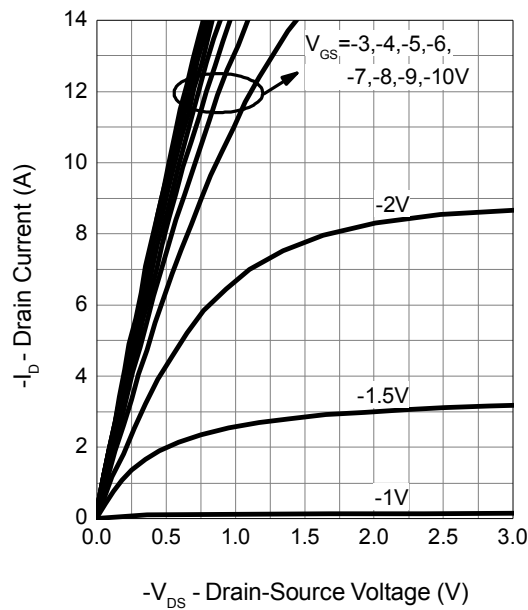


Thermal Transient Impedance

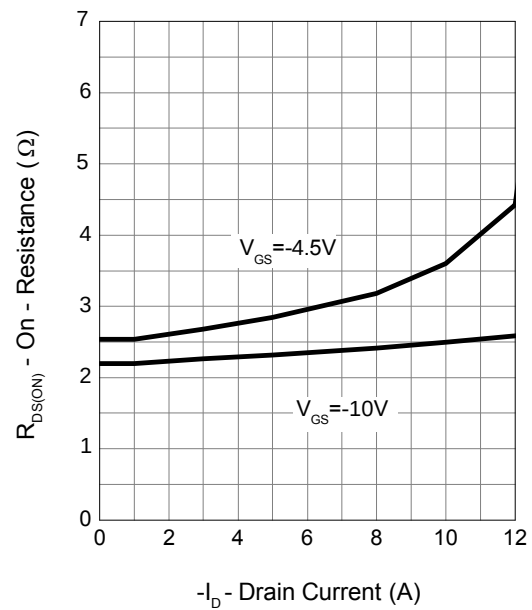


## Typical Operating Characteristics (Cont.)

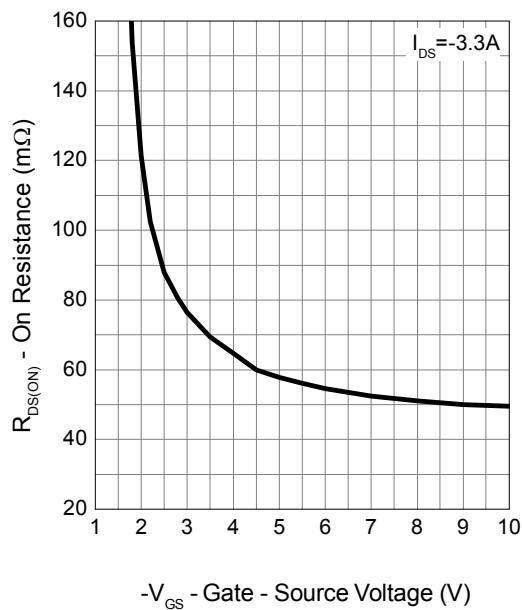
Output Characteristics



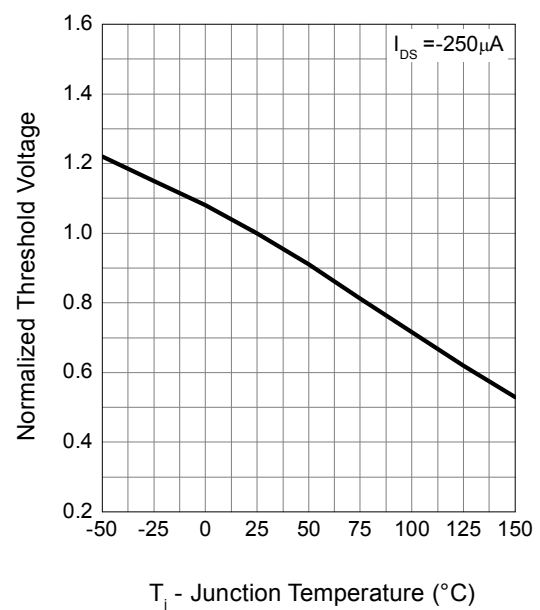
Drain-Source On Resistance



Gate-Source On Resistance

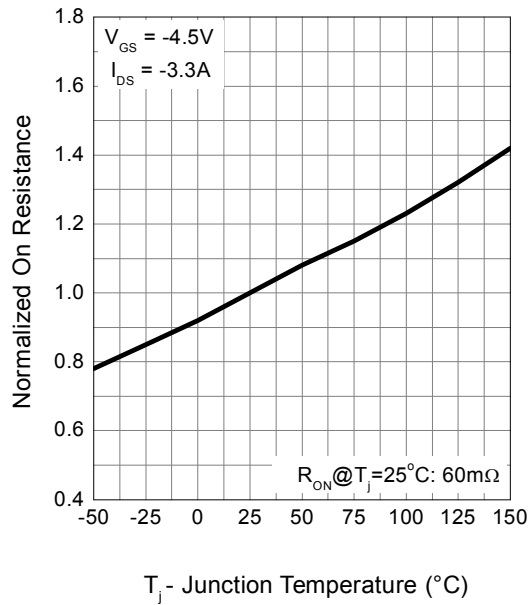


Gate Threshold Voltage

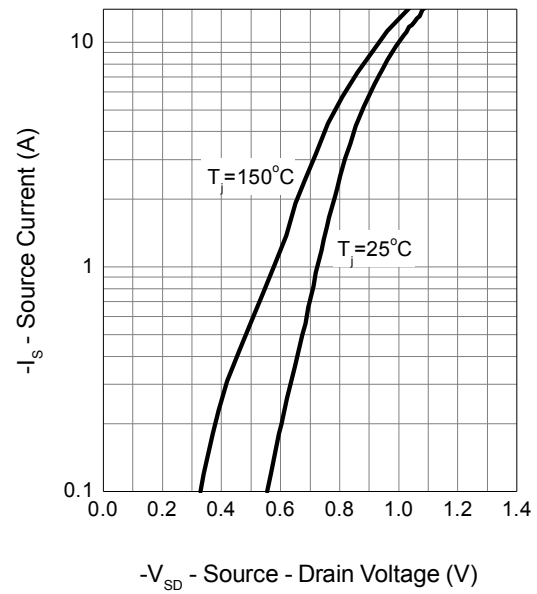


## Typical Operating Characteristics (Cont.)

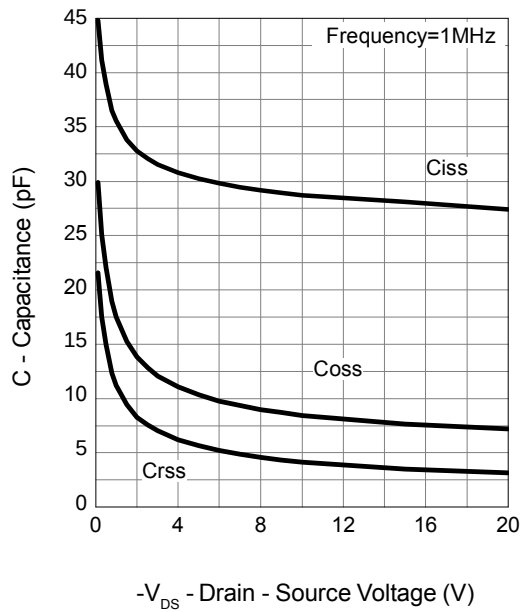
Drain-Source On Resistance



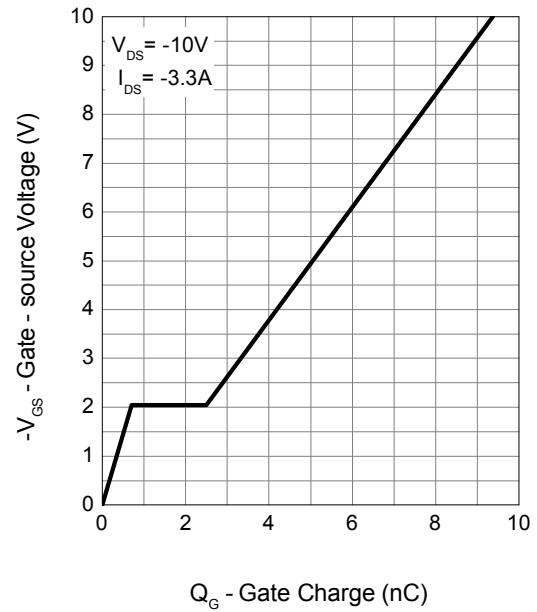
Source-Drain Diode Forward



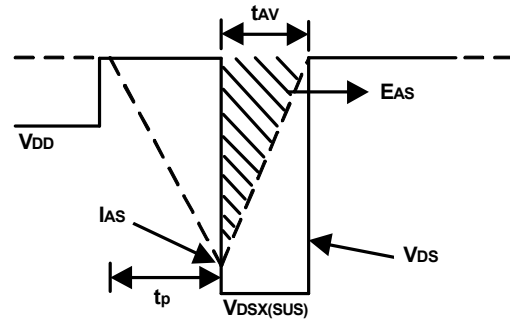
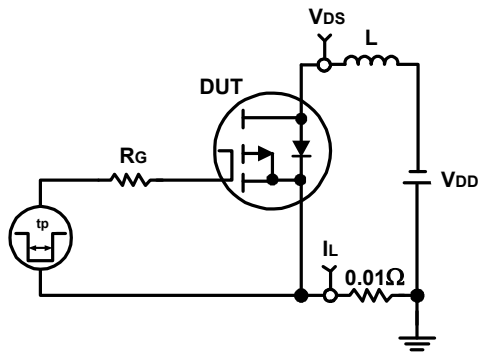
Capacitance



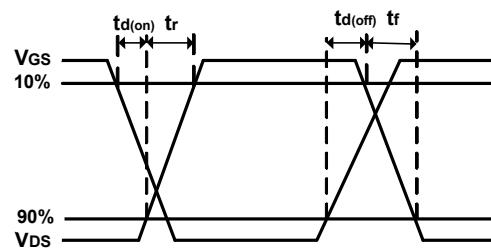
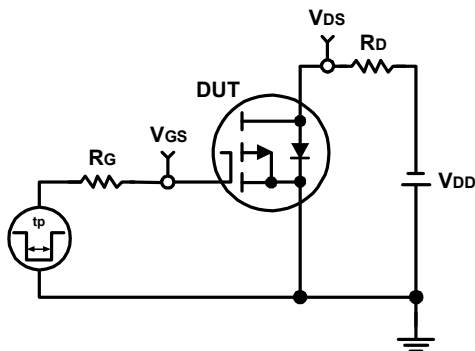
Gate Charge



## Avalanche Test Circuit and Waveforms

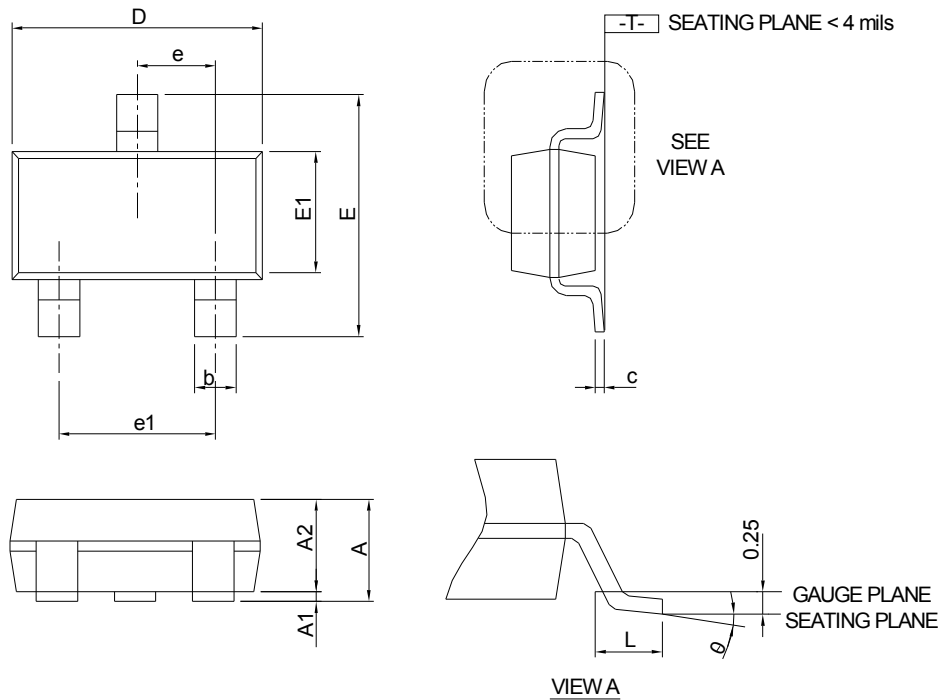


## Switching Time Test Circuit and Waveforms



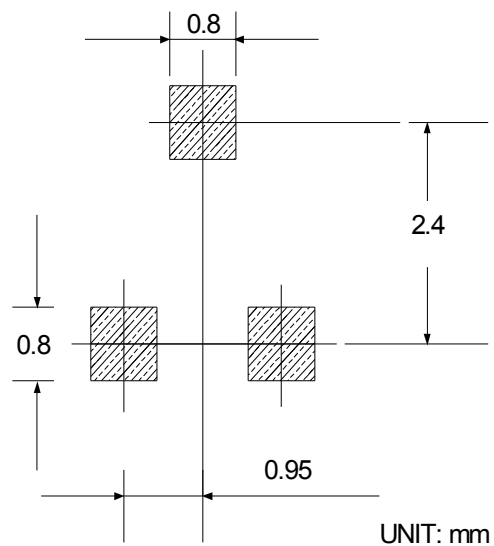
## Package Information

### SOT-23-3



| SYMBOL | SOT-23-3    |      |           |       |
|--------|-------------|------|-----------|-------|
|        | MILLIMETERS |      | INCHES    |       |
|        | MIN.        | MAX. | MIN.      | MAX.  |
| A      |             | 1.20 |           | 0.047 |
| A1     | 0.00        | 0.08 | 0.000     | 0.003 |
| A2     | 0.90        | 1.12 | 0.035     | 0.044 |
| b      | 0.30        | 0.50 | 0.012     | 0.020 |
| c      | 0.08        | 0.22 | 0.003     | 0.009 |
| D      | 2.70        | 3.10 | 0.106     | 0.122 |
| E      | 2.60        | 3.00 | 0.102     | 0.118 |
| E1     | 1.40        | 1.80 | 0.055     | 0.071 |
| e      | 0.95 BSC    |      | 0.037 BSC |       |
| e1     | 1.90 BSC    |      | 0.075 BSC |       |
| L      | 0.30        | 0.60 | 0.012     | 0.024 |
| θ      | 0°          | 8°   | 0°        | 8°    |

### RECOMMENDED LAND PATTERN



Note : Dimension D and E1 do not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 10 mil per side.