

PRODUCT CHARACTERISTICS

V_{DSS}	650V
$R_{DS(ON)} Typ(@V_{GS}=10V)$	1.12 Ω
$Q_g@typ$	23nC
I_D	7A

APPLICATIONS

- * High efficiency switch mode power supplies
- * Electronic lamp ballasts based on half bridge
- * LED power supplies

FEATURE

- * High Switching Speed
- * Improved dv/dt capability

ORDER INFORMATION

Order Codes		Package	Packing
Halogen-Free	Halogen		
N/A	RST7N65AD	TO-252	2500 pieces/Reel

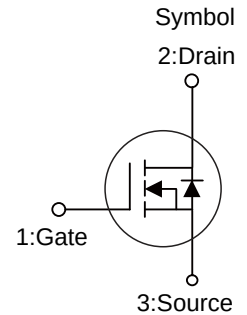
ABSOLUTE MAXIMUM RATINGS($T_A=25^{\circ}C$, unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DSS}	650	V
Gate-Source Voltage	V_{GSS}	± 30	V
Drain Current Continuous(@ $V_{GS}=10V, T_A=25^{\circ}C$)	I_D	7	A
Drain Current Pulsed	I_{DM}	28	A
Avalanche Energy *	E_{AS}	845	mJ
Peak Diode Recovery dv/dt	dv/dt	5.0	V/ns
Power Dissipation	P_D	100	W
Junction Temperature	T_J	+150	$^{\circ}C$
Storage Temperature	T_{STG}	-55~ +150	$^{\circ}C$

THERMAL CHARACTERISTICS

Parameter	Symbol	Typ	Unit
Junction to Ambient	R_{thJA}	100	$^{\circ}C/W$
Junction to Case	R_{thJC}	1.25	$^{\circ}C/W$

Note: * EAS condition: $T_J=25^{\circ}C, V_{DD}=50V, V_G=10V, L=10mH, R_g=25\Omega$



■ ELECTRICAL CHARACTERISTICS ($T_C=25^{\circ}\text{C}$, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Off characteristics						
Drain to Source Breakdown Voltage	V _{DSS}	V _{GS} =0V,I _D =250μA	650	-	-	V
Drain to Source Leakage Current	I _{DSS}	V _{DS} =650V,V _{GS} =0V	-	-	1	μA
Gate to Source Forward Leakage	I _{GSS(F)}	V _{DS} =0V, V _{GS} =+30V	-	-	100	nA
Gate to Source Reverse Leakage	I _{GSS(R)}	V _{DS} =0V, V _{GS} =-30V	-	-	-100	nA
On characteristics						
Drain to Source On-Resistance	R _{DS(ON)}	V _{GS} =10V,I _D =3.5A	-	1.12	1.35	Ω
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} ,I _D =250μA	2	-	4	V
Dynamic characteristics						
Gate capacitance	R _g	V _{GS} =0V,V _{DS} =0V ,f=1.0MHz	-	2.2	-	Ω
Forward Transconductance	g _{fs}	V _{DS} =10V,I _D =3A	-	6	-	S
Input Capacitance	C _{iss}	V _{DS} =25V,V _{GS} =0V f=1.0MHz	-	1200	-	pF
Output Capacitance	C _{oss}		-	80	-	pF
Reverse Transfer Capacitance	C _{rss}		-	1.9	-	pF
Resistive Switching Characteristics						
Turn-on Delay Time	t _{d(ON)}	I _D =7A, V _{DS} =325V R _G =10Ω ,V _{GS} =10V	-	21	-	ns
Rise Time	t _r		-	17	-	ns
Turn-off Delay Time	t _{d(OFF)}		-	38	-	ns
Fall Time	t _f		-	16	-	ns
Total Gate Charge	Q _g	I _D =7A, V _{DS} =325V V _{GS} =10V	-	23	-	nC
Gate to Source Charge	Q _{gs}		-	6.2	-	nC
Gate to Drain("Miller") Charge	Q _{gd}		-	11	-	nC
Source-Drain Diode Characteristics						
Continuous Source Current(Body Diode)	I _S		-	-	7	A
Maximum Pulsed Current(Body Diode)	I _{SM}		-	-	28	A
Diode Forward Voltage	V _{SD}	I _{SD} =1A, V _{GS} =0V	-	0.72	1.2	V
Reverse Recovery Time	t _{rr}	I _{SD} =7A, T _J =25℃ dI/dt=100A/μs	-	765	-	ns
Reverse Recovery Charge	Q _{rr}		-	4750	-	nC

TYPICAL CHARACTERISTICS

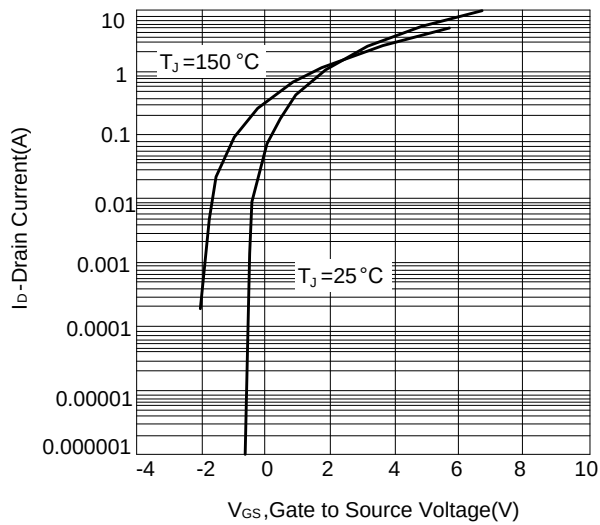


Figure 1: Transfer Characteristics

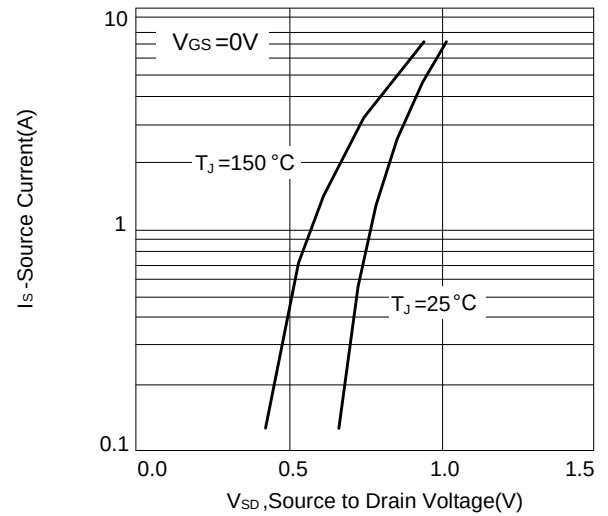


Figure 2: Body Diode Characteristics

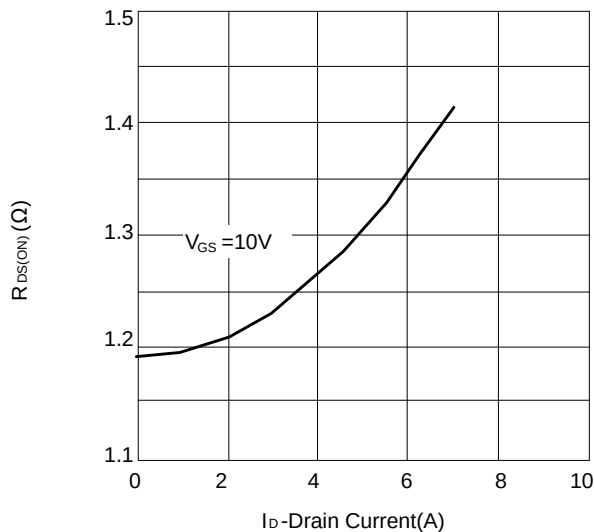


Figure 3: Drain to Source On-Resistance vs. Drain Current

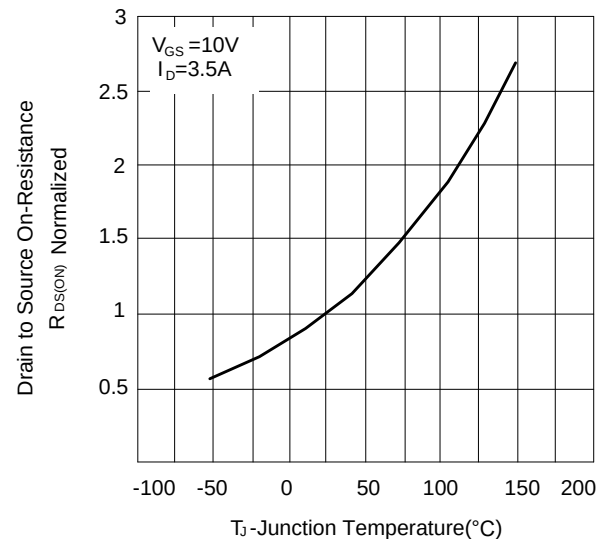


Figure 4: Drain to Source On-Resistance vs. Junction Temperature

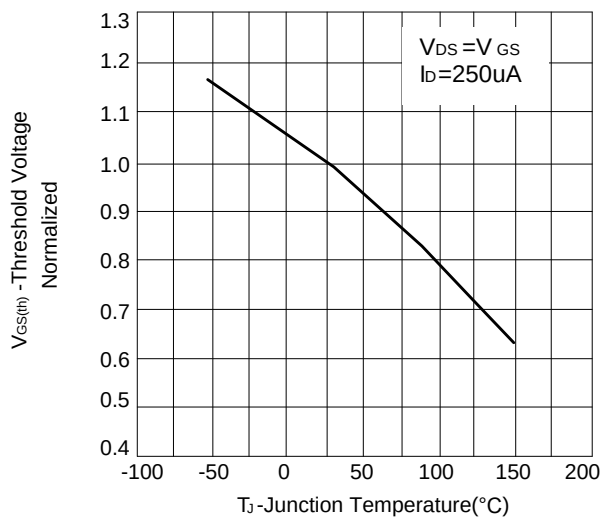


Figure 5: Threshold Voltage vs. Junction Temperature

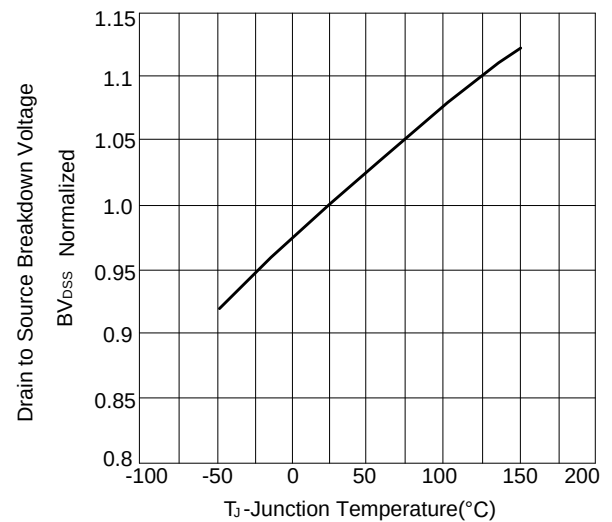


Figure 6: Breakdown Voltage vs. Junction Temperature

■ TYPICAL CHARACTERISTICS(Cont.)

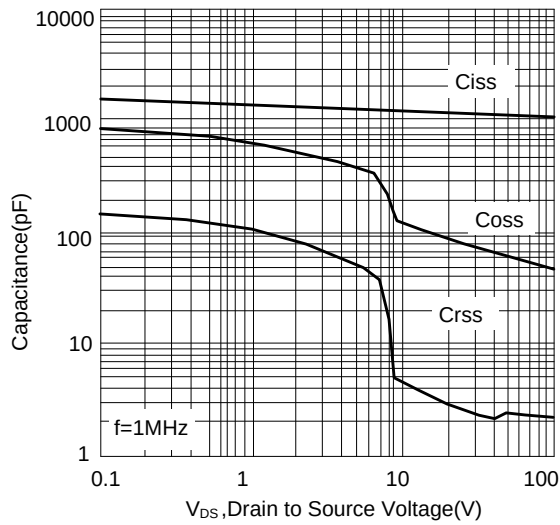


Figure 7: Capacitance vs Drain to Source Voltage

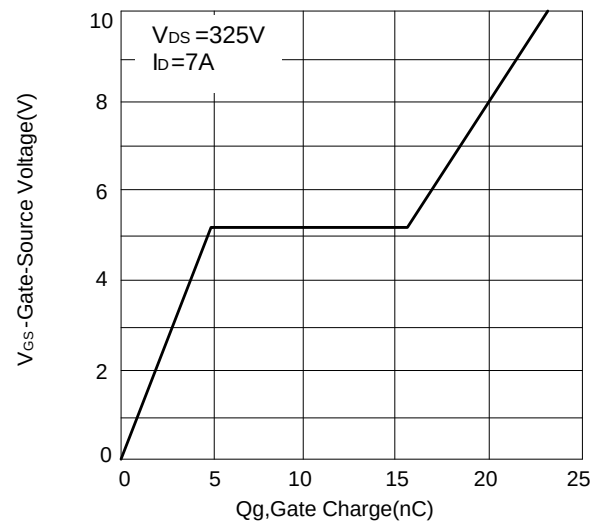


Figure 8: Gate Charge vs Gate to Source Voltage

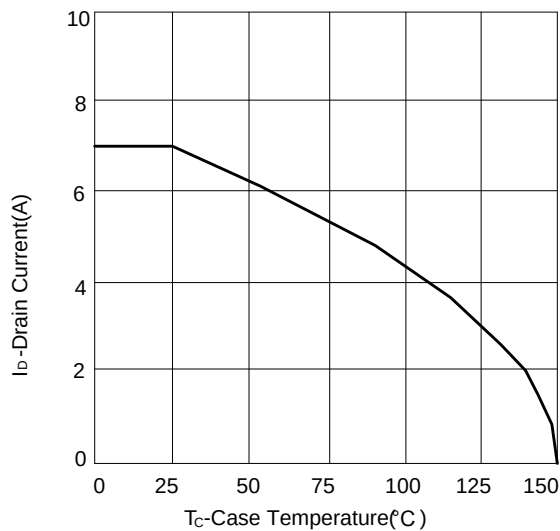


Figure 9: Continuous Drain vs Case Temperature

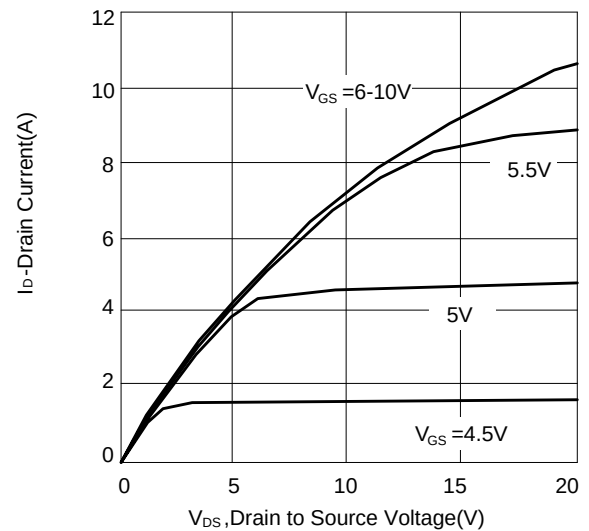


Figure 10: Output Characteristics

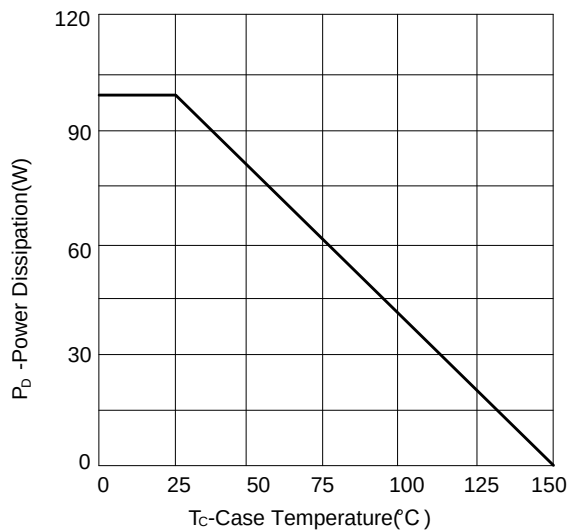


Figure 11: Power Dissipation vs Case Temperature

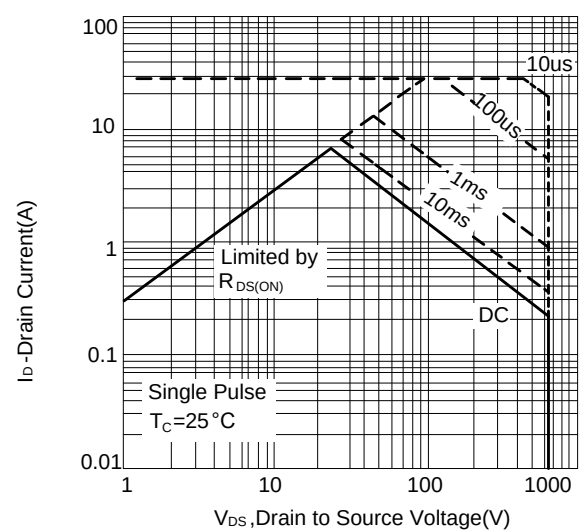


Figure 12: Safe operating Area

■ TO-252 PACKAGE OUTLINE DIMENSIONS

