P-Channel Enhancement Mode MOSFET

Features

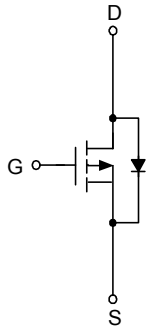
- -30V/-4.3A ,
  $R_{DS(ON)}$= 50mΩ (typ.) @ V_{GS}=-10V
  $R_{DS(ON)}$= 60mΩ (typ.) @ V_{GS}=-4.5V
  $R_{DS(ON)}$= 72mΩ (typ.) @ V_{GS}=-2.5V
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

Pin Description

Top View of SOT23-3L

Applications

- Power Management in Notebook Computer, Portable Equipment and Battery Powered Systems.



P-Channel MOSFET

Absolute Maximum Ratings (T_A = 25°C unless otherwise noted)

Symbol	Parameter		Rating	Unit
V_{DSS}	Drain-Source Voltage		-30	V
V_{GSS}	Gate-Source Voltage		±12	
I_D*	Continuous Drain Current	T_A=25°C	-4.3	A
		T_A=70°C	-3.5	
I_{DM}*	300μs Pulsed Drain Current	T_A=25°C	-17.3	
		T_A=70°C	-13.8	
I_S*	Diode Continuous Forward Current		-1	
T_J	Maximum Junction Temperature		150	°C
T_{STG}	Storage Temperature Range		-55 to 150	
P_D*	Maximum Power Dissipation	T_A=25°C	1.56	W
		T_A=70°C	1.0	
$R_{\theta JA}$*	Thermal Resistance-Junction to Ambient	t ≤ 10s	80	°C/W
		Steady state	120	
$R_{\theta JL}$	Thermal Resistance-Junction to Lead	Steady state	52	

Note : *Surface Mounted on 1in² pad area, t ≤ 10sec.

Electrical Characteristics (T_A = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_{DS}=-250μA	-30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}=-24V, V_{GS}=0V	-	-	-1	μA
		T_J=85°C	-	-	-3 0	
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_{DS}=-250μA	-0.5	-0.9	-1.3	V
I_{GSS}	Gate Leakage Current	V_{GS}=±12V, V_{DS}=0V	-	-	±100	nA
$R_{DS(ON)}$ [a]	Drain-Source On-State Resistance	V_{GS}=-10V, I_{DS}=-4.3A	-	50	55	mΩ
		V_{GS}=-4.5V, I_{DS}=-3.5A	-	60	65	
		V_{GS}=-2.5V, I_{DS}=-2.7A	-	72	85	
V_{SD} [a]	Diode Forward Voltage	I_{SD}=-1A, V_{GS}=0V	-	-0.7	-1	V
Gate Charge Characteristics [b]						
Q_g	Total Gate Charge	V_{DS}=-15V, V_{GS}=-10V, I_{DS}=-4.3A	-	13.6	-	nC
Q_{gs}	Gate-Source Charge		-	1.2	-	
Q_{gd}	Gate-Drain Charge		-	2.0	-	

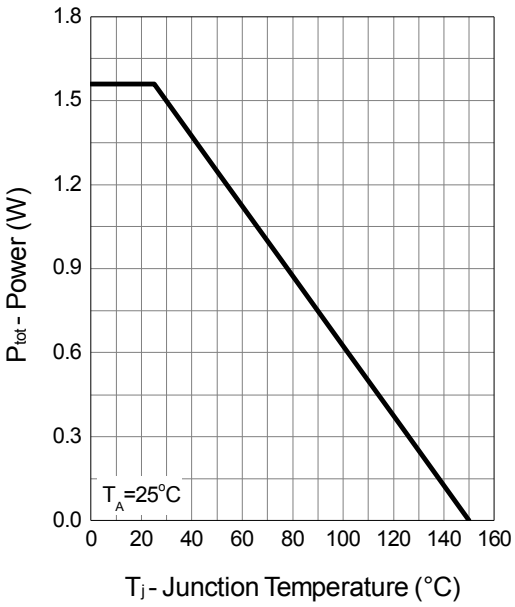
Electrical Characteristics (Cont.) (T_A = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Dynamic Characteristics [b]						
R_G	Gate Resistance	V_{GS}=0V,V_{DS}=0V,F=1MHz	-	3.6	-	Ω
C_{iss}	Input Capacitance	V_{GS}=0V, V_{DS}=-15V, Frequency=1.0MHz	-	573	-	pF
C_{oss}	Output Capacitance		-	74	-	
C_{rss}	Reverse Transfer Capacitance		-	53	-	
$t_{d(ON)}$	Turn-on Delay Time	V_{DD}=-15V, R_L=10Ω, I_{DS}=-1A, V_{GEN}=-10V, R_G=6Ω	-	6.9	-	ns
t_r	Turn-on Rise Time		-	12.3	-	
$t_{d(OFF)}$	Turn-off Delay Time		-	25	-	
t_f	Turn-off Fall Time		-	13	-	
t_{rr}	Reverse Recovery Time	I_F=-4.3A, dI_{SD}/dt =100A/μs	-	16	-	ns
Q_{rr}	Reverse Recovery Charge		-	11	-	nC

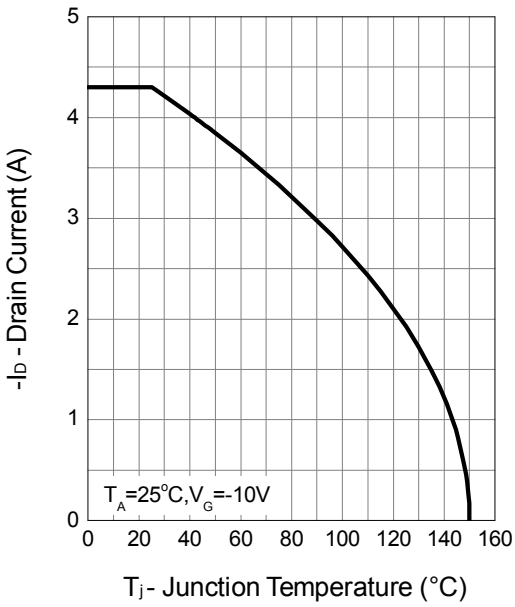
Note a : Pulse test ; pulse width≤300μs, duty cycle≤2%.
Note b : Guaranteed by design, not subject to production testing.

Typical Operating Characteristics

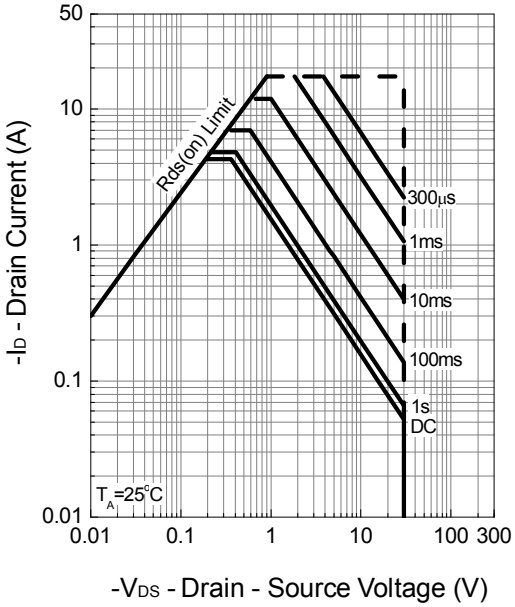
Power Dissipation



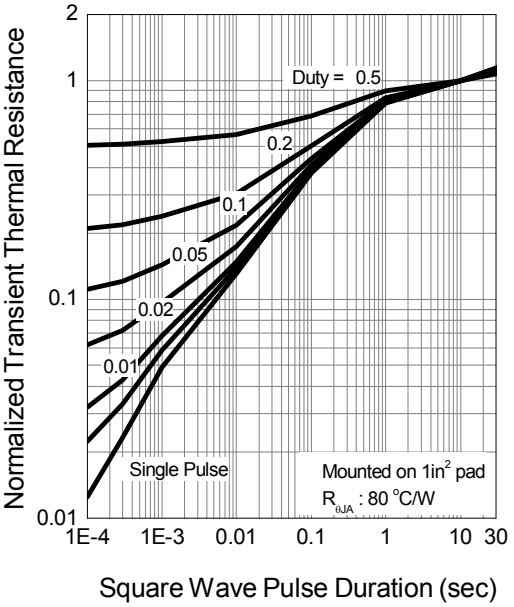
Drain Current



Safe Operation Area

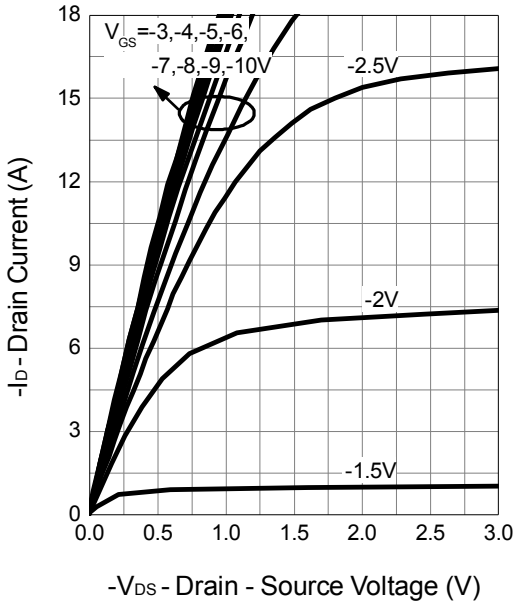


Thermal Transient Impedance

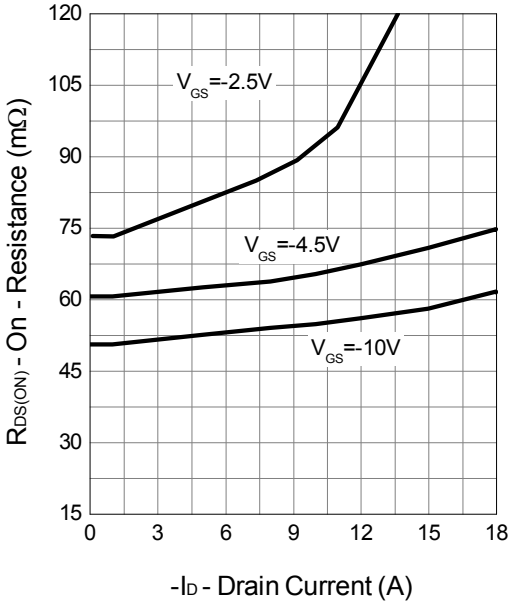


Typical Operating Characteristics (Cont.)

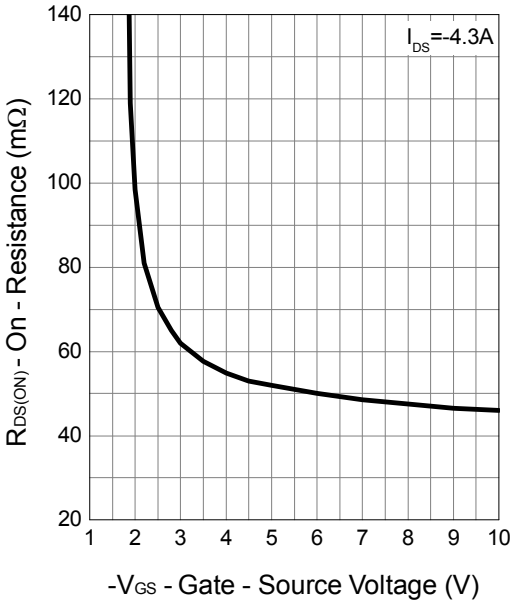
Output Characteristics



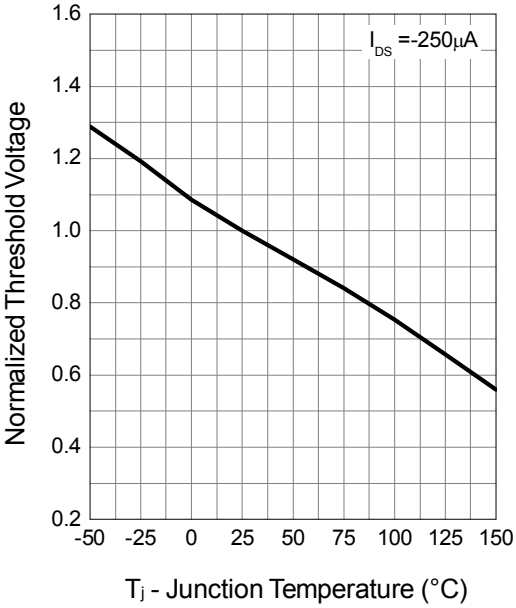
Drain-Source On Resistance



Gate-Source On Resistance

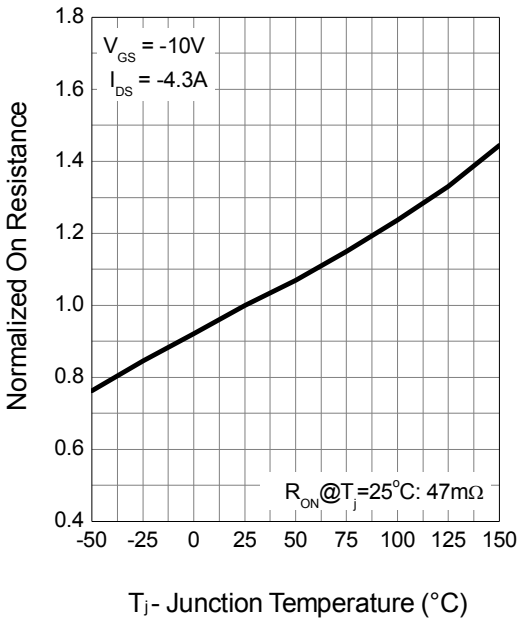


Gate Threshold Voltage

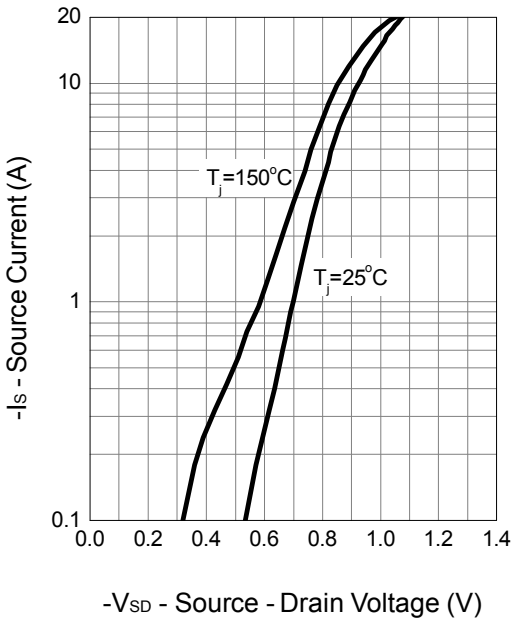


Typical Operating Characteristics (Cont.)

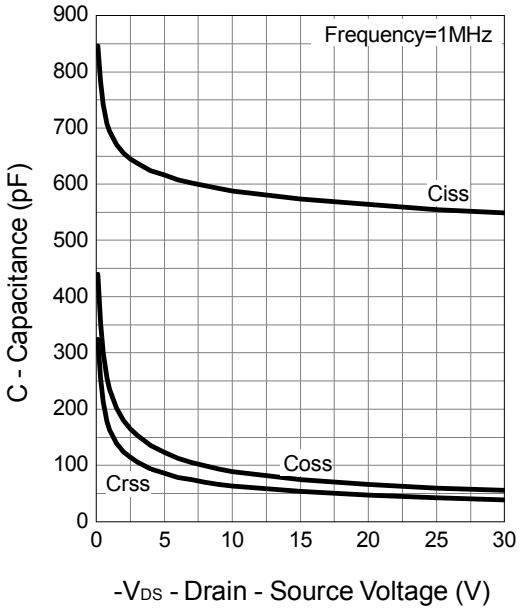
Drain-Source On Resistance



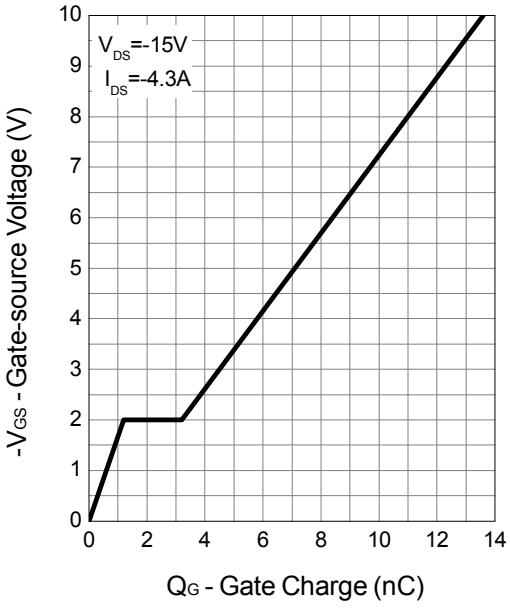
Source-Drain Diode Forward



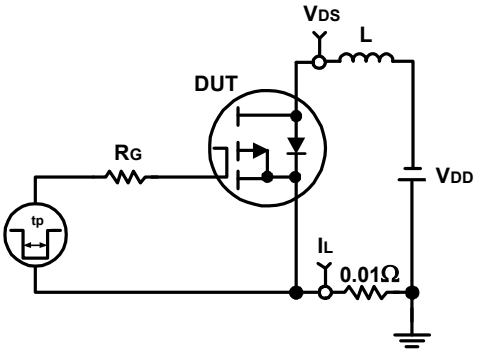
Capacitance

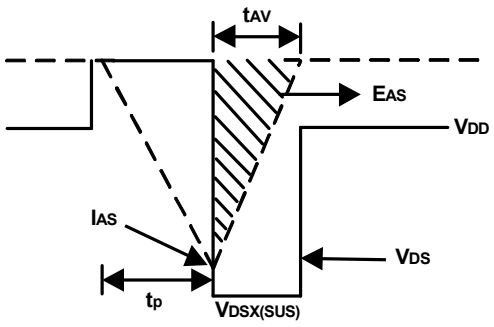


Gate Charge

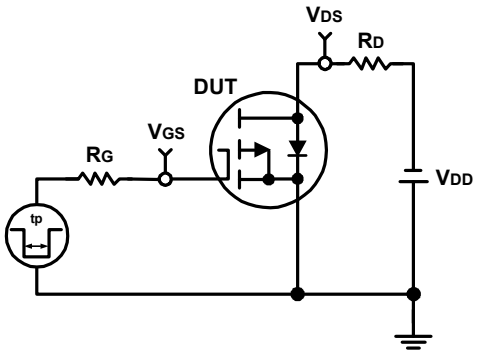


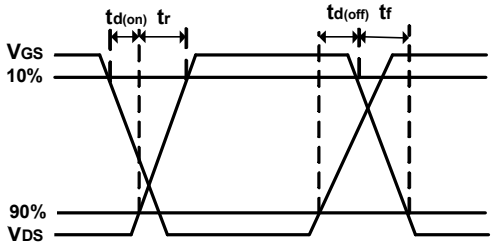
Avalanche Test Circuit and Waveforms





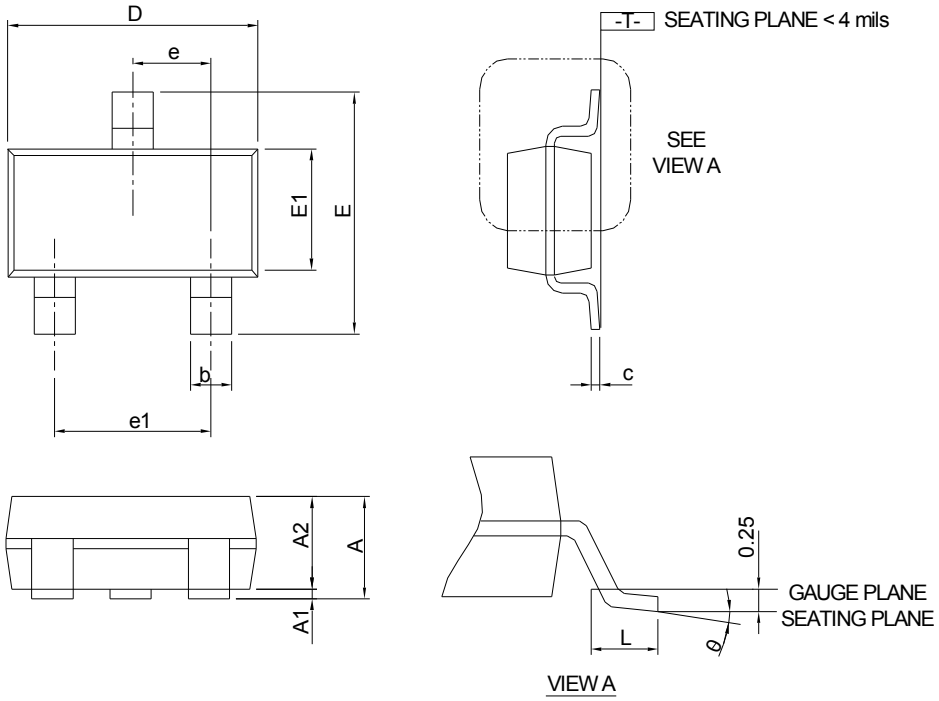
Switching Time Test Circuit and Waveforms





Package Information

SOT23-3L



SYMBOL	SOT23-3L			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	-	1.20	-	0.047
A1	0.00	0.08	0.000	0.003
A2	0.90	1.12	0.035	0.044
b	0.30	0.50	0.012	0.020
c	0.08	0.22	0.003	0.009
D	2.70	3.10	0.106	0.122
E	2.60	3.00	0.102	0.118
E1	1.40	1.80	0.055	0.071
e	0.95 BSC		0.037 BSC	
e1	1.90 BSC		0.075 BSC	
L	0.30	0.60	0.012	0.024
θ	0°	8°	0°	8°

Note : Dimension D and E1 do not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 10 mil per side.

RECOMMENDED LAND PATTERN