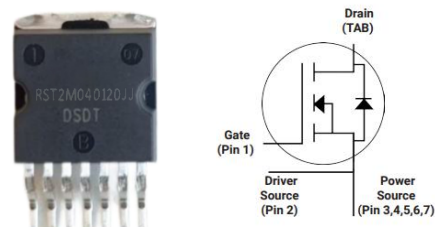


Features

- High speed switching
- Very low switching losses
- Fully controllable dv/dt
- High blocking voltage with low on-resistance
- Fast intrinsic diode with low reverse recovery (Q_{rr})
- Temperature independent turn-off switching losses
- Halogen free, RoHS compliant



Benefits

- Cooling effort reduction
- Efficiency improvement
- Reduced cooling requirements
- Increased power density
- Increased system switching frequency

TO-263-7L



Applications

- On-board charger/PFC
- EV battery chargers
- Booster/DC-DC converter
- Switch mode power supplies

Table 1 Key performance and package parameters

Type	V_{DS}	I_{DS} ($T_C = 25^\circ C$, $R_{th(j-c),max}$)	$R_{DS(on), typ}$ ($V_{GS} = 18V$, $I_D = 32A$, $T_J = 25^\circ C$)	$T_{J,max}$	Marking	Package
RST2M040120JJ	1200V	75A	35mΩ	175°C	RST2M040120JJ	TO-263-7L

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1、Maximum ratings

Table 2 Maximum rating (Tc = 25°C unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
$V_{DS,max}$	Drain source voltage	1200	V	$V_{GS} = 0V, I_D = 100\mu A$	
$V_{GS,max}$	Gate source voltage	-8 / +22	V	Absolute maximum values	Note1
V_{GSop}	Gate source voltage	-4 / +18	V	Recommended operational values	
I_D	Continuous drain current	75	A	$V_{GS} = 18V, T_C = 25^\circ C$	Fig.19
		53		$V_{GS} = 18V, T_C = 100^\circ C$	
$I_{D(pulse)}$	Pulsed drain current	150	A	Pulse width t_p limited by $T_{J,max}$	Fig.22
P_D	Power dissipation	366	W	$T_C = 25^\circ C, T_J = 175^\circ C$	Fig.20
T_J, T_{stg}	Operating Junction and storage temperature	-55 to +175	°C		
T_L	Soldering temperature	260	°C	1.6mm (0.063”) from case for 10s	
T_M	Mounting torque	1	Nm	M3 or 6-32 screw	
		8.8	lbf-in		

Note 1: when using MOSFET Body Diode $V_{GS,max} = -4 / +22V$

2、Thermal characteristics

Table 3 Thermal characteristics

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
$R_{th(j-c)}$	Thermal resistance from junction to case	-	0.33	0.41	°C/W	-	Fig.21

3、Electrical characteristics

3.1 Static characteristics

Table 4 Static characteristics (Tc = 25°C unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
$V_{(BR)DSS}$	Drain-source breakdown voltage	1200	-	-	V	$V_{GS} = 0V, I_D = 100\mu A$	
$V_{GS(th)}$	Gate threshold voltage	2.3	2.8	3.6	V	$V_{DS} = V_{GS}, I_D = 9mA$	Fig.11
		-	2.1	-	V	$V_{DS} = V_{GS}, I_D = 9mA$ $T_J = 175^\circ C$	
I_{DSS}	Zero gate voltage drain current	-	1	10	μA	$V_{DS} = 1200V, V_{GS} = 0V$	
I_{GSS}	Gate source leakage current	-	-	100	nA	$V_{GS} = 18V, V_{DS} = 0V$	
$R_{DS(on)}$	Current drain-source on-state resistance	-	40	59	m Ω	$V_{GS} = 15V, I_D = 32A$	Fig.4,5,6
			67			$V_{GS} = 15V, I_D = 32A,$ $T_J = 175^\circ C$	
			35	48		$V_{GS} = 18V, I_D = 32A$	
		-	64	-		$V_{GS} = 18V, I_D = 32A,$ $T_J = 175^\circ C$	
g_{fs}	Transconductance	-	22	-	S	$V_{DS} = 20V, I_D = 32A$	Fig.7
		-	20	-		$V_{DS} = 20V, I_D = 32A,$ $T_J = 175^\circ C$	
$R_{g,int}$	Intenal gate resistance	-	2.0	-	Ω	$V_{AC} = 25mV, f = 1MHz,$ open drain	

3.2 Dynamic characteristics

Table 5 Dynamic characteristics (Tc = 25°C unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
C_{iss}	Input capacitance	-	1820	-	pF	$V_{DS} = 1000V, V_{GS} = 0V$ $T_J = 25^\circ C, V_{AC} = 25mV$ $f = 100kHz$	Fig.17,18
C_{oss}	Output capacitance	-	106	-			
C_{rss}	Reverse capacitance	-	4.5	-			
E_{oss}	Coss stored energy	-	59	-	μJ		Fig.16

Q_{gs}	Gate source charge	-	22	-	nC	$V_{DS} = 800V, V_{GS} = -4/+18V$ $I_D = 32A$	Fig.12
Q_{gd}	Gate drain charge	-	18	-			
Q_g	Gate charge	-	60	-			

3.3 Switching characteristics

Table 6 Dynamic characteristics($T_c = 25^\circ C$ unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
E _{on}	Turn on switching energy	-	257	-	μJ	V _{DS} = 800V, V _{GS} = -4/+18V I _D = 32A, R _g = 2.5Ω L = 120μH	Fig.26
E _{off}	Turn off switching energy	-	50	-			
t _{d (on)}	Turn on delay time	-	15	-	ns		Fig.27
t _r	Rise time	-	9	-			
t _{d (off)}	Turn off delay time	-	23	-			
t _f	Fall time	-	8	-			

Table 7 Body diode characteristics

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
V_{SD}	Diode forward voltage	-	3.7	-	V	$V_{GS} = -4V, I_{SD} = 16A$	Fig.8,9, 10
		-	3.3	-	V	$V_{GS} = -4V, I_{SD} = 16A$ $T_J = 175^\circ C$	
I_S	Continuous diode forward current	-	70	-	A	$V_{GS} = -4V, T_c = 25^\circ C$	Note2
t_{rr}	Reverse recovery time	-	29.5	-	ns	$V_R = 800V, V_{GS} = -4V$ $I_{SD} = 32A$ $di/dt = 2944A/\mu s$ $T_J = 175^\circ C$	
Q_{rr}	Reverse recovery charge	-	237	-	nC		
I_{rrm}	Peak reverse recovery current	-	17.5	-	A		

Note 2: When using SiC Body Diode the maximum recommended $V_{GS} = -4V$

4、Electrical characteristic diagrams

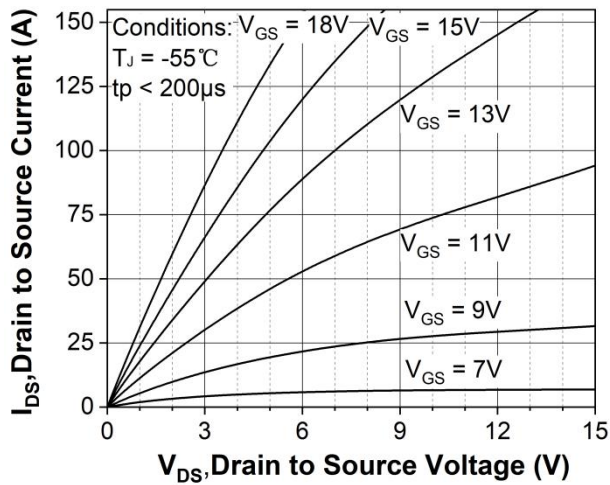


Figure 1. Output characteristics $T_J = -55\text{ }^{\circ}\text{C}$

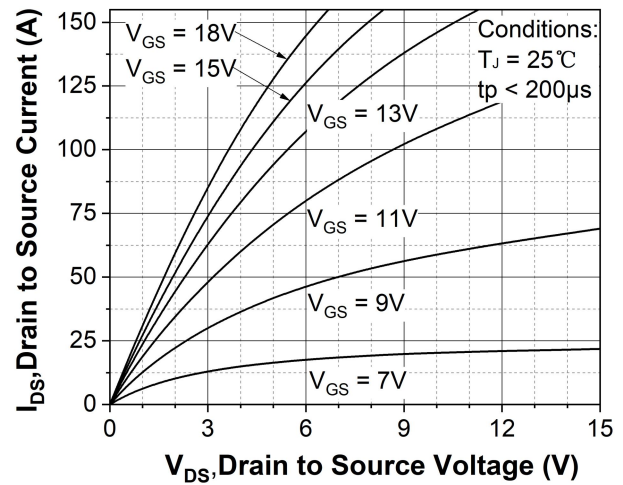


Figure 2. Output characteristics $T_J = 25\text{ }^{\circ}\text{C}$

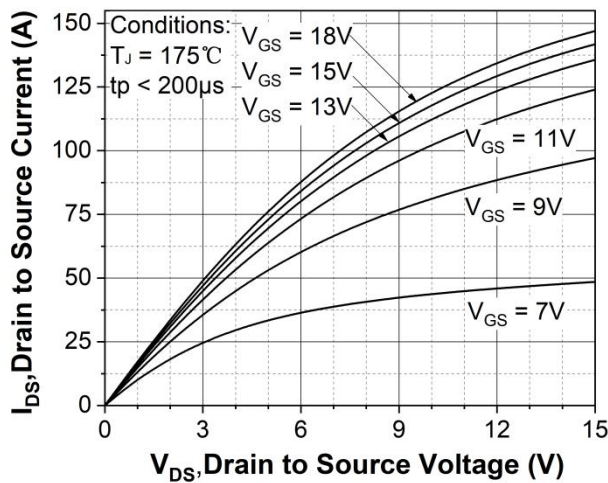


Figure 3. Output characteristics $T_J = 175\text{ }^{\circ}\text{C}$

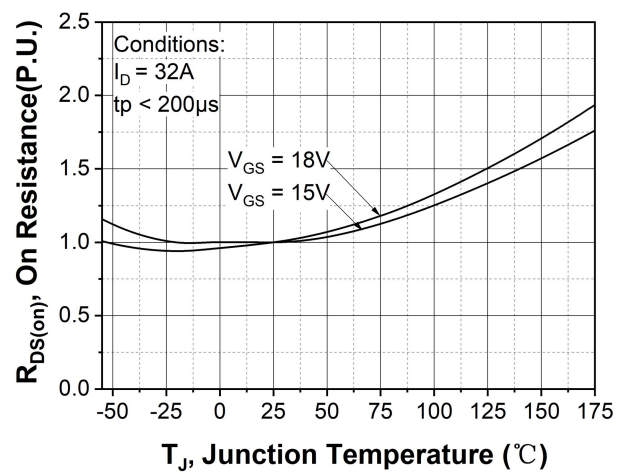


Figure 4. Normalized on-resistance vs. temperature

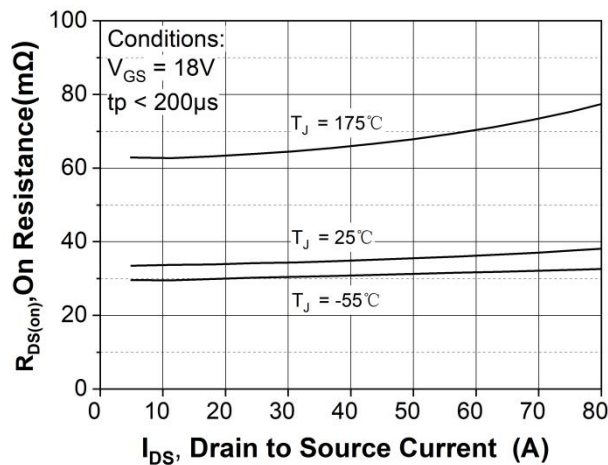


Figure 5. On-resistance vs. drain current
for various temperatures

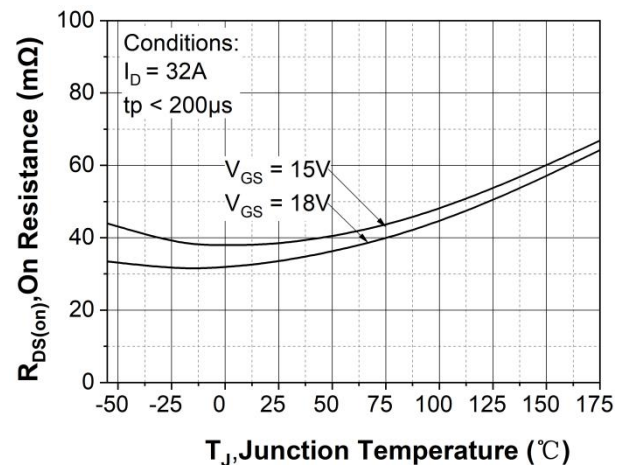


Figure 6. On-resistance vs. temperature

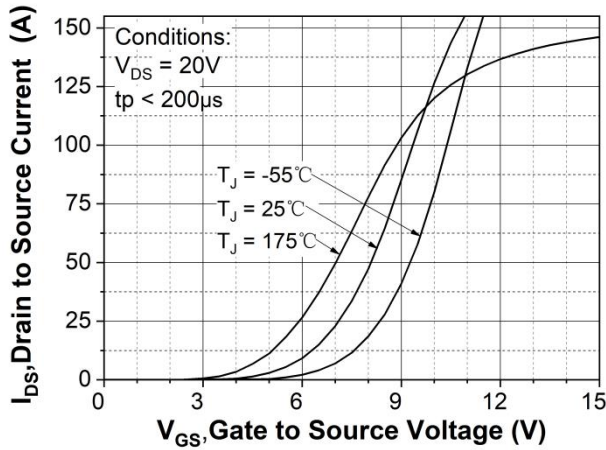


Figure 7. Transfer characteristic for various junction temperatures

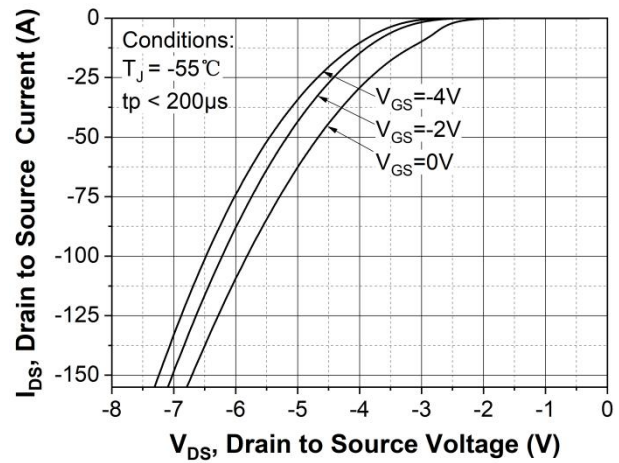


Figure 8. Body diode characteristic at $T_J = -55^\circ\text{C}$

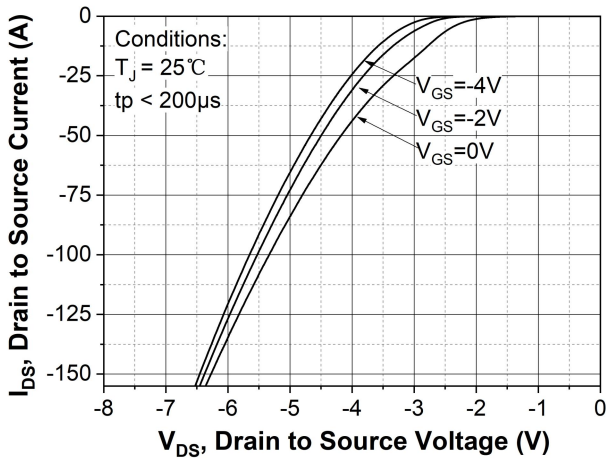


Figure 9. Body diode characteristic at $T_J = 25^\circ\text{C}$

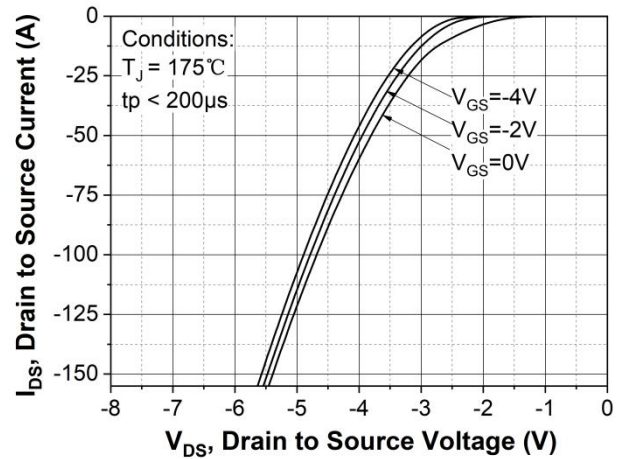


Figure 10. Body diode characteristic at $T_J = 175^\circ\text{C}$

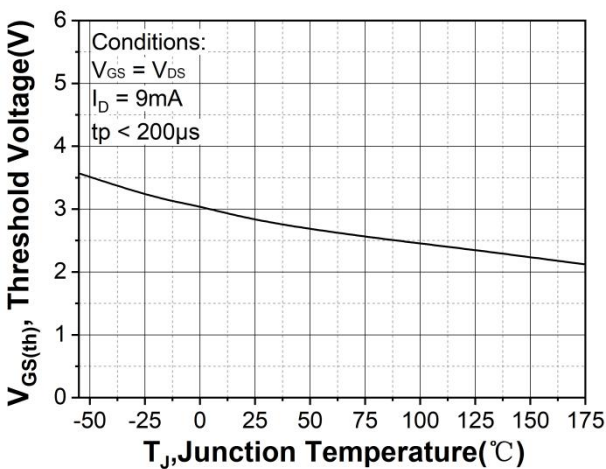


Figure 11. Threshold voltage vs. temperature

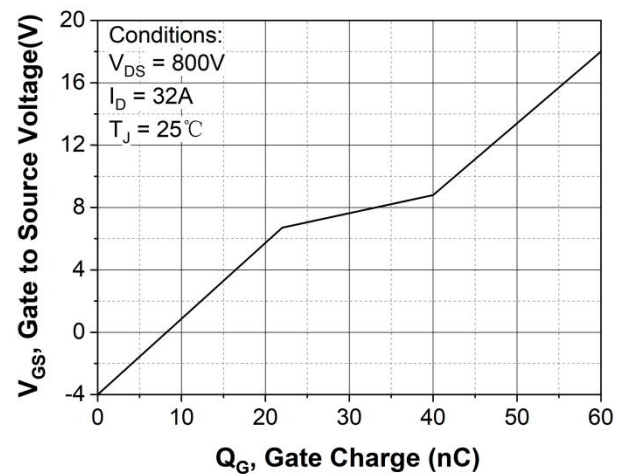


Figure 12. Gate charge characteristic

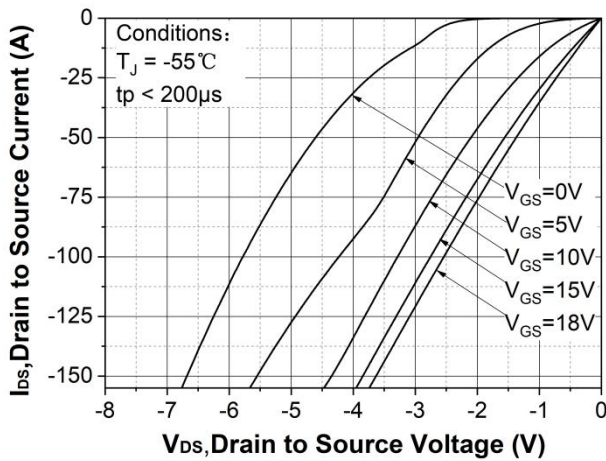


Figure 13. 3rd quadrant characteristic at $T_J = -55\text{ }^{\circ}\text{C}$

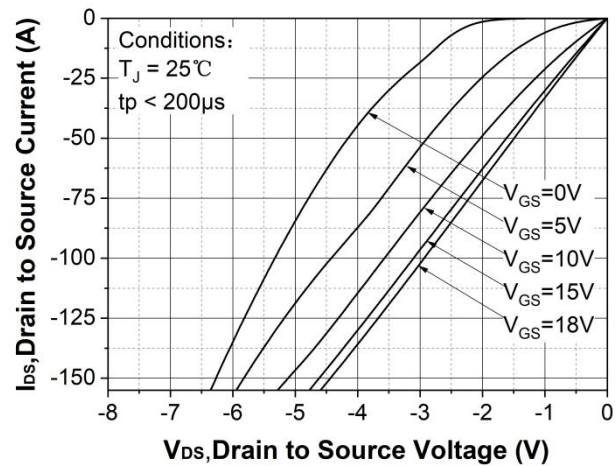


Figure 14. 3rd quadrant characteristic at $T_J = 25\text{ }^{\circ}\text{C}$

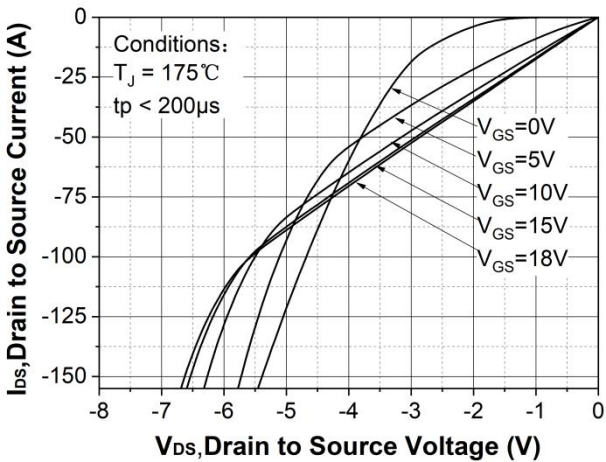


Figure 15. 3rd quadrant characteristic at $T_J = 175\text{ }^{\circ}\text{C}$

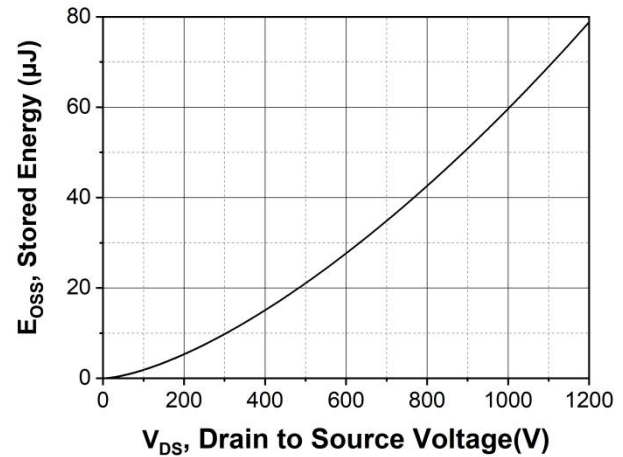


Figure 16. Output capacitor stored energy

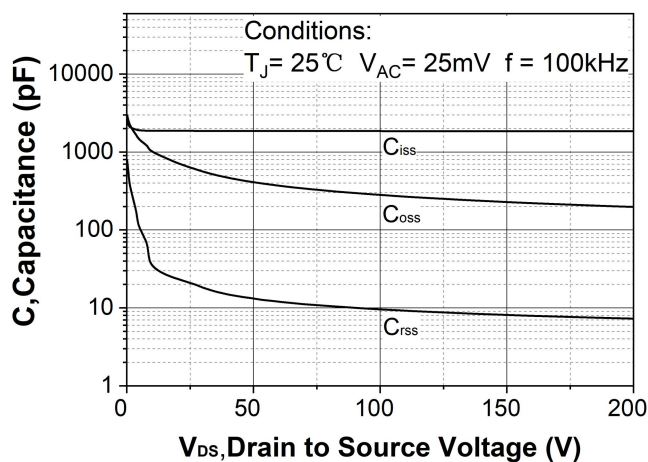


Figure 17. Capacitances vs. drain-source voltage (0 - 200V)

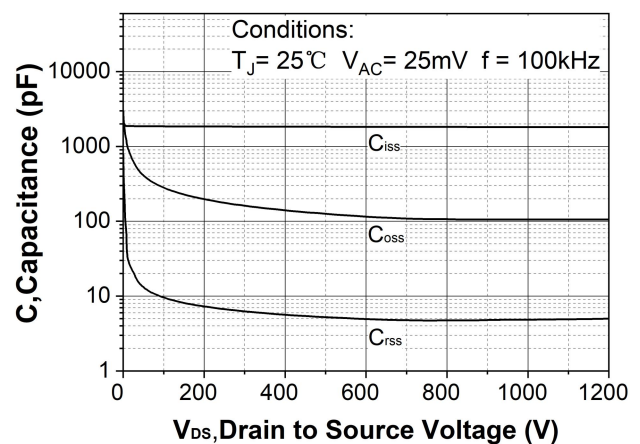


Figure 18. Capacitances vs. drain-source voltage (0 - 1200V)

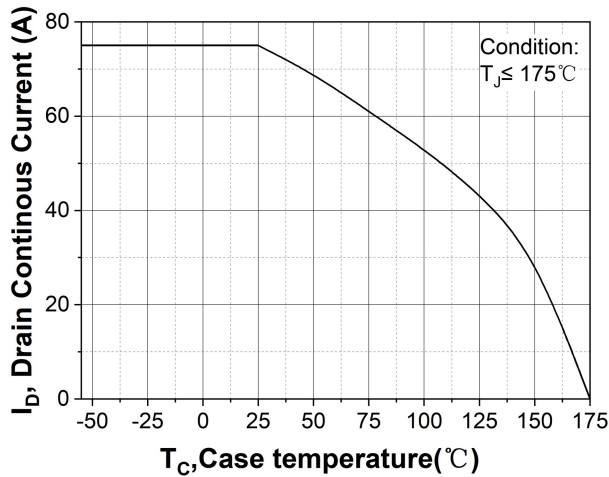


Figure 19. Continuous drain current derating vs. case temperature

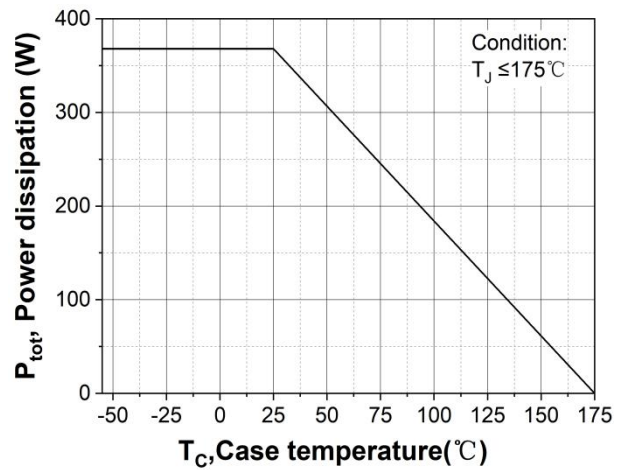


Figure 20. Maximum power dissipation derating vs. case temperature

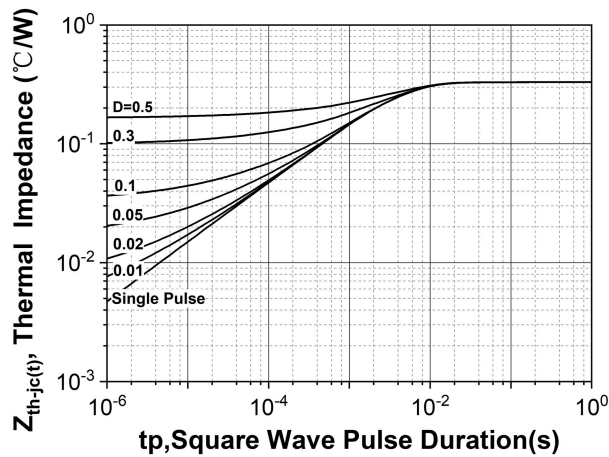


Figure 21. Transient thermal impedance (junction - case)

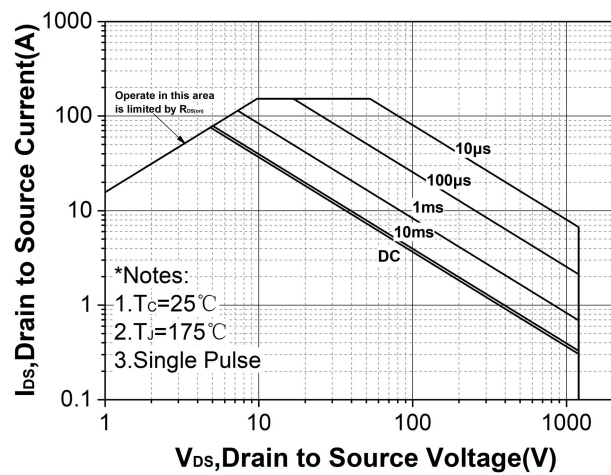


Figure 22. Safe operating area

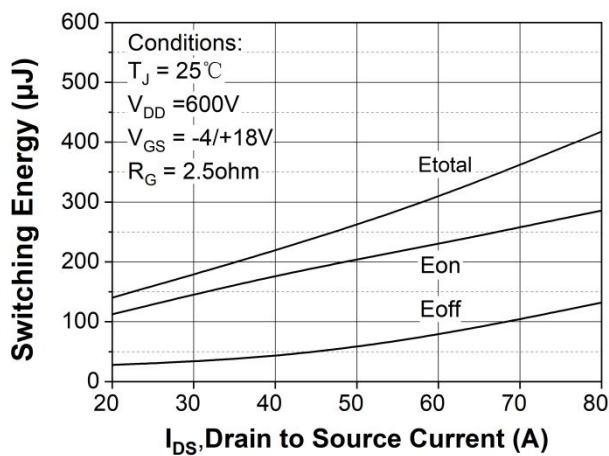


Figure 23. Clamped Inductive switching energy vs. drain current ($V_{DD} = 600V$)

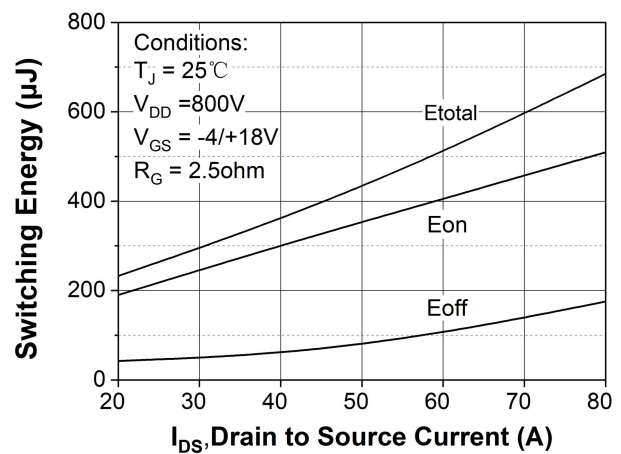


Figure 24. Clamped inductive switching energy vs. drain current ($V_{DD} = 800V$)

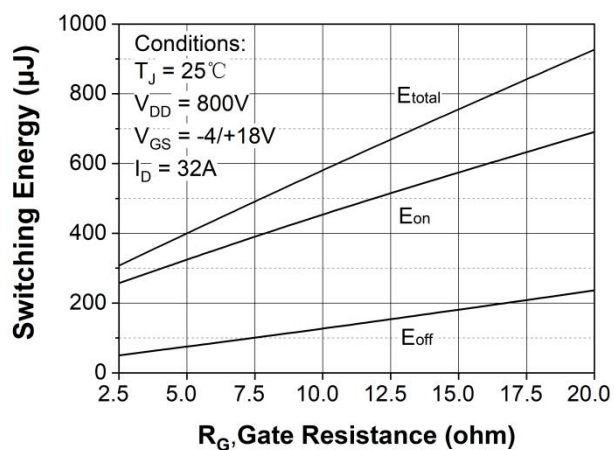


Figure 25. Clamped inductive switching energy vs. $R_G(\text{ext})$

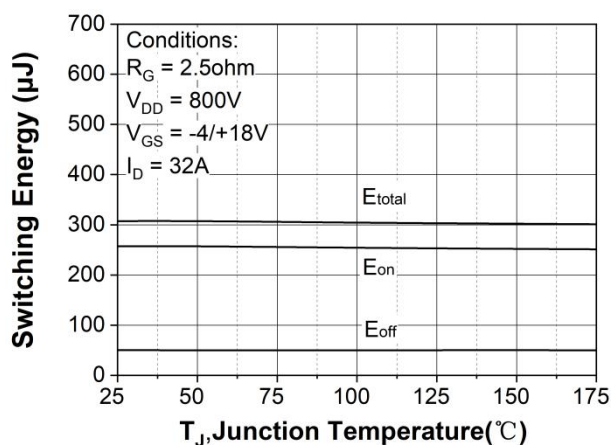


Figure 26. Clamped inductive switching energy vs. temperature

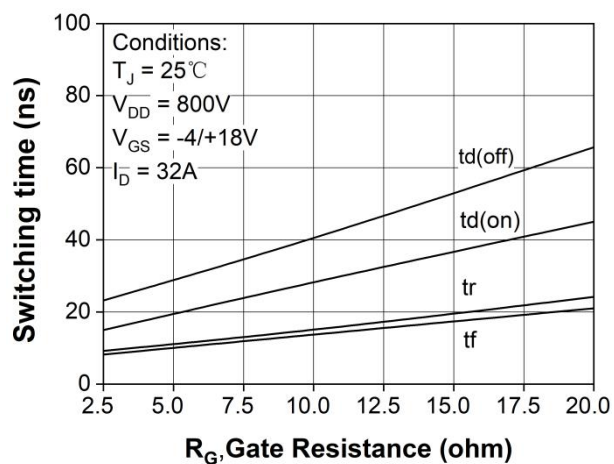
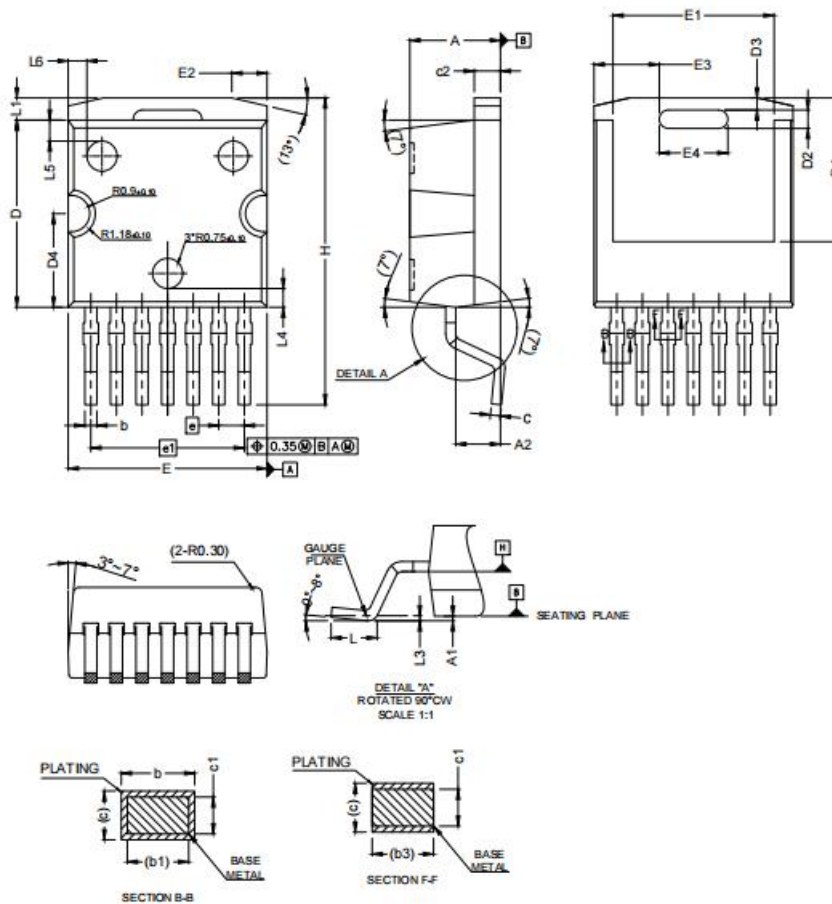


Figure 27. Switching times vs. $R_G(\text{ext})$

5、 Package drawing (TO-263-7L-A)



SYMBOL	MIN	MAX	SYMBOL	MIN	MAX
A	4.30	4.70	L	1.78	2.79
A1	-	0.25	L1	-	1.60
A2	2.02	2.42	L3	0.25BSC	
b	0.50	0.70	L4	0.93BSC	
b1	0.50	0.65	L5	1.04BSC	
b3	0.60	0.75	L6	0.93BSC	
c	0.45	0.60	H	14.61	15.88
c1	0.45	0.55			
c2	1.25	1.40			
D	9.10	9.50			
D1	6.86	7.42			
D2	0.72	1.12			
D3	0.40	0.80			
D4	4.45	4.85			
E	9.68	10.08			
E1	7.70	8.30			
E2	1.55	1.95			
E3	3.04	3.44			
E4	3.21	3.61			
e	1.27 BSC				
e1	7.62 BSC				

6、 Test conditions

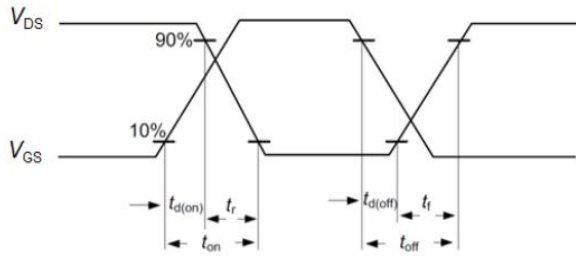


Figure A. Definition of switching times

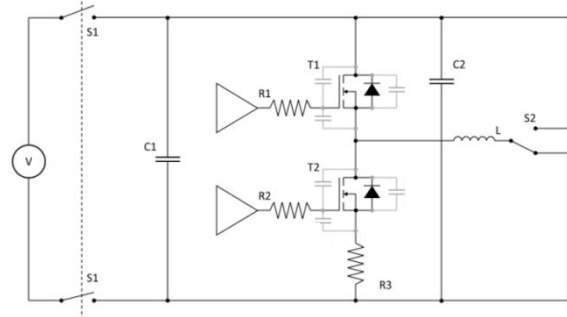


Figure B. Dynamic test circuit

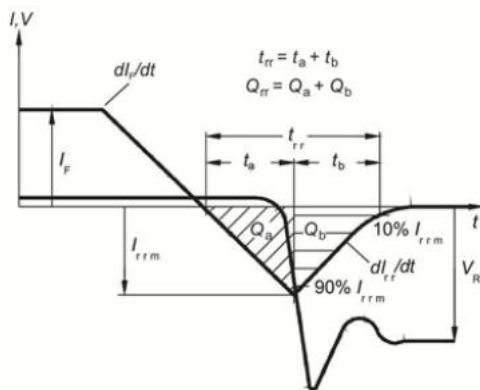


Figure C. Definition of body diode
switching characteristics

Revision history

Document version	Date of release	Description of changes
V01_00	2024-11-04	--

Attention

RoHS compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/ EC (RoHS2), as implemented January 2, 2013.