Features

- Uses advanced SGT technology
- Extremely low on-resistance R_{DS}(on)
- Excellent gate charge x R_{DS}(on) product(FOM)

Application

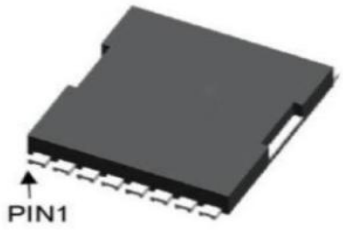
- Motor control and drive
- Battery management
- DC/DC

Product Summary

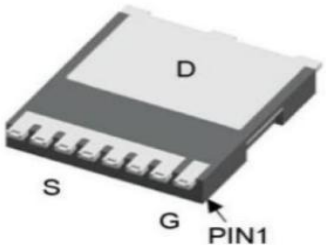
Package	TOLL
V_{DS}	120V
$R_{DS(on)@VGS=10V}$	1.17mΩ
I_D	330A

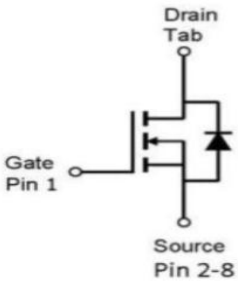
Package Marking

TOLL Top View



TOLL Bottom View





Type	Package	Marking	Reel Size	Tape Width	Packing	Qty
RST016N12ST10	TOLL	LR016N12ST10	330*28.5mm	24mm	Reel&Tape	2000

Maximum Ratings (TC=25℃unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	120	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous drain current T_c=25℃（Silicon limit） T_c=25℃（package limit） T_c=100℃（Silicon limit）	I_D	 407 330 258	A
Pulsed Drain Current	I_{DM}	1320	
Maximum Power Dissipation	P_d	431	W
Single pulse avalanche energy	E_{AS}	2601	mJ
Operating Junction and Storage Temperature Range	T_J,T_{STG}	-55 To 150	℃

Thermal Characteristic

Thermal Resistance,Junction-to-Case	R_{thJC}	0.29	℃/W
Thermal Resistance,Junction-to-Ambient	R_{thJA}	40	

Electrical Characteristics (T_C=25℃unless otherwise noted)

Parameter	Symbol	Condition	min	typ	max	Unit
Static Characteristic						
Drain-source breakdown voltage	$V_{(BR)DSS}$	V_{GS}=0V, I_D=250uA	119	121	-	V
Gate threshold voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250uA	2.2	3.0	4.0	
Zero gate voltage drain current	I_{DSS}	V_{DS}=110V,V_{GS}=0V	-	-	1	μA
Gate-source leakage current	I_{GSS}	V_{GS}=±20V,V_{DS}=0V	-	-	±100	nA
Drain-source on-state resistance	$R_{DS(on)}$	I_D=40A,V_{GS}=10V	-	1.17	1.6	mΩ
Forward transconductance	g_{fs}	V_{DS}=5V, I_D=100A	-	380	-	S
Dynamic Characteristic						
Input Capacitance	C_{iss}	V_{DS}=60V,V_{GS}=0V, f=1MHz	-	13427	-	pF
Output Capacitance	C_{oss}		-	2786	-	
Reverse Transfer Capacitance	C_{rss}		-	108	-	
Gate resistance	Rg	f=1MHz	-	1.3	-	Ω
Gate Total Charge	Q_G	V_{DS}=50V,I_D=100A, V_{GS}=10V	-	189	-	nC
Gate-Source charge	Q_{gs}		-	70	-	
Gate-Drain charge	Q_{gd}		-	20	-	
Turn-on delay time	$t_{d(on)}$	V_{GS}=10V, V_{DS}=50V, R_G=1.6Ω	-	131	-	ns
Rise time	t_r		-	143	-	
Turn-off delay time	$t_{d(off)}$		-	71	-	
Fall time	t_f		-	70	-	

Body Diode Characteristic

Diode Forward Voltage	V_{SD}	V_{GS}=0V,I_F=40A	-	0.85	1.1	v
Reverse Recovery Time	t_{rr}	I_F =100A di/dt = 100A/μs	-	100	-	ns
Reverse Recovery Charge	Q_{rr}		-	280	-	nC

Typical Performance Characteristics

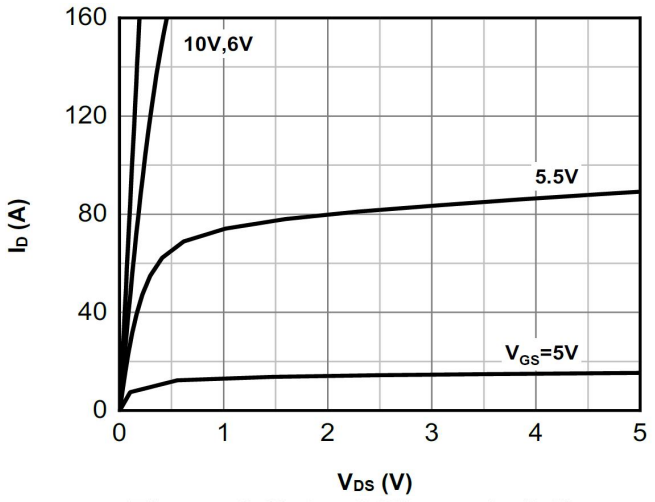


Figure 1 Output Characteristics

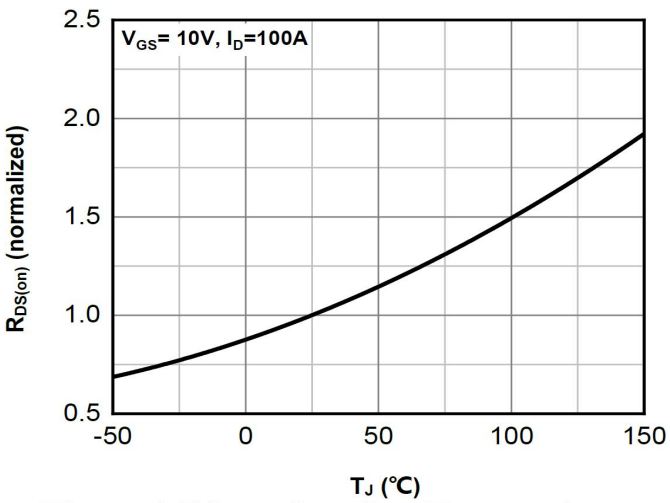


Figure 4 Rdson-Junction Temperature

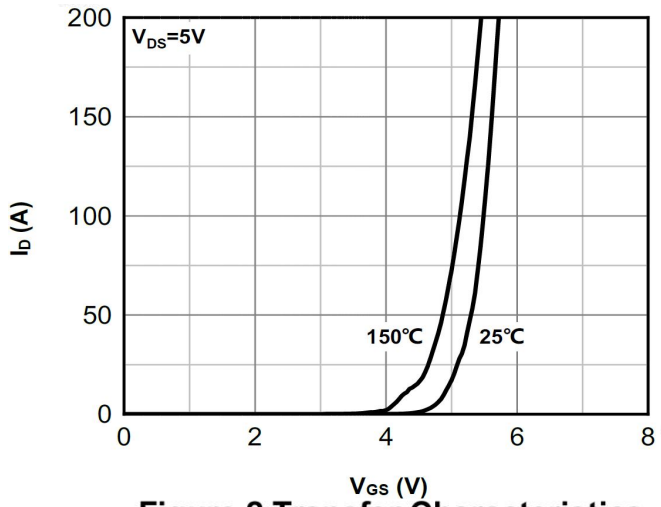


Figure 2 Transfer Characteristics

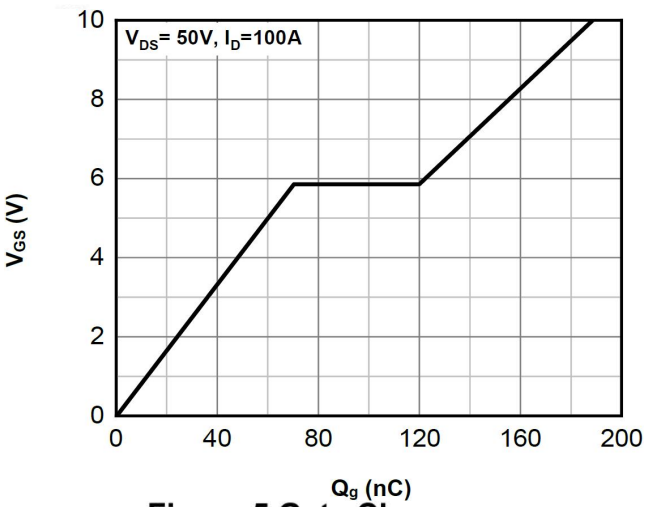


Figure 5 Gate Charge

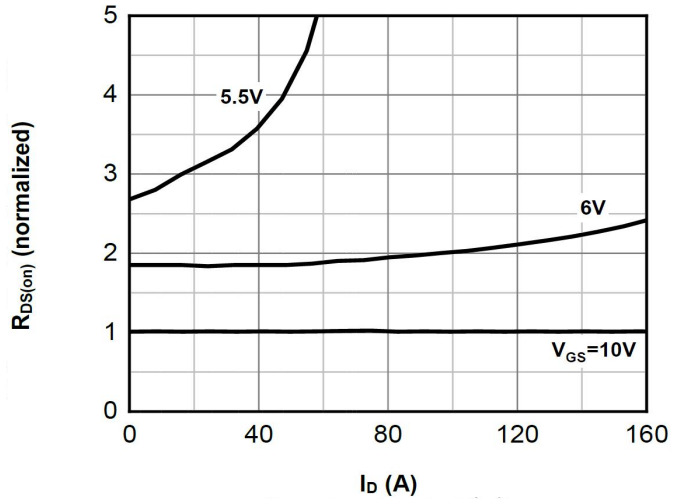


Figure 3 Rdson- Drain Current

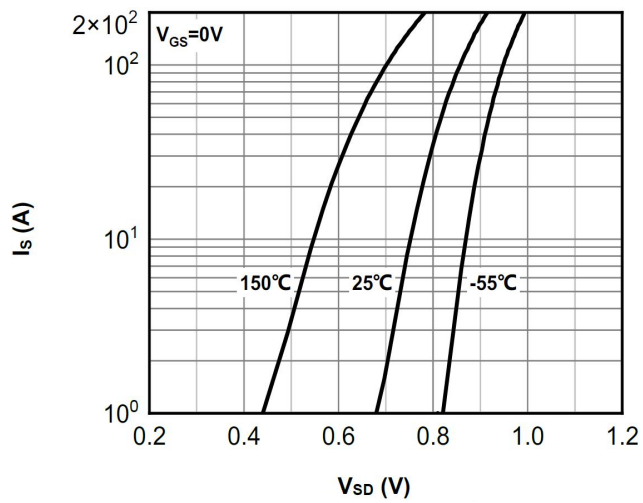


Figure 6 Source- Drain Diode Forward

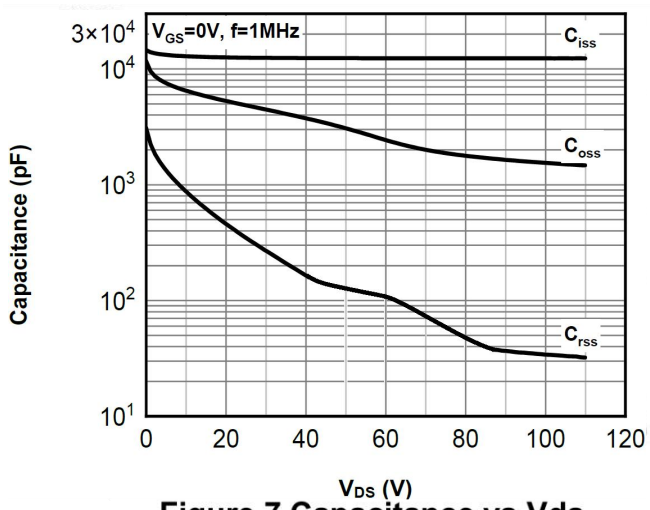


Figure 7 Capacitance vs Vds

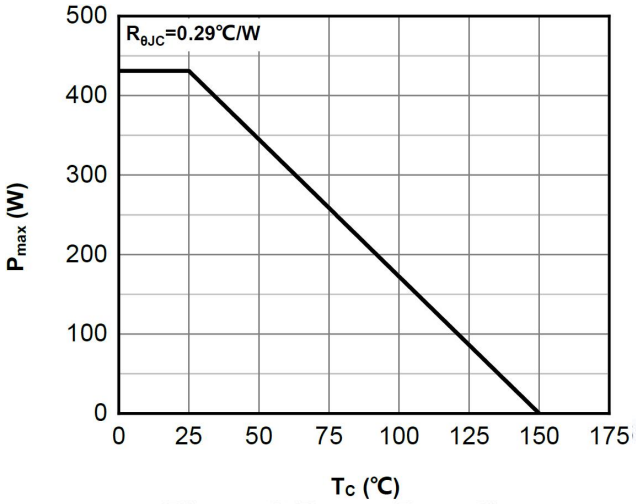


Figure 9 Power De-rating

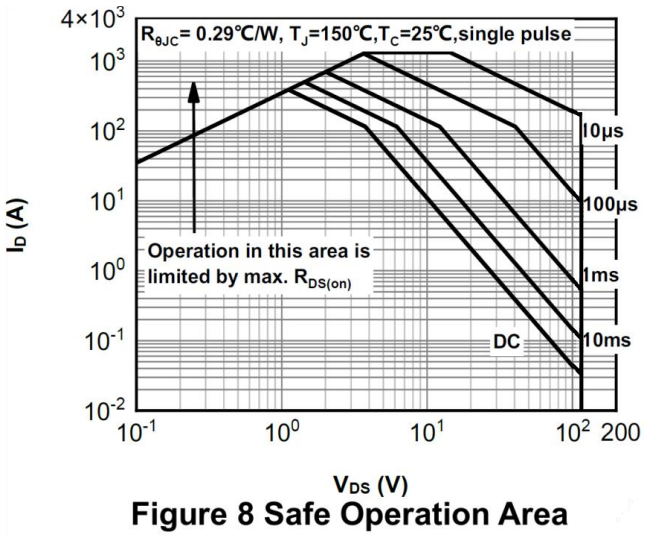


Figure 8 Safe Operation Area

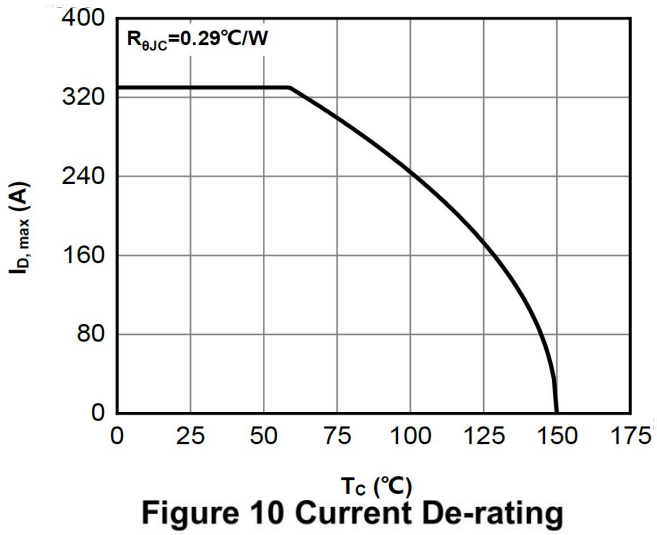


Figure 10 Current De-rating

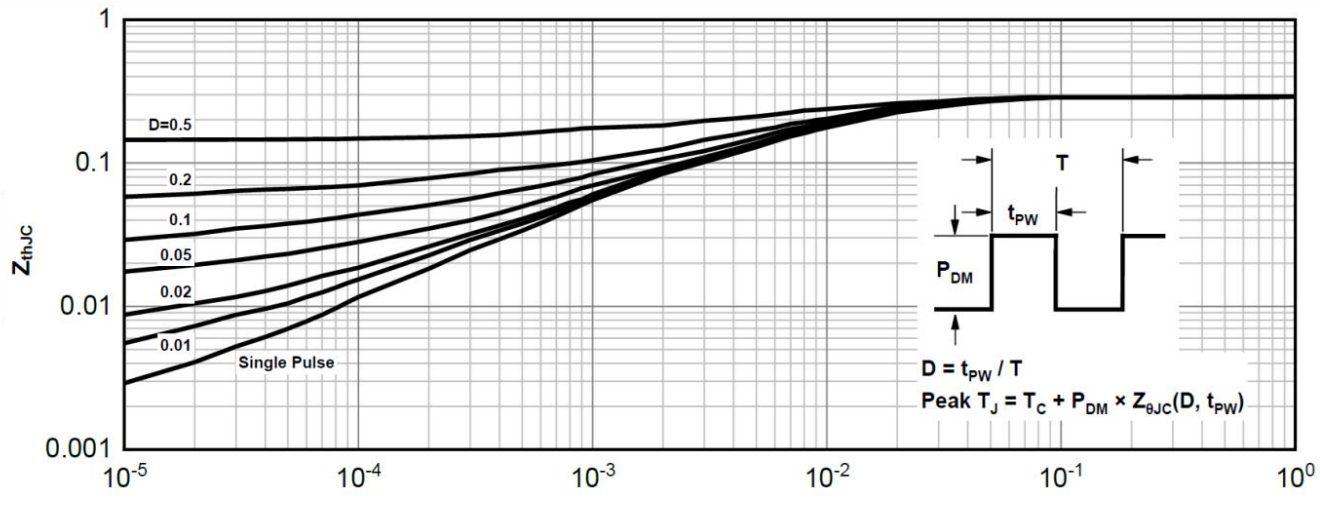
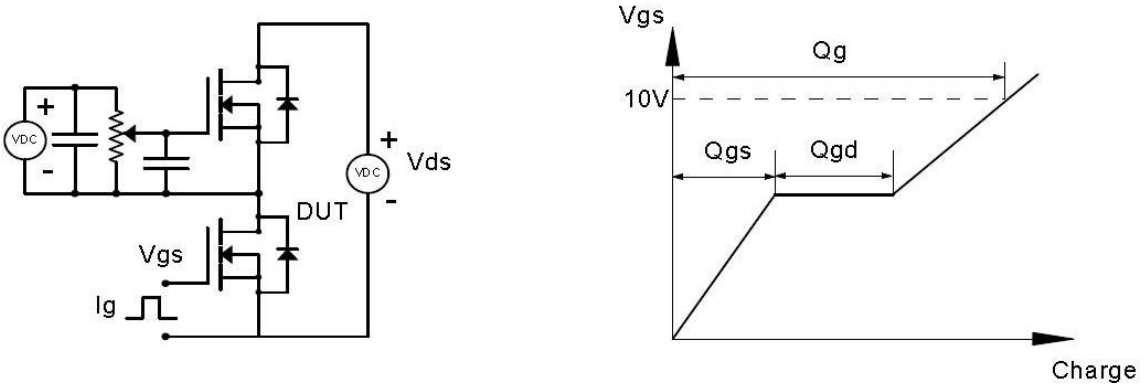


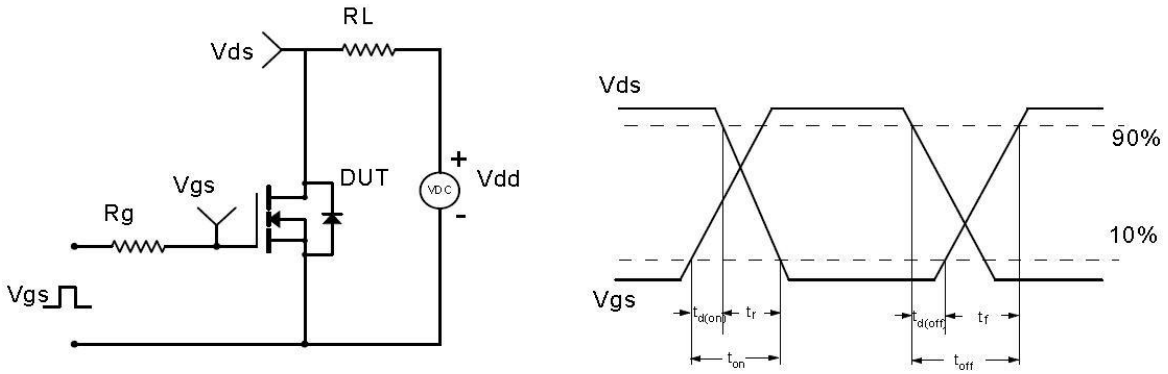
Figure 11 Maximum Transient Thermal Impedance

Test Circuit & Waveform

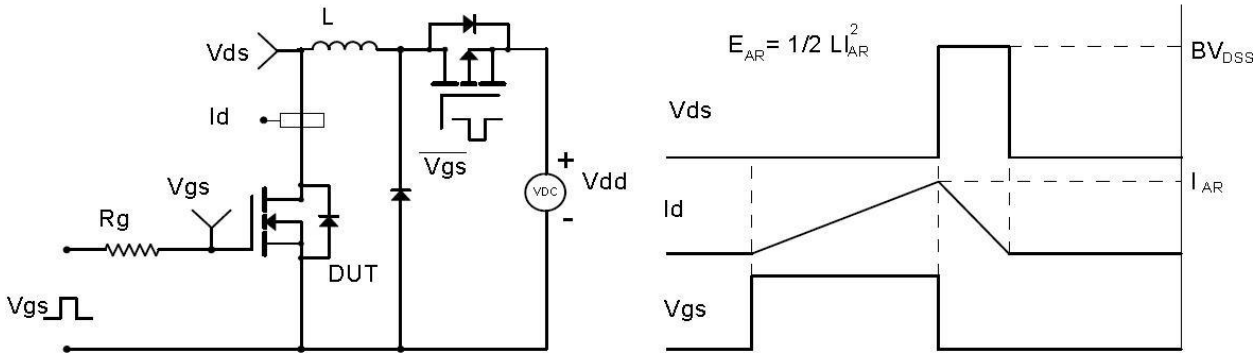
Gate Charge Test Circuit & Waveform



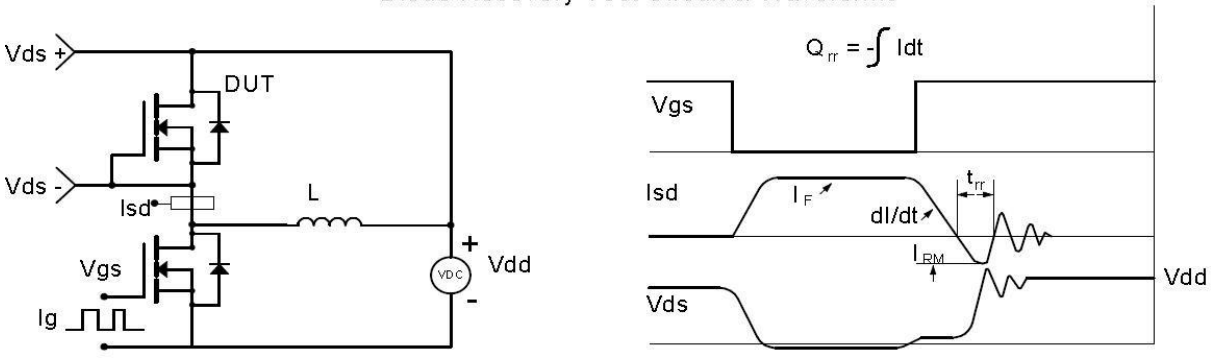
Resistive Switching Test Circuit & Waveforms



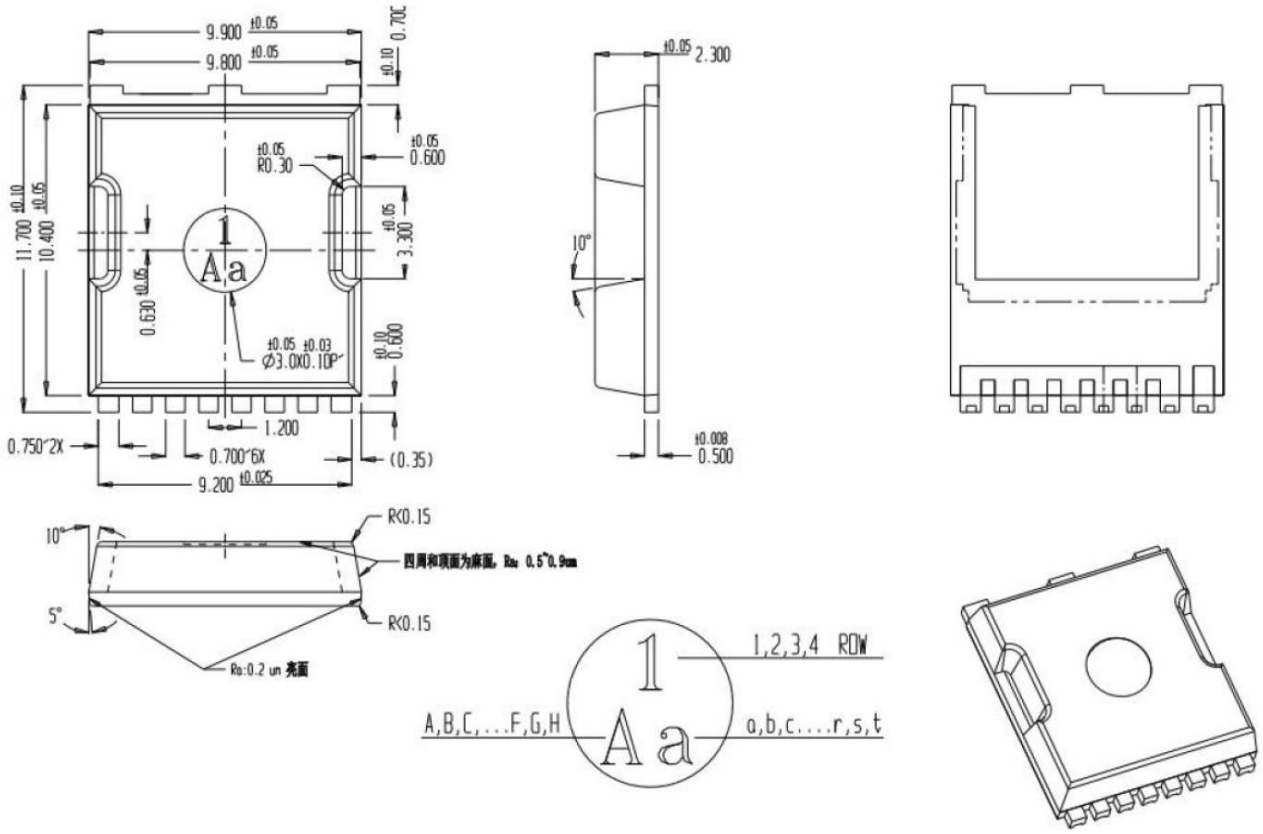
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Package Outline:TOLL



Revision History：

Revision	Date	Major changes
1.0	2025/9/15	Release of Preliminary version.
2.0	2025/11/21	Update BV.